NET's

NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

~~GUEST LECTURE/SEMINR/ADD-ON COURSE~~/ *Faculty Development Programme*/~~WORKSHOP /CONFERENCE/SHORT TERM TRAINING PROGRAMME/ANY OTHER PROPOSAL~~

Date: 15.04.2016

1	**ACADEMIC YEAR**	2015-16
2	**TYPE OF THE PROGRAMME PROPOSED**	*Faculty Development Programme*
3	**TITLE OF THE PROGRAMME:**	**"MATLAB and Its ApplicationsTo Engineering Problems"**
4	**BROAD OBJECTIVE OF THE PROGRAMME:**	At various stages in engineering study, one comes across mathematical problems of engineering applications irrespective of the engineering discipline. A simple computational tool such as MATLAB can be quite handy to solve such mathematical problems, provided we can formulate the problems appropriately. This FDP will include a session on the introduction of MATLAB by writing very simple programs for performing basic computations such as matrix operations. Also, couple of sessions will be conducted for the use of MATLAB to demonstrate its applications to specific topics such as Power Electronics and Drives, Signal & Image Processing, control systems.
5	**NAME OF THE INSTITUTION**	NETs NAVODAYA INSTITUTE OF TECHNOLOGY
6	**NAME OF THE DEPARTMENT(S)**	ELECTRICAL AND ELECTRONICS ENGINEERING
7	**BENEFICIARY**	Faculty members, Post Graduates students, Research Scholars from Electrical Electronics and Engineering.
8	**PROPOSED DATE OF THE PROGRAMME**	29.04.2016 to 01.05.2016
9	**ELIGIBILITY CRITERIA SET:**	Faculty members from various disciplines.
10	**NUMBER OF DAYS REQUIRED TO ORGANISE THE PROGRAMME**	Three Day FDP
11	**PROPOSED DURATION OF THE PROGRAMME**	6 hours

12	**TOTAL NO. OF PERIODS REQUIRED FOR THE PROGRAMME**	18 Periods
13	**NO. OF PERIODS REQUIRED PER DAY**	06 Periods
14	**LOCATION DETAILS:**	NETs NIT Auditorium
15	**MINIMUM NO OF PARTICIPANTS:**	30
16	**DETAILS OF SPONSORERS:**	-
17	**DETAILS OF RESOURCE PERSONS:**	**1. Dr.C.Gowrishankar,** Professor, Department of EEE, K.S.R College of Engineering, Thiruchengode, Nammakkal. **2. Dr. M.Ramasamy,** Professor, Department of EEE, K.S.R College of Engineering, Thiruchengode, Nammakkal.
18	**REGISTRATION FEE WITH CERTIFICATION:**	Rs 200 per participant
19	**REGISTRATION FEE WITHOUT CERTIFICATION:**	Rs 100 per participant.
20	**FACULTY COORDINATOR:**	1.Mr.Mazumdar B.K 2. Mr. Daniel N
21	**EVENT ORGANISERS (STUDENTS):**	1.Mr.Aatif Niyaz 2.Ms.Saniya Tanveer
22	**PROGRAMME PROPOSED BY:**	Dept. of EEE
23	**ANY OTHER RELEVANT MATTER:**	Nil
24	**ESTIMATED BUDGET IN RS:**	Rs.35000

This is to the Honorable Chairman of NIT for kind approval:

PRINCIPAL

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

HOD

Head of the Department
Department of Electrical and Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103, Karnataka

EVENT COORDINATOR

15-04-2016

APPROVED/NOT APPROVED

ABOUT RAICHUR:



Raichur, popularly known as 'Cotton City' is bounded by Krishna river on the North and Tungabhadra river on the south. A world famous Hatti Gold Mines is 75 kms away. RTPS, contributing major portion of state's power requirement is situated at Shakti Nagar about 15 kms from the city. Famous pilgrimage centres Mantralayam and Panchamukhi are within a radius of about 35kms.

HOW TO REACH RAICHUR:

Raichur is well connected by Road and Railway to all nearby and important cities like Bangaluru, Hyderabad, Chennai, Mumbai, Tirupathi, Kalaburgi, Hubballi, Ballary and so on.

ABOUT THE INSTITUTION

Navodaya Institute of Technology, Raichur, Karnataka is a self-financing co-educational institution established in the year 2008 with all the necessary infrastructural facilities provided through the vision of Navodaya Education Trust (NET®) that ably governs the institution. The college is approved by AICTE, New Delhi and is affiliated to Visvesvaraya Technological University (Belagavi). The institution has completed Eight years of dedicated and excellent service in thefield of technicaleducation.

The guiding spirit and the driving force for all the progress and achievements of the Institution is our Honorable chairman of Navodaya Education Trust (NET®), Sri S. R. Reddy.

NIT focuses on promoting quality technical education in north Karnataka region and Hyderabad-Karnataka region in particular. This institution offers six UG courses in MECH., CIVIL, CSE, ECE, EEE, IT and Two PG courses in M.Tech. - Thermal Power Engineering and Digital Communication & Networking programmes.

The Vision of this college aims at developing NIT the most respected institution of higher learning renowned for its excellence in Science and Technology and for its commitment to produce high quality engineers who are versatile, innovative and globally competitive.

ABOUT THE DEPARTMENT

The Department was started in the year 2008 with an intake of 60 students with the vision to provide the quality technical education. The department of EEE has well equipped laboratories, one well established computer center with internet connectivity, excellent library, experienced & highly qualified faculty members to impart knowledge in the areas of Electrical Machines, Power Systems, Power Electronics and Drives, Controls Systems, High VoltageEngineering.

ABOUT THE FDP

At various stages in engineering study, one comes across mathematical problems of engineering applications irrespective of the engineering discipline. A simple computational tool such as MATLAB can be quite handy to solve such mathematical problems, provided we can formulate the problems appropriately. This FDP will include a session on the introduction of MATLAB by writing very simple programs for performing basic computations such as matrix operations. Also, couple of sessions will be conducted for the use of MATLAB to demonstrate its applications to specific topics such as Power Electronics and Drives, Signal & Image Processing, control systems.

NAVODAYA
Institute of Technology
RAICHUR



Faculty Development Programme On "MATLAB and Its Applications to Engineering Problems"

29th April -1st May 2016

Convener
Dr.M.Srinivasan,
Professor andHead/EEE

Coordinators
Prof.Mazumdar.B.K.
Prof.Daniel.N

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

ORGANIZED BY
Department of Electrical & Electronics Engineering
Navodaya Education Trust®
NAVODAYA INSTITUTE OF TECHNOLOGY
Navodaya Nagar, Bijiangera Road, Raichur-584103,Karnataka,India.

Website: www.navodaya.edu.in

Registration Form
(Photocopy may be used if required)

Name (in BLOCK letters):

Academic Qualification:

Experience:

Designation &

Department:

CollegeAddress:

Name ofAffiliatedUniversity:

E mail :

ContactNumber:

DD Number/Date

Date:
Place: Signature of theApplicant

Mr./Ms./Dr.
is an employee (faculty)/PG student of our institute. He / She will be permitted to attend the programme if selected.

Date:
Place:

Signature & Seal
HOD/Principal/Head of the Organization

Chief Patron
Sri. S. R. Reddy, Hon'ble Chairman,
Navodaya Education Trust (NET®), Raichur.

Patron
Sri. N. Bharath Reddy,, Trustee,
Navodaya Education Trust (NET®), Raichur.

Chief Convener
Dr.Shivaprakash C. K., Dean & Principal, NIT, Raichur

Convener
Dr. M.Srinivasan, HOD-EEE, NIT,Raichur.

Address for communication:
The Principal
Navodaya Institute of Technology (NIT), Navodaya Nagar, Bijiangera Road, RAICHUR- 584 103.

Coordinator
Prof.Mazumdar.B.K
Dept of EEE, NIT
E-mail :mazumdarbk@gmail.com
Contact no. : +91 - 9845229996

Co-Coordinator
Prof.Daniel.N
Dept of EEE, NIT
E-mail :danielnixon25@gmail.com
Contact no. : +91 - 9986247085

Organizing Committee
1. Prof.Vijayakumar.B
2. Prof.Geeta.K.M
3. Prof.Satishkumar.K.S
4. Prof.Velu.A
5. Prof.Sudhakar.K.
6. Prof.Kala.M

NAVODAYA INSTITUTE OF TECHNOLOGY * RAICHUR

COURSE CONTENT

Day 01
- Introduction toMATLAB.
- Applications in Signal and ImageProcessing

Day02
- Applications in Power Electronics andDrives

Day03
- Mini Project in Signal & ImageProcessing
- Mini Project in Power Electronics andDrives

Resource Persons:

1. Dr.C.Gowrishankar,
   Professor,
   Department ofEEE,
   K.S.R College of Engineering, Thiruchengode, Nammakkal.

2. Dr. M.Ramasamy,
   Professor,
   Department ofEEE,
   K.S.R College of Engineering, Thiruchengode, Nammakkal.

Registration fees :

Rs. 500/- per participant for faculty members and students and Rs. 1000/- for others. Lunch will be arranged during the programme. Accommodation will be provided in the campus hostels to the out station participants on priceintimation.

Important dates
FDPdate : 29th April to 01st May,2016
Last datefor Registration : 27th April 2016

Registration fee may be paid in the form of DD in favour of **Principal, Navodaya Institute of Technology**, payable at **SBI, NET campus, Raichur.**

VENUE: NIT Auditorium, NIT, Raichur

NAVODAYA
Institute of Technology
RAICHUR
Affiliated to Visvesvaraya Technological University (VTU), Belgaum
Approved by AICTE, New Delhi and Recognized by the Govt. of Karnataka

Navodaya Nagar, Bijanagera Road
RAICHUR - 584 103, Karnataka, INDIA
Ph: +91-8532-240334/ 240115
Fax: 240054/ 240061/ 223070
E-mail: nit.princ@gmail.com
web: www.navodaya.edu.in

Ref: NIT'R / EEE / FDP /218/ 2015-2016 Date: 21.04.2016

To

The Principal,
KSR College of Engineering,
K.S.R. Kalvi Nagar
Thiruchengode - 637215
Namakkal Dt. Tamilnadu.

Sir,

Sub: Requisition to depute a Faculty to guest speaker in FDP-reg.

The Department of Electrical and Electronics Engineering of our Institution is organizing a faculty development programme "MATLAB and Its Applications To Engineering Problems" during 29rh April - 01st May 2016. (3days) at our college premises. Kindly depute **Dr.C.Gowrishankar, Associate Professor, Department of EEE,** to deliver a lecture on the topic Matlab applications to signal and Image on 29th & 30th April 2016.

Yours sincerely,

21.04.16

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

NAVODAYA
Institute of Technology
RAICHUR
(Affiliated to Visvesvaraya Technological University (VTU), Belgaum
Approved by AICTE, New Delhi and Recognized by the Govt. of Karnataka)

Navodaya Nagar, Bijanagera Road
RAICHUR - 584 103, Karnataka, INDIA
Ph: +91-8532-240334/ 240115
Fax: 240054/ 240061/ 223070
E-mail: nit.princi@gmail.com
web: www.navodaya.edu.in

Ref: NIT'R / EEE / FDP/ 57 / 2015-2016 Date: 21.04.2016

To

The Principal,
KSR College of Engineering,
K.S.R. Kalvi Nagar
Thiruchengode - 637215
Namakkal Dt. Tamilnadu.

Sir,

Sub: Requisition to depute a faculty to guest speaker in FDP-reg.

The Department of Electrical and Electronics Engineering of our Institution is organizing a faculty development programme "MATLAB and Its Applications To Engineering Problems" during 29rh April - 01st May 2016. (3days) at our college premises. Kindly depute **Dr.M.Ramasamy, Associate Professor, Department of EEE**, to deliver a lecture on the topic Matlab applications to power electronics and drives on 29th & 30th April 2016.

Yours sincerely,

21.04.16.

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

NET's
NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

Tel : +91 8532-200115, 240334, Fax : +91-8532-240054
E-Mail :hodece.nit@navodaya.edu.in

CIRCULAR

Chief Patron
Shri. S. R. Reddy, Hon'ble Chairman,
Navodaya Education Trust (NET®),
Raichur.

Patron
Shri. N. Bharath Reddy., Hon'ble Trustee.
Navodaya Education Trust (NET®),
Raichur.

Chief Convener
Dr.ShivaPrakash C K,
Principal and Dean, NIT, Raichur

Convener
Dr.M.Srinivasan,
HOD/ EEE, NIT. Raichur

Coordinator
Prof.Mazumdar B.K,EEE, NIT
E-Mail: mazumdarbk@gmail.com
Contact no. : +91 - 9845229996

Co-Coordinator
Prof.Daniel N,EEE, NIT
E-Mail: Daniel.nixon@navodaya.edu.in
Contact no. : +91 - 9986247085

Organizing Committee
1. Prof.Vijay Kumar B
2. Prof.Geeta K.M
3. Prof.Sathishkumar K.S
4. Prof.Velu.A
5. Prof.Sudhakar.K.
6. Prof.Kala.M

26.04.2016

Respected Sir / Madam,
Greetings from Navodaya Institute of Technology, Raichur…

The department of Electrical & Electronics Engineering is organizing a Three day FDP on **"MATLAB and Its Applications To Engineering Problems"** The attached brochure may kindly be circulated among the students and faculty members / colleagues.

At various stages in engineering study, one comes across mathematical problems of engineering applications irrespective of the engineering discipline. A simple computational tool such as MATLAB can be quite handy to solve such mathematical problems, provided we can formulate the problems appropriately. This FDP will include a session on the introduction of MATLAB by writing very simple programs for performing basic computations such as matrix operations. Also, couple of sessions will be conducted for the use of MATLAB to demonstrate its applications to specific topics such as Power Electronics and Drives, Signal & Image Processing, control systems.

We are pleased to inform that the department of Electrical and Electronics Engineering, Navodaya Institute of Technology is organizing a Faculty Development Programme during 29th April-01st May 2016.

In this context, we look forward your support and request you to encourage participation of professionals by circulating the attached brochure.

Thanking you,
Yours sincerely,

Dr.M.Srinivasan
Convener

NAVODAYA INSTITUTE OF TECHNOLOGY * RAICHUR

NET's
NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Ph. No.:08532-220330, Extn-703,702,713, 07353295425
e-mail:hodeee.nit@navodaya.edu.in

Chief Patron
Shri. S. R. Reddy, Hon'ble Chairman,
Navodaya Education Trust (NET®),
Raichur.

Patron
Shri. N. Bharath Reddy., Hon'ble Trustee.
Navodaya Education Trust (NET®),
Raichur.

Chief Convener
Dr.Shivaprakash C. K.,
Principal and Dean, NIT, Raichur

Convener
Dr.M.Srinivasan,
HOD/ EEE, NIT.Raichur

Coordinator
Prof.Mazumdar.B.K.Dep
t of EEE,NIT

Co-Coordinator
Prof.Daniel.NDep
t of EEE,NIT

Organizing Committee
1. Prof.Vijayakumar.B
2. Prof.Geeta.K.M
3. Prof.Satishkumar.K.S
4. Prof.Velu.A
5. Prof.Sudhakar.K.
6. Prof.Kala.M

20.04.2016

Faculty Development Programme
On
"MATLAB and Its Applications To Engineering Problems"

To

The Principal,

Warm Greeting!!!

Kind attention: Head of the Department / EEE/ECE/EIE and other disciplines.

Dear Sir/Madam,

Sub: Invitation for attending faculty development programme on "MATLAB and Its Applications to Engineering Problems"- reg.

We are pleased to inform that the Department of Electrical and Electronics Engineering, Navodaya Institute of Technology is organizing a Faculty Development Programme on "MATLAB and Its Applications To Engineering Problems" during 29th April - 01st May 2016, (3days) at our college premises. This programme will equip the faculty to teach the subject in better way and also enable them to offer final year project in this specialization. We invite your faculty members to participate in this FDP from your esteemed institution.

Please see the brochure enclosed with this for your perusal and circulation among the faculty members of EEE/ECE/EIE and other department.

For further details please feel free to contact.

NAVODAYA INSTITUTE OF TECHNOLOGY • RAICHUR

Thanks & Regards

Dr.M.Srinivasan

NET's

NAVODAYA INSTITUTE OF TECHNOLOGY

BIJENGERA ROAD, RAICHUR - 584103

INVITATION

Department of Electrical and Electronics Engineering

HODs & Faculties are invited to

attend three days

FACULTY DEVELOPMENT PROGRAMME

on

"MATLAB and Its Applications To Engineering Problems"



From 29th *April* To 1st *May* 2016

Venue: NIT, Auditorium





Inaugural function Agenda

1. *Introduction*
2. *Invocation song*
3. *Welcome address by Dr.M.Srinivasan*
5. *Introduction of Resource person*
6. *Chief Guest Address*
7. *Presidential address*
8. *FDP - Session - I*

Valedictory Function Agenda

1. *Introduction*
2. *Feedback - Students and Faculty participants*
3. *Feedback by HOD*
5. *Feedback by Coordinator*
6. *Feedback by Resource person/Chief Guest*
7. *Certificate distribution*
8. *Presidential address*
9. *Vote of Thanks*



Dr.C.GOWRI SHANKAR

29/12,Tharakkadu,Veerapampalayam,
Thindal Post, Erode-638012.
Phone: 99428 88773
Email :cgsshankarme@gmail.com



EDUCATION

- **Ph.D** in the area of **Medical Image Processing** from Anna University, Chennai in May 2013.

  Title of Ph.D. Thesis:

  "A Multidisciplinary approach to the Development of a CAD System for the Detection and Classification of Abnormalities in Medical Images"

- **M.E. Applied Electronics** from Bannari Amman Institute of Technology under Anna University, Chennai in May 2005 with **73%** of marks.
- **B.E. Electrical & Electronics Engg** from Annai Mathammal Sheela Engg College under Periyar University in May 2003 with **81.7%** of marks.
- **HSC** from AET Matriculation Higher Secondary School in April 1999 with **87.7%** of marks.
- **SSLC** from Sree Amman Matriculation Higher Secondary School in April 1997 with **72.8%** of marks.

EXPERIENCES

- Currently working as an **Associate Professor** from 01-12-2010 to till date in Department of Electrical and Electronics Engineering, K.S.R. College of Engineering, Tiruchengode. Handled courses for PG (Applied & Power Electronics) and UG students (EEE). **(5 Years 3 Months)**
- Worked as an **Assistant Professor** from 23-6-2005 to 30-11-2010 in Department of Electrical and Electronics Engineering, Velalar College of Engineering and Technology, Erode. Handled courses for PG (Embedded System Technologies) and UG students (EEE & ECE). **(5 Years 5 Months)**

Total teaching Experience: **10 Years 8 Months**

Programs Organized

- Organized a Seven days Faculty Development Training Programme on **"Power System Operation and Control"** during 23rd to 30th June 2014 and delivered a talk on **"Algorithms to design LFC Controllers"**
- Organized a Two day workshop on **"Algorithms in Image processing"** on 11th & 12th September 2009 and delivered a talk on **"Denoising and Enhancement of Images"**
- Organized a One day workshop on **"Algorithms in Image processing"** on 6th September 2008 and presented a talk on **"Medical Image Analysis".**

Funding Proposals

- Submitted a proposal titled **"Role of Imaging Systems in Radio Diagnosis"** for seminar grant under Seminar/ Symposia Scheme to **Department of Science and Technology (DST), New Delhi.**
- Submitted a proposal titled **"Computer-Aided Diagnosis of Breast Cancer: Towards the Detection and Analysis of Early and Subtle Signs"** for seminar grant to **Indian Council of Medical Research (ICMR), New Delhi.**

Guest Lectures Delivered

- Delivered a Lecture on the topic **"Design of Digital Filter"** on 22.12.2014 in Anna University approved Faculty Development Training Programme on **"Discrete Time systems and Signal Processing"** organized by **Vivekanandha Institute of Engineering and Technology for Women**, Tiruchengode.
- Delivered a lecture on the topic **"Information content security and Unsupervised pattern classification"** on 19th September 2013 in AICTE sponsored three day seminar named "Watermark Technique in Multimedia for Authentication and Copyrights Production" organized by **P.A. College of Engineering and Technology**, Pollachi.
- Delivered a Lecture on the topic **"Role of Imaging Systems in Radio Diagnosis"** on 19th January 2013 in CSIR Sponsored National Level Seminar titled "Engineer's Role in Designing Medical Devices" organized by **Kongu Engineering College**, Erode
- Delivered a Guest Lecture on the topic **"An Insight into Signal & Image Processing"** on 13.08.2012 organized by School of Engineering and Technology, **Jay Shriram Group of Institutions**, Tirupur.
- Delivered a lecture on the topic **"Medical Image Processing"** on 25th June 2009 in AICTE sponsored two weeks Faculty Development Program titled "Current and Future trends in Medical Image Processing" organized by **KSR College of Technology**, Tiruchengode from 15th June to 27th June 2009.

- **Gowri Shankar, C.** and Shanmugam, A. "A Novel Technique for MR Image Contrast Enhancement", International Journal on Electronic and Electrical Engineering, Vol. 1, No. 1, pp 46-53, 2009.

- **Gowri Shankar, C.,** Balakumaran, T. and Ila Vennila, "Microcalcification Detection using Multiresolution Analysis and Neural Network" in International Journal of Recent Trends in Engineering, Vol 2, No. 2, November 2009

Conference Publications

- **Gowri Shankar, C.** and Sanjula, S. " Recognizing surgically altered face images using Extreme learning machine", International Conference on Advances in Engineering, Technology and Sciences held at Hotel Clouds Valley Leisure, Munar Kerala, India, pp. 124-129, 2014.

- **Gowri Shankar, C.** and Abdul Kadar, A. " A novel method for automatic detection and classification of abnormalities in mammogram", International Conference on Advances in Engineering, Technology and Sciences held at Hotel Clouds Valley Leisure, Munar Kerala, India, pp. 120-123, 2014.

- **Gowri Shankar, C.** and Shanmugam, A. "Neural network based compatible abnormality detection scheme for CT and MRI brain images", International convention cum pre-conference workshop on Innovations in Engineering and Technology for Sustainable Development, organized by Bannari Amman Institute of Technology, Sathyamangalam, India and University of Derby on 3rd to 5th September 2012.

- **Gowri Shankar, C.** and Shanmugam, A. "A Multiresolution Approach for Enhancement and Denoising of Mammogram", International conference on Mathematical Modeling and Applied Computing, organized by Coimbatore Institute of Technology, Coimbatore, India on 11th to 13th July 2012.

- **Gowri Shankar, C.** and Sanjula, S. "A novel approach for recognizing facial structures using Extreme learning machine", in National conference on Trends in Automotive Parts and Systems and Applications organized by The Society of Automotive Engineers, India and Sri Krishna college of Engineering and Technology, Coimbatore on 21st and 22nd March 2014.

- **Gowri Shankar, C.** and Shanmugam, A. "Polynomial Approach to Magnetic Resonance Image Contrast Enhancement", International conference on Computing, Communication and Networking held at Chettinad College of Engineering and Technology, Karur on 18th to 20th, December 2008

- **Gowri Shankar, C.,** Nandhini, J. and Balakumaran, T. "Development of CAD Scheme for the Analysis of Digital Mammogram" in National Conference on Innovations in Instrumentation and Intelligent Technologies organized by Erode Sengunthar Engineering College, June 2010.



Dr.M.RAMASAMY
Associate Professor
Electrical and Electronics Engineering
K.S.R. College of Engineering
Tiruchengode – 637 215, TN, India
ramasamyksrce@gmail.com
+919715614317

Experience 9 Years, 02 Months

- Associate Professor – Electrical and Electronics Engineering – K.S.R. College of Engineering, Thiruchengode (01.07.2010 to till Date).
- Lecturer – Electrical and Electronics Engineering – K.S.R. College of Engineering, Thiruchengode (03.12.2008 to 30.06.2010).
- Lecturer – Sengunthar Engineering College, Thiruchengode (08.01.2007 to 28.11.2008).

Academic

- **Ph.D.:** Hybrid Wind-Photovoltaic System Based Dynamic Voltage Restorer with Significant Energy Conservation, 2013, Anna University Chennai, Tamil Nadu, India.
- **M.E.:**Power Electronics and Drives, 2007, K.S.Rangasamy College of Technology, Anna University Chennai, Tamil Nadu, India.
- **B.E.:** Electrical and Electronics Engineering, 2005, Mahendra Engineering College, Thiruchengode, Anna University Chennai, Tamil Nadu, India.
- **Diploma:** Electrical and Electronics Engineering, 2002, Aarthi Polytechnic, Thiruchengode, DOTE, Tamil Nadu, India.

Research Area

Custom Power Devices and Filters:

Static VAR Compensator, Static Compensator (STATCOM), Distribution Static Compensator (DSTATCOM), Dynamic Voltage Restorer (DVR), Unified Power Quality Conditioner (UPQC) and Active Filters: Shunt and Series.

Photovoltaic Array and DC-DC Converters:

Modeling of Photovoltaic Arrays, Maximum Power Point Tracking (MPPT) Algorithms, Converters: Buck, Boost, Buck-Boost and Cuk.

Projects

P.G.:Intelligent Controllers for Controlling the Speed of an Induction Motor Drive
U.G. : Micro controller based Static VAR compensator
Diploma : Micro Controller based Under Voltage, Phase Loss and Frequency Monitor

9. Vijayakumar, M. and Ramasamy, M A Novel Reference Current Generation Method of PV based Three Level NPC Shunt Active Power Filter", U.P.C. Scientific Bulletin, Vol. 77, No. 3, pp. 235-250, 2015.
10. Ramasamy, M. and Thangavel, S. "Photovoltaic based Dynamic Voltage Restorer with Outage Handling Capability using PI controller", Energy Procedia (Elsevier), No. 12, pp. 560-569, 2011. DOI: 10.1016/j.egypro.2011.10.076
11. Ramasamy, M. and Thangavel, S. "Photovoltaic based Dynamic Voltage Restorer with Energy Conservation Capability using Fuzzy Logic Controller", IEEE Conference Publications, pp. 485-492, 2012. DOI: 10.1109/INCOSET.2012.6513953
12. Nithya, K. and Ramasamy, M. "Multi-input DC-DC converter for Renewable Energy Sources", International Journal of Research in Engineering and Technology, Volume 3, Issue 7, pp. 714-719, 2014.
13. Ramasamy, M. and Thangavel, S. "Novel High Step-Up DC-DC Converter with Coupled-Inductor Based Dynamic Voltage Restorer", International Journal of Power Systems and Energy Management, Vol.1, No.1, pp.17-22, 2012.
14. Sharmila, S. and Ramasamy, M. "A Combined intercept and star line tracking algorithms for trabecular bone fracture analysis", International Journal of Novel Research in Engineering & Pharmaceutical Science, pp. 32-39, 2015.
15. Piramanathan, P. and Ramasamy, M. "PV based Dynamic Voltage Restorer with Minimal Energy Control using Fuzzy", International Journal of Engineering Sciences and Research Technology, 2(26), pp. 1636-1641, June 2013.
16. Piramanathan, P. and Ramasamy, M. "A New Minimal Energy Injection Control Strategy for Dynamic Voltage Restorer using Fuzzy Logic Controller", International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering, Vol. 2, Issue 6, pp. 2669-2676, June 2013.
17. Nithya, K. and Ramasamy, M."Modeling of Multi-input DC-DC converter for Renewable Energy Sources", International Journal of Innovative Research in Science, Engineering and Technology, Volume 3, Special Issue 1, pp. 970-975, Feb 2014.
18. Vidhukrishnan, U. and Ramasamy, M. "An Enhancement Method for the compensation of voltage sag/swell and Harmonics by Dynamic Voltage Restorer", International Journal of Modern Engineering Research, Vol.2, Issue 2, pp. 475-478, March 2012.
19. Vidhukrishnan, U. and Ramasamy, M."An mitigation method for Total Harmonic Distortion and Power Quality Improvement with DVR using Neural Networks Techniques", International Journal of Advanced Research in Technology, Vol. 2, Issue 1, pp. 95-99, 2012.

International Conferences

1. Ramasamy, M. and Thangavel, S. "Photovoltaic based Dynamic Voltage Restorer with Energy Conservation Capability Using Fuzzy Logic Controller", International Conference on Smart Grid and Clean Energy Technology,UESTC, China energy Association, Chengdu, China, 27-30 September 2011.
2. Ramasamy, M. and Thangavel, S. "Photovoltaic based Dynamic Voltage Restorer with Outage Handling Capability using PI controller", International Conference on Emerging Trends in Science, Engineering and Technology, J.J. College of Engineering and Technology, Tirchy, 13th & 14th Dec 2012.

Review Committee/Editorial BoardMember ofJournals

1. IEEE Transactions on Industrial Electronics.
2. IEEE Transactions on Power Electronics.
3. IEEE Transactions on Sustainable Energy.
4. InternationalJournalofElectrical Power and Energy System,Elsevier.
5. Journal of Circuit Systems and Computers.
6. European Transactions on Electrical Power.
7. Electric Power Components
8. Journal of Testing and Evaluation

Member of Board of Studies & Expert Committee

- MemberofBoardofStudies,U.G. – EEE, Departmentof Electrical and Electronics Engineering,K.S.R.Collegeof Engineering, Tiruchengode – 637 215.

SubjectsTaught

U.G.:

Digital Signal Processing, Electronic Devices and Circuits, Electrical Machines – II, Bio Medical Instrumentation, Electrical Drives and Control, Electrical Energy Conservation and Utilization andBasic Electrical and Electronics Engineering.

P.G.:

Power Quality, Solid State AC Drives, Advanced Power Converters, Non-Conventional Energy Sources

SpecialTalks

1. Lectureon"Digital Signal Processing"conductedby Department of Electrical and Electronics Engineering, Tagore Institute of Engineering and Technology, Attur, 07thApril 2011.
2. Lecture on "PV based Custom Power Devices", in the AICTE Sponsored Faculty Development Programme, Conducted at K.S.R. College of Engineering, Tiruchengode, 15thto 28th May 2013.
3. Lecture on "Insight into Electronics", CMS College of Engineering, Namakkal, 19thFeb 2015.
4. Lecture on "Insight into Digital Signal Processing", Sengunthar Engineering College, 05thFeb 2014.
5. Lecture on "Power Electronics Applications in Power System", K.S.R. College of Engineering, Tiruchengode, 30thJune 2014.
6. Lecture on "Electrical Machines", Sengunthar Engineering College, 30thOct2014.
7. Lecture on "Modeling of Dynamic Voltage Restorer" in a Two days' Workshop conducted at K.S.Rangasamy College of Technology, 10th oct 2015.
8. Lecture on "Single Phase and Three Phase AC Voltage Controllers", Sengunthar Engineering College, 27thMay 2015.

18. Two Days National Workshop on "Modeling and Simulation of Power Electronic Circuits", Organized by CARE Group of Institutions, Tiruchirappalli, 08th and 09th May 2014.
19. One day workshop on "Algorithms in Image Processing", Velalar College of Engineering, Erode, 6th Sep 2008.
20. Two Days Training Programme on "Design and Manufacture of Electronics Products", TSK India Electronics, Tiruchengode, 22 & 23rd Aug 2015.
21. Two days' workshop on "Journal Paper Writing and Preparation of Winner Research Proposal", K.S.R. College of Engineering, Tiruchengode, 19 & 20th Aug 2015
22. One day workshop on "Blooms Taxonomy and Its Assessment", K.S.R. College of Engineering, Tiruchengode, 20th March 2015

Responsibilities

- College Level Stationery In-Charge.
- Class Advisor for U.G. Students.
- Institutional Level Patent Registration In-charge
- BoS Member for U.G. - Electrical and Electronics Engineering.
- Anna University Inspection Committee Member.
- Measurements and InstrumentationLaboratoryIn-Charge.

Question paper setting & Valuation Board Member

- Anna University, Chennai
- Bannari Amman Institute of Technology, Sathyamangalam
- AdhiyamanCollegeofEngineering,Hosur
- SonaCollegeofTechnology,Salem
- Kongu Engineering College, Perundurai

PersonalDetails

DateofBirth	: 11.05.1984	PassportNo.	: -
Citizenship	: Indian	MaritalStatus	: Married
Languages	: English&Tamil "	EMail	: ramasamyksrce@gmail.com
Address	: 2/119-C MariammanKovil St, Kozhikkalnatham – P.O, Tiruchengode –TK, Namakkal – DT. Pincode: 637302.	Mobile	: +919715614317

NET's

NAVODAYA INSTITUTE OF TECHNOLOGY, RAICHUR

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

SUMMARY OF FACULTY DEVELOPMENT PROGRAMME ON

"MATLAB AND ITS APPLICATIONS TO ENGINEERING PROBLEMS"

Department of EEE organized faculty development programme on "MATLAB and its Applications to Engineering Problems" was held at Navodaya Institute of Technology, seminar hallfrom29th April to 01st May 2016. The session was inaugurated with prayer song followed by the welcome speech by the Head of the department, Dr.M.Srinivasan. Dr.Sivaprakash C.K. -Principal presided over the function. Dr.C.Gowrishankar, and Dr.M.Ramasamy, Professors, Department of EEE, KSR College of Engineering, Tiruchengode, Tamilnadu were the resource persons.

First day Dr.C.Gowrishankar explained the simulation of signal processing and image processing applications briefly.

Second day Dr.M.Ramasamy explained the simulation of power electronics and drive applications briefly and third day resource persons demonstrated the real time applications.

Totally twenty five faculty members were participated and benefited.



Dr.Shivaprakash C.K., Dean& Principal ,NIT-Raichur giving presidential address.

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Head of the Department
Dep: Electrical and
F Engineering
Na te of Technology,
103. Karnataka

25.04.2016

From
Dr.M.Srinivasan,
Professor and Head,
Navodaya Institute of Technology,
Raichur-584103.

To
The Principal,
Navodaya Institute of Technology,
Raichur-584103

Sir

Subject: Req. for Auditorium hall to conduct FDP - Reg

We have proposed to conduct the FDP on the topic "**MATLAB and Its Applications to Engineering Problems**" on 29.04.2016 and 01.05.2016. For this Purpose, we need auditorium on 29.04.2016- Time-10.00AM-5.30PM.

Thanking You

Yours faithfully

[Dr. M.Srinivasan]

Head of the Department
Department of Electrical and Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103. Karnataka

From

Dr.M.Srinivasan,

HoD/EEE.,

NIT, Raichur

25/04/2016

To

The Dean cum Principal,

NIT, Raichur.

Sir,

Sub: Requisition for Accommodation and Food -Reg

I bring to your kind notice that we are conducting the Three days FDP on "MATLAB and its Applications to Engineering Problems" our department on 29th April 2016 to 01st May2016. For that we want to arrange the accommodation and food for the Resource Persons in the above days. So kindly do the needful.

Resource Person Details.

1. Dr.C.Gowrishankar,
   Professor,
   Department of EEE,
   K.S.R College of Engineering,
   Thiruchengode, Nammakkal.
   Tamil Nadu.

2. Dr.M.Ramasamy,
   Professor,
   Department of EEE,
   K.S.R College of Engineering,
   Thiruchengode, Nammakkal.
   Tamil Nadu.

Thanking you,

25/4/16

HOD/EEE

Head of the Department
Department of Electrical and Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103, Karnataka

NAVODAYA INSTITUTE OF TECHNOLOGY * RAICHUR

NAVODAYA
Institute of Technology
RAICHUR

Department of Electrical and Electronics Engineering

Faculty Development Programme (FDP) on

"MATLAB and its Applications to Engineering Problems"

From 29th April to 01st May 2016

S.No.	Name of the Faculty	Designation & Dept.	Address
1.	Mr.V.K.Jalihal	Assistant Professor/ECE	SLN College of Engineering, Raichur.
2.	Prof.VijayGomarsi	Asst. Professor/CE	NIT, Raichur
3.	Prof.Virupaxappa	Asst. Professor/CE	NIT, Raichur
4.	Prof.Ashish.P.Desai	Asst. Professor/CE	NIT, Raichur
5.	Prof.KrishtaiahShetty.K.	Asst. Professor/CE	NIT, Raichur
6.	Prof.Sangamesh.N.D.	Asst. Professor/Mech	NIT, Raichur
7.	Prof.AshokMalipatil	Asst. Professor/Mech	NIT, Raichur
8.	Prof.MohamedAdil	Asst. Professor/CE	NIT, Raichur
9.	Prof.B.Rajmohan	Asst. Professor/CE	NIT, Raichur
10.	Prof.Rajashekar	Asst. Professor/Mech	NIT, Raichur
11.	Prof.ImranBasha	Asst. Professor/Mech	NIT, Raichur
12.	Prof.Rajasekarappa	Asst. Professor/Mech	NIT, Raichur

INSTITUTE OF TECHNOLOGY RAICHUR

Head of the Department
Department of Electrical and Electronics Engineering
Navodaya Institute of Technology
RAICHUR-584 103. Karnataka

13.	Prof.Manjunath	Asst. Professor/Mech	NIT, Raichur
14.	Prof.Venkat Reddy	Asst. Professor/Mech	NIT, Raichur
15.	Prof.Shanthi	Asst. Professor/CSE	NIT, Raichur
16.	Dr.K.M.Palanisamy	Professor & Head/ECE	NIT, Raichur
17.	Prof. Pujari Sridhar	Associate Professor/ECE	NIT, Raichur
18.	Prof..Smitachetti	Asst. Professor/ECE	NIT, Raichur
19.	Prof.Laxmikanth Reddy	Asst. Professor/ECE	NIT, Raichur
20.	Prof.SachinKumar	Asst. Professor/ECE	NIT, Raichur
21.	Prof.Kalamuttumari	Asst. Professor/EEE	NIT, Raichur
22.	Prof.Geeta KM	Asst. Professor/EEE	NIT, Raichur
23.	Prof.Velu A	Asst. Professor/EEE	NIT, Raichur
24.	Prof.SudhakarKothuru	Asst. Professor/EEE	NIT, Raichur
25.	Prof.SathishKumar K.S	Asst. Professor/EEE	NIT, Raichur

INSTITUTE OF TECHNOLOGY RAICHUR

HOD & Convener

Head of the Department
Department of Electrical and Electronics Engineering
Navodaya Institute of Technology
RAICHUR-584 103, Karnataka

NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

FDP ATTENDED-INTERNAL

DEPARTMENT: EEE DATE: 01.05.2016

TITLE OF PROGRAMME: "MATLAB AND ITS APPLICATIONS TO ENGINEERING PROBLEMS"

RESOURCE PERSONS: Dr. C. Gowrishankar and Dr. N. Ramasamy, KSR COLLEGE OF

INSTITUTION/ORGANIZATION: Department of EEE, KSR COLLEGE OF ENGG. ENGG.

ADDRESS: Thiruchengode, Namakkal.

DATES: 29.4.2016 to 01.05.2016

SNO	NAME	DESIGNATION	SIGNATURE
1)	Geeta K.M	Asst. Prof	
2)	Velu.A	Asst. Professor	
3	SATHISH KUMAR. K.S	ASSISTANT PROFESSOR	
4.	E Chaina Veeraamma	Asst. professor	
5.	Md. Shamsheedh	Asst. Prof.	
6.	Sachin Kumar	Asst. Professor	
7.	Madhusri Debichoudhury	Asst professor	
8	Sana Anjum	Asst Professor	
9.	Swetha G.	Asst Prof	
10.	vijaykumar.B	Asst Prof.	Vijay.B.
11	R. Madhu Lakshmi	Asst. prof	
12.	Ashish P. Desai	Asst. prof	
13	Mohd. Adil	Asst Prof	Adil
14	Rajmohan	Asst Prof	Rajmohan
15	Imran Basha	ASST PROF	Imran.B
16	Rajasekarappa	Asst Prof	Rajasekarappa
17.	Ashok Malipatil	Asst. Prof	
18.	krishnaiah chetty.k	Asst prof	
19.	Sangameshwar.D	Assistant Professor	
20.	vijay Gomarsi	Asst. Prof.	Vijay G.
21.	Virupayapa	Asst Professor	
22	Kalamuthumani	AP	
23	Prof. Kiranakar Kottur.	AP - EEE	

HEAD OF THE DEPARTMENT

Head of the Department
Department of Electrical and
Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103. Karnataka

Faculty Development Programme Evaluation Form

We would appreciate if you could take a few minutes to share your opinions with us so we can serve you better.

Please return this form to the instructor or organizer at the end of the FDP. Thank you.

FDP title: **"MATLAB and its application to Engineering Problems"**

Date: 29th April to 1st May 2016

Handled by : Dr.C.Gowrishankar and Dr.M.Ramasamy

	Strongly agree				Strongly disagree
1. The content was as described in publicity materials	1	✓2	3	4	5
2. The FDP was applicable to my job	✓1	2	3	4	5
3. I will recommend this FDP to other conservators	✓1	2	3	4	5
4. The program was well paced within the allotted time	1	✓2	3	4	5
5. The instructor was a good communicator	✓1	2	3	4	5
6. The material was presented in an organized manner	1	✓2	3	4	5
7. The instructor was knowledgeable on the topic	✓1	2	3	4	5
8. I would be interested in attending a follow-up, more advanced FDP on this same subject	✓1	2	3	4	5

9. Given the topic, was this FDP: ☑ a. Too short ☐ b. Right length ☐ c. Too long

10. In your opinion, was this FDP: ☐ a. Introductory ☑ b. Intermediate ☐ c. Advanced

11. Please rate the following:

	Excellent	Very Good	Good	Fair	Poor
a. Visuals	☐	☐	☐	☐	☐
b. Acoustics	☑	☐	☐	☐	☐
c. Meeting space	☑	☐	☐	☐	☐
d. Handouts	☐	☐	☐	☐	☐
e. The program overall	☐	☑	☐	☐	☐

12. What did you most appreciate/enjoy/think was best about the course? Any suggestions for improvement?

Complete session was good, MATLAB simulation in Power Electronics circuit and Image processing was best in the Faculty development Program. Thanks to NIT management and organizers.

Thank you!

NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

FDP - FEEDBACK

DEPARTMENT: EEE DATE: 1/5/2016

SEMESTER :

Name of the speaker(s)	: Dr. C. GaoriShankar & Dr Ramasamy	
Designation	: Professors	
Institution/University/Organization	: KSR College of Engg	
Title of the Lecture	: MATLAB & IT APPLICATION TO ENGG PROBLEMS	
Date & Time	: 29 to 1st may 2016	
venue	: NIT, Auditorium	
Beneficiary	: Geeta.K.M	
Comments Speaker(s)	: It was good Program.	
Signature	:	

Arranged by : EEE

Date: 1/5/2016

HEAD OF THE DEPARTMENT

Head of the Department
Department of Electrical and Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103, Karnataka

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

FDP - FEEDBACK

DEPARTMENT: EEE DATE: 01-05-2016

SEMESTER :

Name of the speaker(s)	: Dr. C. Gowri Shankar & Dr. M. Ramasamy	
Designation	: PROFESSOR	
Institution/University/Organization	: KSR COLLEGE OF ENGG	
Title of the Lecture	: MATLAB & ITS APPLICATION TO ENGG PROBLEMS	
Date & Time	: 29-4-2016 TO 01-05-2016	
venue	: NIT, AUDITORIUM	
Beneficiary	: SATHISH KUMAR. KS	
Comments Speaker(s)	: The resource persons are good. FDP was interactive.	
Signature	:	

Arranged by : EEE

Date: 01-05-2016

HEAD OF THE DEPARTMENT

Head of the Department
Department of Electrical and Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103, Karnataka

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

FDP - FEEDBACK

DEPARTMENT: E.C.E. dept. DATE: 01/05/2016

SEMESTER: –

Name of the speaker(s)	:	Dr. C. Gowrishankar & Dr. M. Ramasamy
Designation	:	Professor.
Institution/University/Organization	:	KSR college of Engg.
Title of the Lecture	:	MATLAB & it's Application to Engg problems
Date & Time	:	29-04-2016 to 01-05-2016
venue	:	NIT. Auditorium
Beneficiary	:	Sathiskumar. Dept of ECE.
Comments Speaker(s)	:	- the FDP was useful and learnt the various applications of MATLAB.
Signature	:	

Arranged by : E.E.E.

Date: 01/05/2016.

HEAD OF THE DEPARTMENT

Head of the Department
Department of Electrical and
Electronics Engineering
Navodaya Institute of Technology
RAICHUR-584 103. Karnataka

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

FDP - FEEDBACK

DEPARTMENT: EEE DATE: 01/05/2016

SEMESTER :

Name of the speaker(s)	: Dr C. Hanmi Shanker & Dr. Ramanjaneyulu	
Designation	: Professor	
Institution/University/Organization	: KSR College of Engg.	
Title of the Lecture	: MATLAB & ITS Applications to Engg Problems	
Date & Time	: 29/04/2016 to 01/05/2016	
venue	: NIT- Auditorium	
Beneficiary	: Md. Shambulinga Madhi	
Comments Speaker(s)	: Excellent delivery of lectures. Many programs / projects discussed	
Signature		

Arranged by : EEE Dept

Date: 01/05/2016

HEAD OF THE DEPARTMENT

Head of the Department
Department of Electrical and
Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103. Karnataka

INSTITUTE OF TECHNOLOGY RAICHUR

NAVODAYA INSTITUTE OF TECHNOLOGY

Navodaya Nagar, Bijangera Road, Raichur-584103.

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

CERTIFICATE

This is to certify Mr./Ms./Dr. Channaveeramma.E of ECE, NIT, Raichur has attended the Faculty Development Programme on "**MATLAB and its Applications to Engineering Problems**" organized by the department of Electrical & Electronics Engineering, from 29th April to 1st May 2016.

Convener

Principal

NAVODAYA INSTITUTE OF TECHNOLOGY

Navodaya Nagar, Bijangera Road, Raichur-584103.

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

CERTIFICATE

This is to certify Mr./~~Ms./Dr.~~ Rammurthy .D of NIT, Raichur has attended the Faculty Development Programme on "MATLAB and its Applications to **Engineering Problems**" organized by the department of Electrical & Electronics Engineering from 29th April to 1st May 2016.

Convener

Principal

NAVODAYA INSTITUTE OF TECHNOLOGY

Navodaya Nagar, Bijangera Road, Raichur-584103.

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

CERTIFICATE

This is to certify Mr./~~Ms.~~/~~Dr.~~ Md. Shoaibuddin of NIT, Raichur has attended the Faculty Development Programme on **"MATLAB and its Applications to Engineering Problems"** organized by the department of Electrical & Electronics Engineering, from 29th April to 1st May 2016.

Convener

Principal

NET's

NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

~~GUEST LECTURE/SEMINR/ADD-ON COURSE~~/ Faculty Development Programme / ~~WORKSHOP~~ / ~~CONFERENCE/SHORT TERM TRAINING PROGRAMME/ANY OTHER PROPOSAL~~

Date: 27.03.2017

1	**ACADEMIC YEAR**	2016-17
2	**TYPE OF THE PROGRAMME PROPOSED**	*Faculty Development Programme*
3	**TITLE OF THE PROGRAMME:**	"EMBEDDED SYSTEM AND ITS APPLICATIONS"
4	**BROAD OBJECTIVE OF THE PROGRAMME:**	Embedded Systems continue to play an important role in modern society. Embedded system products contain computing capabilities but are not as themselves computers, and may be found in a wide spectrum of applications. Engineering challenges are abound in the design and development of innovative products due to system level integration of hardware and software. The embedded systems market is snowballing at a furious pace and this trend has caught up with a majority of the populace from all across the globe. Virtually every façade of our contemporary lives are dependent on it and in such a scenario, it is also emerging as one of the most promising career alternatives in today's epochs of state of the art Technologies and advanced gadgets.
5	**NAME OF THE INSTITUTION**	NETs NAVODAYA INSTITUTE OF TECHNOLOGY
6	**NAME OF THE DEPARTMENT(S)**	ELECTRICAL AND ELECTRONICS ENGINEERING
7	**BENEFICIERY**	Faculty Members, Post Graduates students, Research Scholars from Electrical Electronics and Engineering.
8	**PROPOSED DATE OF THE PROGRAMME**	12.04.2017 to 13.04.2017
9	**ELIGIBILITY CRITERIA SET:**	Faculties from EEE, ECE and CSE.
10	**NUMBER OF DAYS REQUIRED TO ORGANISE THE PROGRAMME**	Two Day FDP
11	**PROPOSED DURATION OF THE PROGRAMME**	14 hours
12	**TOTAL NO. OF PERIODS REQUIRED FOR THE PROGRAMME**	14 Periods
13	**NO. OF PERIODS REQUIRED PER DAY**	07 Periods

14	**LOCATION DETAILS:**	NETs NIT Auditorium
15	**MINIMUM NO OF PARTICIPANTS:**	30
16	**DETAILS OF SPONSORERS:**	-
17	**DETAILS OF RESOURCE PERSONS:**	Dr.R.Mohanasundaram, Professor, School of Computer Science and Engineering, Vellore Institute of Technology, Vellore. Dr.S.K Manikandan, Professor, Velalar College Of Engineering and Technology, Erode
18	**REGISTRATION FEE WITH CERTIFICATION:**	Rs 200 per participant
19	**REGISTRATION FEE WITHOUT CERTIFICATION:**	Rs 100 per participant.
20	**FACULTY COORDINATOR:**	1.Mr.Mazumdar B.K 2. Mr. Daniel N
21	**EVENT ORGANISERS (STUDENTS):**	1.Mr.Sampreth D 2.Ms.Vijayalakshmi
22	**PROGRAMME PROPOSED BY:**	Dept. of EEE
23	**ANY OTHER RELEVANT MATTER:**	Nil
24	**ESTIMATED BUDGET IN RS:**	Rs.30000/-

This is to the Honorable Chairman of NIT for kind approval:

PRINCIPAL

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

HOD

Head of the Department
Department of Electrical and
Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103, Karnataka

EVENT COORDINATOR

APPROVED/NOT APPROVED



ABOUT RAICHUR:

Raichur, popularly known as 'Cotton City' is bounded by Krishna river on the North and Tungabhadra river on the south. A world famous Hatti Gold Mines is 75 kms away. RTPS, contributing major portion of state's power requirement is situated at Shakti Nagar about 20 kms from the city. Famous pilgrimage centres Mantralayam and Panchamukhi are within a radius of about 35 kms.

HOW TO REACH RAICHUR:

Raichur is well connected by Road and Railway to all nearby and important cities like Bangaluru, Hyderabad, Chennai, Mumbai, Tirupathi, Kalaburgi, Hubballi, and Ballary and so on.

ABOUT THE INSTITUTION

Navodaya Institute of Technology, Raichur, Karnataka is a self-financing co-educational institution established in the year 2008 with all the necessary infrastructural facilities provided through the vision of Navodaya Education Trust (NET®) that ably governs the institution. The college is approved by AICTE, New Delhi and is affiliated to Visvesvaraya Technological University (Belgaum). The institution has completed Eight years of dedicated and excellent service in the field of technical education.

The guiding spirit and the driving force for all the progress and achievements of the Institution is our Honorable chairman of Navodaya Education Trust (NET®), Sri S. R. Reddy. NIT focuses on promoting quality technical education in north Karnataka region and Hyderabad-Karnataka region in particular.

This institution offers five UG courses in CIVIL, MECH., EEE, ECE, and CSE. Two PG courses (M.Tech) in Thermal Power Engineering and Digital Communication & Networking programmes.

The Vision of this college aims at developing NIT as the most respected institution of higher learning renowned for its excellence in Science and Technology and for its commitment to produce high quality engineers who are versatile, innovative and globally competitive.

ABOUT THE DEPARTMENT

The Department was started in the year 2008 with an intake of 60 students with the vision to provide the quality technical education. The department of EEE has well equipped laboratories, one well established computer center with internet connectivity, excellent library, experienced & highly qualified faculty members to impart knowledge in the areas of Electrical Machines, Power Systems, Power Electronics and Drives, Controls Systems, High Voltage Engineering.

ABOUT THE FDP

Embedded Systems continue to play an important role in modern society. Embedded system products contain computing capabilities but are not in themselves computers, and may be found in a wide spectrum of applications. Engineering challenges are abound in the design and development of innovative products due to system level integration of hardware and software.

The embedded systems market is snowballing at a furious pace and this trend has caught up with a majority of the populace from all across the globe. Virtually every façade of our contemporary lives are dependent on it and in such a scenario, it is also emerging as one of the most promising career alternatives in today's epochs of state of the art technologies and advanced gadgets.

NAVODAYA
Institute of Technology
RAICHUR



Faculty Development Programme On

"EMBEDDED SYSTEM AND ITS APPLICATIONS"

12th & 13th APRIL 2017

NAVODAYA INSTITUTE OF TECHNOLOGY * RAICHUR

Convenor
Dr.M.Srinivasan,
Professor and Head/EEE

Coordinators
Prof.Mazumdar.B.K.
Prof.Daniel.N

ORGANIZED BY
Department of Electrical & Electronics Engineering
Navodaya Education Trust®
NAVODAYA INSTITUTE OF TECHNOLOGY
Navodaya Nagar, Bijiangera Road, Raichur-584103, Karnataka, India.

E-mail: hodeee.nit@navodaya.engg.ac.in
Website: www.navodayaengg.in

Registration Form

(Photocopy may be used if required)

Name (in BLOCK letters):

_____

Academic Qualification:

_____

Experience:

_____

Designation &
Department:_____

College Address:_____

_____

_____

Name of Affiliated University:_____

E mail : _____

Contact Number:_____

DD Number/ Date_____

Declaration:

The information furnished above is true to the best of my knowledge.

Date:

Place: Signature of the Applicant

Mr./Ms./Dr._____ is an employee (faculty)/PG student of our institute. He / She will be permitted to attend the programme if selected.

Date:

Place:

Signature & Seal

HOD/Principal/Head of the Organization

Chief Patron

Shri. S. R. Reddy, Hon'ble Chairman
Navodaya Education Trust (NET®), Raichur.

Patron

Shri. N. Bharath Reddy, Hon'ble Trustee.
Navodaya Education Trust (NET®), Raichur.

Chief Convener

Dr.Shivaprakash C. K., Dean & Principal, NIT, Raichur

Convener

Dr. M.Srinivasan, HOD, EEE, NIT, Raichur

Address for communication:

The Principal
Navodaya Institute of Technology (NIT),
Navodaya Nagar, Bijangera Road, Raichur- 584 103.

Coordinator

Prof.Mazumdar.B.K.
Dept of EEE, NIT
E-mail : mazumdar.bk@navodaya.edu.in
Contact no. : +91 - 9845229996

Co-Coordinator

Prof.Daniel.N
Dept of EEE, NIT
E-Mail : Daniel.nixon@navodaya.edu.in
Contact no. : +91 - 9986247085

NAVODAYA INSTITUTE OF TECHNOLOGY * RAICHUR

Organizing Committee

1. Ms.Geeta.K.M
2. Mr.Satishkumar.K.S
3. Mr.Velu.A
4. Mr.Sudhakar.K.
5. Mr.Madhu Babu.B
6. Ms.Sana Anjum
7. Mr.Kiran Kumar

COURSE CONTENT

- Introduction about Embedded System with Applications
- Emerging Trends in Embedded System.
- Embedded Networking Protocols.
- Design of Multiplier using Vedic Mathematics.
- Hands on Training Using Keil IDE for 8051 Microcontroller

Resource Persons:

Experts from reputed educational institutes and industries

Registration Fee:

UG/PG Students : Rs.200/-
Academicians / Research Scholars : Rs.500/-
Industry Personnel's : Rs.1000/-

Lunch will be arranged during the programme. Accommodation will be provided in the campus hostels to the out station participants on prior intimation.

Important dates

FDP date : 12th & 13th April, 2016
Last date for Registration : 09th April, 2016

Registration fee may be paid in the form of DD in favour of **Principal, Navodaya Institute of Technology**, payable at **SBI, NET campus, Raichur.**

VENUE: NIT Auditorium, NIT, Raichur

NAVODAYA
Institute of Technology
RAICHUR

Tel : +91 8532-200115, 240334, Fax : +91-8532-240054
E-Mail :hodeee.nit@navodaya.edu.in

CIRCULAR

Chief Patron
Shri. S. R. Reddy, Hon'ble Chairman,
Navodaya Education Trust (NET®),
Raichur.

Patron
Shri. N. Bharath Reddy., Hon'ble Trustee.
Navodaya Education Trust (NET®),
Raichur.

Chief Convener
Dr.ShivaPrakash C K,
Principal and Dean, NIT, Raichur

Convener
Dr.M.Srinivasan,
HOD/ EEE, NIT. Raichur

Coordinator
Prof.Mazumdar B.K,EEE, NIT
E-Mail: mazumdarbk@gmail.com
Contact no. : +91 - 9845229996

Co-Coordinator
Prof.Daniel N,EEE, NIT
E-Mail: Daniel.nixon@navodaya.edu.in
Contact no. : +91 - 9986247085

Organizing Committee
1. Prof.Geeta K.M
2. Prof.Sathishkumar K.S
3. Prof.Velu.A
4. Prof.Sudhakar.K.
5. Prof.Madhubabu B
6. Prof.Sana Anjum
7. Prof.Kiran Kumar
8. Prof.Kala.M

27.03.2017

Respected Sir / Madam,
Greetings from Navodaya Institute of Technology, Raichur…

The department of Electrical & Electronics Engineering is organizing a Two day FDP on "**EMBEDDED SYSTEM AND ITS APPLICATIONS**" The attached brochure may kindly be circulated among the students and faculty members / colleagues.

Embedded Systems continue to play an important role in modern society. Embedded system products contain computing capabilities but are not as themselves computers, and may be found in a wide spectrum of applications. Engineering challenges are abound in the design and development of innovative products due to system level integration of hardware and software. The embedded systems market is snowballing at a furious pace and this trend has caught up with a majority of the populace from all across the globe. Virtually every façade of our contemporary lives are dependent on it and in such a scenario, it is also emerging as one of the most promising career alternatives in today's epochs of state of the art technologies and advanced gadgets.

We are pleased to inform that the department of Electrical and Electronics Engineering, Navodaya Institute of Technology is organizing a Faculty Development Programme during 12th April-13th April 2017.

In this context, we look forward your support and request you to encourage participation of professionals by circulating the attached brochure.

Thanking you,
Yours sincerely,

Dr.M.Srinivasan
Convener

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

Inaugural function Agenda

1. *Introduction*
2. *Invocation song*
3. *Welcome address by Dr.M.Srinivasan*
5. *Introduction of Resource person*
6. *Chief Guest Address*
7. *Presidential address*
8. *FDP - Session - I*

Valedictory Function Agenda

1. *Introduction*
2. *Feedback - Students and Faculty participants*
3. *Feedback by HOD*
5. *Feedback by Coordinator*
6. *Feedback by Resource person/Chief Guest*
7. *Certificate distribution*
8. *Presidential address*
9. *Vote of Thanks*

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Head of the Department
Department of Electrical and
Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103, Karnataka

NAVODAYA
Institute of Technology

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Invitation

FDP on

"Embedded System and Its Applications"

BY

Dr. Mohanasundaram.R

Professor, School of Computer Science Engineering (SCOPE)

Vellore Institute of Technology, Vellore

&

Dr. Manikandan. S.K.

Professor , Department of EEE,

Velalar College of Engineering and Technology, Erode.

On

12th April 2017 & 13th April 2017

Venue: NIT, AUDITORIUM

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Head of the Department
Department of Electrical and Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103, Karnataka

Curriculum Vitae

Dr. Mohanasundaram Ranganathan BE., ME., Ph.D.,
Assistant Professor (Senior)
School of Computer Science and Engineering,
VIT University, Vellore – 632014,
Tamilnadu, India.
Email: mohanasundaramr@vit.ac.in mohan.sundhar@gmail.com
+91 98948 20008, +91 9943666126
skype id: mohanasundaram_r



ACADEMIC / PROFESSIONAL BACKGROUNDS (9 years)

- **VIT UNIVERSITY**, Vellore, Tamilnadu, India from 02.01.2013 to till date
- K.S.R. College of Engineering, Tiruchengode from 01.12.2010 to 31.12.2012
- Velalar College of Engineering and Tech, Erode from 02.06.2008 to 30.11.2010
- Nandha Engineering College, Perundurai from 08.02.2008 to 31.05.2008

EDUCATIONAL BACKGROUND

- **Ph.D,** (Electrical Engineering - Jan 2012 to Dec 2015) Anna University - Chennai, TN, India.
- **M.E** (Embedded System Technologies - June 2006 to April 2008), Velalar College of Engineering and Technology, Erode, TN, India
- **B.E.** (Electrical and Electronics Engineering - June 2003 to April 2006), Velalar College of Engineering and Technology, Erode, TN, India
- **Diploma** (Electrical and Electronics Engineering - June 2001 to April 2003), MPNMJ Polytechnic College, Chennimalai, Erode, TN, India

PROFESSIONAL MEMBERSHIP

- Life Member, ISTE (ID: LM65275)
- Life Member, CSI (ID: I1501938)

AWARDS

- Research Award 2015 for Publications, VIT University, India on April 2016.
- Research Award 2014 for Publications, VIT University, India on April 2015.
- Certificate of Appreciation for best organizer, International conference on computing, cybernetics and intelligent Information Systems (CCIIS' 2013), VIT University, India.

PEER-REVIEWER:

- Wireless Personnel Communications (Springer)
- International Journal of Circuits and Systems
- International Journal of Engineering Technology
- Journal of Testing and Evaluation
- Bonfring publications
- Inderscience-International Journal of Advanced Intelligence Paradigms (IJAIP)
- Bio-Medical Research

INVITED LECTURE GIVEN:

- 27 January 2015, MPNMJ Engineering College, TN, India – "Digital Principles".
- 05 March 2016, KSR College of Engineering, TN, India – Chief Guest and Keynote speaker for a national conference on cutting edge technologies in information and communication.
- 28 March 2016, KSR College of Engineering, TN, India- "Soft tools for Digital Logic - Quatrus-II".

RESEARCH INTEREST AND ACCOMPLISHMENTS:

- Wireless sensor networks – Energy optimization
- Swarm Intelligence – Positioning of Storage node
- Embedded systems – Intelligent Buildings
- Signals processing – Voice recognition
- Very large scale integration – VLSI design
- Energy conservation – power factor management, stabilizer

Master Degree project: **Embedded Based Home PC Access and Control using VNC through GSM** - This project is to control and access our home personal computer by using VNC (Virtual Network Computing) from anywhere in the world.

Under Graduate Project: **Implementation of Multi-rate FIR Filter in FPGA** - The objective of this project is to develop a FPGA based system, which implements the Multirate FIR Filter. This allows a signal to pass from input to output according to the frequency of the signal. Frequencies above the pass band are attenuated with a high degree of attenuation.

Diploma Project: **5KV** Automatic Voltage Stabilizer

- Guided More than 15 number of Engineering Projects, 6 M.Tech Project.
- At present guiding 3 Ph.D (by Research) candidates and 1 M.Tech (by research) candidate.

PUBLICATIONS:

Google scholar profile: https://goo.gl/KyDhbd

** Three more papers has been communicated*

PUBLICATIONS - THOMSON REUTERS INDEXED:

1. **Mohanasundaram Ranganathan**, and Pappampalayam Sanmugam Periasamy. "Hybrid Swarm Intelligence Optimization Approach for Optimal Data Storage Position Identification in Wireless Sensor Networks." The Scientific World Journal 2015 (2015). (Impact Factor : 1.219)
2. **Mohanasundaram R**, P.S.Periasamy, "Clustering Based Optimal Data Storage Strategy Using Hybrid Swarm Intelligence In WSN", Wireless Personal Communications, SPRINGER 2015 (Impact Factor : 0.979)

PUBLICATIONS – SCOPUS INDEXED:

1. Dr.A.Jayanthiladevi, **Dr.Mohanasundaram**, Dr. K. Sakthivel, "E-Agricultural Market", International Journal of Control Theory and Applications, 9(28), pp. 1-8, 2016.
2. **R.Mohanasundaram**, Atana Iyer D, Suganthi A, "A Gas leakage detection system based on Embedded systems", International Journal of Pharma and Bio Sciences ISSN 6299-0975 special issue in computational biological data science, June 2016, pp. 68-72. (Impact Factor : 5.121)
3. P.Anitha, **R.Mohanasundaram**, P.S.Periasamy, "Optimization Methods for Data Storage Position in Wireless Sensor Networks: A Survey", Asian Journal of Information Technology Medwell Journals, (ISSN: 1993-5994), Vol 15, Issue: 13, pp. 2238-2243, 2016. (Impact Factor : 0.053)
4. **R.Mohanasundaram**, P.S.Periasamy, "A Meta heuristic Algorithm for Optimal Data Storage Position in Wireless Sensor Networks", Pakistan Journal of Biotechnology (ISSN : 1812-1837), special issue on Innovations in information Embedded and communication Systems) Pp. 463-468 (2016). (Impact Factor : 0.300)
5. **R.Mohanasundaram**, P. S. Periasamy "Swarm Based Optimal Data Storage Position Using Enhanced Bat Algorithm In Wireless Sensor Networks" International Journal of Applied Engineering Research ISSN 0973-4562 Volume 10, Number 2 (2015) pp. 4311-4328. (Impact Factor : 0.217)

PUBLICATIONS – NON-SCOPUS INDEXED:

1. **R.Mohanasundaram**, V.R.Raghuveer ,"Improving the Effectiveness of Learning Content Retrieval through Content Classification and Profiling", International Journal of Computer Science and Mobile Computing, Volume 3, Issue 8, August 2014
2. **R. Mohanasundaram**, K. Aarthi Priyamvadha, , "An Efficient Deduction Of Data Loss Rate In Wireless Sensor Network"International Journal of Modern Engineering Research (IJMER), Vol. 3, Issue. 3, pp-1649-1653 May - June 2013.
3. R.Arun, **R.Mohanasundaram**, "EGMP: Multicasting Routing Protocol with QoS Improvements", Bonfring International Journal of Research in Communication Engineering Volume: 2 | Issue: Special Issue 1, Pages: 98-101, February 2012.

4. **R. Mohanasundaram,** E.K. Kavithaashri, " Small Delay Defect Detection Using Signature Register", Bonfring International Journal of Power Systems and Integrated Circuits, Vol. 2, Special Issue 1, Part 2, Pages: 60-67, February 2012.

5. B.Sudharsan, **R.Mohanasundaram**, "Observing and Control of Agricultural Parameterns by using Wired/Wireless Networking Infrastructure", International Journal of Advanced Research n Technology, Vol.2, Issue 2, 2012, Pg No: 170-176.

INTERNATIONAL CONFERENCES:

1. **R.Mohanasundaram**, Dr.P.S.Periasamy "Embedded Based Home Personal Computer Access and Control using Virtual Network Computing" , International Conference on Cloud Computing and Services 2011 (ICCCS 2011) held on Dec 14 & 15 , 2011 at Savitha Engineering College, Chennai.

2. **R.Mohanasundaram**, Dr.P.S.Periasamy "A Meta heuristic Algorithm for Optimal Data Storage Position in Wireless Sensor Networks", 2016 International Conference on Innovations in information, Embedded and Communication Systems (ICIIECS), 17&18 March 2016 at Karpagam Engineering College, Coimbatore.

NATIONAL CONFERENCES:

1. **R.Mohanasundaram**, M.Sreedhar "Embedded Based Home PC Access and Control Using VNC through GSM" in the 5th National Conference on "Application of Emerging Technologies" during 24 - 25 March 2008 at Adhiyamaan College of Engineering, Hosur, Tamilnadu, India.

2. **R.Mohanasundaram**, M.Sreedhar "Embedded Based Home PC Access and Control Using VNC through GSM" in the 1st National Conference on "Computing and Communication Technologies" on 21st March 2009 at Surya Engineering College, Mettukkadai, Erode.

3. **R.Mohanasundaram** "A Compact AES Encryption Standard on Xilinx FPGA" in the 2nd National Conference on "Computing and Communication Technologies" on 20st March 2010 at Surya Engineering College, Mettukkadai, Erode.

4. **R.Mohanasundaram**, N.Vijay "Implementation of ASSIST in Embedded based Medical Technology" in the 1st National Conference on "Trends in Electronics, Instrumentation, Embedded System and Automation" on 27th March 2010 at Karunya University.

5. **R.Mohanasundaram**, P.R.K.Kavithasri " Small Delay Defect Detection Using Signature Register", National Conference on Innovaations in Electrical and Electronics Engineering (IEEE'12) on 21.01.2012 at M.P.N.M.J Engineering College, Chennimalai.

6. B.Sudharsan, **R.Mohanasundaram**, "Monitoring and Control of Green House Parameters by using Wired/Wireless Networking Infrastructure", National Conference on Innovaations in Electrical and Electronics Engineering (IEEE'12) on 21.01.2012 at M.P.N.M.J Engineering College, Chennimalai.

7. **R.Mohanasundaram**, P.R.K.Kavithasri " Small Delay Defect Detection Using Signature Register", National Conference on Communication Technology Interventions for Rural and Social Development(NCCTIRD-2012) during 16.02.2012 & 17.02.2012 at Sri Krishna College of Engineering and Technology, Coimbatore.

8. R.Arun, **R.Mohanasundaram**, "EGMP: Multicasting Routing Protocol With QOS Improvements", National Conference on Communication Technology Interventions for Rural and Social Development (NCCTIRD-2012) during 16.02.2012 & 17.02.2012 at Sri Krishna College of Engineering and Technology, Coimbatore.

PROJECTS AND SEMINAR GRANTS:

S.No	Name of the. sponsor	Title of Project/Seminar	Fund Received in Rs.
1	DRDO	Seminar on Embedded Wireless Sensor Networks	25,000.00 [ERIP/ER/11-12/C/406/M/01]
2	Anna University	FDP on Communication theory	Rs.70,000 15/CFD/Winter/FDTP/2012-44
3	AICTE	Applications Of Wireless Sensor Network In Industry And Consumer Electronics	Rs.200,000
4	DRDO	Two days' workshop on Research challenges in WISENET	Rs.30,000 ERIP/ER/12-13/C/473
5	CSIR	International Conference	Rs.40,000 SYM/8007/13-HRD

TEACHING INTEREST AND SUBJECT TAUGHT

For M.Tech/M.E Programme:

- MCDA (Microcontroller system Design and Applications)
- ESD (Embedded System Design)
- RTS (Real Time System)
- ECS (Embedded Control Systems)
- Embedded System Lab-I
- Embedded System Lab-II
- DECS (Design of Embedded Control Systems)

For B.Tech/B.E Programme:

- Networking protocols
- Digital logic and principles
- Wireless sensor networks
- Wireless communication
- Integrated Circuits lab.
- Computer architecture and organization
- Engineering Practice Lab (Electrical)
- EE (Electrical Engineering)
- DS&OOPS (Data Structures and Object Oriented Programming)
- DS&OOPS Lab
- ECED (Electric Circuits and Electron Devices)
- Microprocessor and Microcontrollers lab
- ECED (Electric Circuits and Electron Devices) lab
- MRISC (Microcontroller and RISC Architecture)
- Data Structures and Algorithm

For Bsc Programme:

- Computer Architectures.
- Digital Logics and Computer Design (theory and lab)
- Data Structures and Algorithm
- Fundamentals of Computer design
- Microprocessors

RESPONSIBILITIES:

- **NAAC** – MOU file in-charge [Component 43, 47, 48, 49]
- **Internal Board of Study member** at VIT University and K.S.R. College of Engineering.
- **Doctoral Committee member** for three research scholars in Anna University.
- **Time Table Committee member of SCSE at VIT** University from April 2013 to till now.
- **NAAC** Course files in-charge at VIT University.
- **VIT Men's Hostel Deputy warden from 2013.**
- **AICTE Proposal incharge at** K.S.R.College of Engineering from June 2011 to Dec 2012.
- **Department Placement co-coordinator** at K.S.R.College of Engineering from June 2011 to Dec 2012.
- **Time Table Coordinator** at K.S.R.College of Engineering from Jan 2012 to Dec 2012.
- **Staff Coordinator** for EEE Student's Association during the academic year 2008-2009.
- **Industrial Visit Coordinator** for EEE Dept during the academic year 2008-2009.
- **Test Coordinator** in EEE Department 2008-2010.
- **Magazine Coordinator** of EEE Department during the Academic year 2008-2009.
- **Staff coordinator** for **VCET Students Alumni Association.**

PARTICIPATION IN SEMINARS / WORKSHOPS/VALUE ADDED COURSES:

S.No	Nature of program	Topic / Title	Organized by	Period	No.of Days
1.	Workshop	SMART board – Note Book Software	ASC, VIT-Vellore	10.01.2017	1
2.	Workshop	Project based Learning (PBL)	ASC, VIT-Vellore	13.12.2016	1
3.	Workshop	High order thinking skills (HOTS)	ASC, VIT-Vellore	12.12.2016	1
4.	FDP	FDP on LaTex	SCOPE, VIT University	16.11.2016	1
5.	FDP	FDP on Mathematical Tools for Signal, Image Processing and Communications	SENSE, VIT University	15.07.2016 to 17.07.2016	3
6.	Workshop	Competency Mapping for Effective Teaching	ASC, VIT-Vellore	07.04.2016	1
7.	FDP	How to set Question Papers for an Open book test	ASC, VIT-Vellore	25.09.2015	1
8.	FDP	Tender Loving Care	ASC, VIT-Vellore	14.07.2015	1
9.	ISTE Workshop	Computer Networking	IIT Bombay and VIT University	28.05.2014 to 05.06.2014	8
10.	FDP	Effective Paper for Journal	VIT University	17.06.2014	1
11.	FDP	MATLAB	VIT University	27.08.2013 to 29.08.2013	3

12.	FDP	Recent Trends in Data Mining Tools, Applications and Research Issues	VIT University	28.06.2013 & 29.06.2013	2
13.	Seminar	Low power VLSI Design	JCT College of Tech, CBE	01.10.2012	1
14.	Seminar	Recent trends and research challenges in Wireless Networks	PSG Tech, CBE	28.09.2012 to 30.09.2012	3
15.	Workshop	Educator's day 2011	National Instruments & NICE at Bangalore	12.10.2011 & 13.10.2011	2
16.	Workshop	Industrial Automation using PLC and DCS	Kongu Engineering College	07.12.2009 to 09.12.2009	3
17.	Workshop	Computational Tools for Engineering Applications	Kongu Engineering College	21.05.2009 to 25.05.2009	5
18.	Workshop	Embedded Sensor Networks	CIT, Coimbatore.	01.02.2008 & 02.02.2008	2
19.	Inplant Training	--	VI Microsystems Pvt Ltd, Chennai	02.06.2007 to 06.06.2007	5
20.	Course	VLSI modular course	SANDS Instrumentation Pvt Ltd, Chennai	09.01.2006 to 21.06.2006	6 Months
21.	Mini Project Exhibition	Gate Locker Security Systems	Dept of ECE, Velalar College of Engineering and Technology.	23.09.2005	1
22.	Seminar	Embedded Systems	Organ Technologies Pvt Ltd	01.02.2005	1
23.	Inplant Training	--	SPB Paper Mill, Erode	14.05.2004 to 03.06.2004	20
24.	Mini Project Exhibition	--	Dept of ECE, Velalar College of Engineering and Technology.	16.02.2004	1

ADMINISTRATIVE COMPONENTS:

S.No	Name of the programme	Sponsored by	Duration
1	Computer Hardware Maintenance and Networking Concepts	Velalar College of Engg and Tech (VCET)	fifteen days
2	Hands on training program on Embedded Software Tools	VCET	20th May 2009
3	Foundational Course on Embedded Systems	VCET	one day
4	PC Hardware Maintenance And Networking	VCET	fifteen days
5	Guest Lecture on Data Structures and Object oriented programming	K.S.R. College of Engineering	2 days
6	National Seminar on Embedded Wireless Sensor Networks	DRDO	2 days
7	Two days workshop on Research challenges in WISENET	DRDO	2 days

8	International Conference	CSIR	3 days
9	CAL Seminar Series on FPGA and VLSI	Self-Supportive	3 days
10	Value added program on Android Application Development	Self-supportive	2 days
11	Recent trends in computer architecture	VIT	One day

PERSONAL DETAILS:

Date of Birth : 15-05-1984.
Father's name : A.Ranganathan
Mother's Name : R.Selvi
Address : 531, Uttukuli Road, Melappalayam, Chennimalai, Erode-638051, TN, India.
Age : 32
Marital Status : Married
Languages Known : English, Tamil, Chinese (Elementary level), Hindi ((Elementary level))
Nationality : Indian

DEPENDENT DETAILS:

Spouse Name and Qualification : K.S.Sivaranjanee B.E., M.E., MBA.,
First Daughters' Name : M.Sanaa (DOB : 23/06/2011 & 5 Years old)
Second Daughters' Name : M.Nayanaa (DOB: 28/03/2015 & 1 Year old)

PASSPOST DETAILS:

Passport Number : K0414291

Date of Expiry : 01/04/2023

REFERENCES:

Dr.P.S.Periasamy
Prof & Head / ECE, K.S.R. College of Engineering, Tiruchengode, TN, India
psperiasamy.ps@gmail.com , +91 9442995539

Dr.M.Sreedhar
Professor/ Department of EEE
Velalar College of Engineering and Technology, Erode, TN, India
callsreedhar@gmail.com, +91 9994566675

DECLARATION:

I hereby declare that the above details are true to my knowledge.

Date : 24.01.2017

Place: VIT, TN, India **(R.Mohanasundaram)**

Curriculum Vitae

Dr.S.K.Manikandan, B.E., M.Tech., Ph.D.,

Professor/ Department of Electrical and Electronics Engineering

Velalar College of Engineering and Technology

Thindal, Erode – 638012

Tamilnadu, India

Email: drmani0705@gmail.com,manikandankandasamy@gmail.com

+91 9159751987, +91 7010747425



ACADEMIC / PROFESSIONAL BACKGROUND (10Yrs 7 months)

- **Velalar College of Engineering and Technology**, Erode, Tamilnadu, India from 21.08.2006 to till date.

EDUCATIONAL BACKGROUND

- **Doctor of Philosophy** in Information and Communication Engineering – Jan 2011 to Dec 2016 Anna University, Chennai, Tamilnadu, India.
- **Master of Technology** in Embedded Systems – June 2004 to April 2006 SASTRA University, Tanjore, Tamilnadu, India.
- **Bachelor of Engineering** in Electronics and Communication Engineering – June 1999 to May 2003, in AnnaiMathammalSheela Engineering College,Periyar University,Namakkal, Tamilnadu, India.

PROFESSIONAL MEMBERSHIP

- **Life Member,** ISTE (ID:LM50929)
- **Life Member,** International Association of Engineers (ID: 145779)
- **Life Member,** Society of Digital Information and Wireless Communications (ID:15222)

AWARDS& ACHIEVEMENTS

- **Ph.D**Thesis titled **"Analysis and Design of Vedic Multipliers for Nikhilam Sutra with reduced bit size using Karatsuba algorithm" -Highly commended**by Indian and Foreign reviewers with '**A**' grade and suggested for applying patent of research work.
- Played vital role as both**NBA and NAAC coordinator**to achieve Accreditation for 2 years and 'A' grade respectively.
- Guided a Project which received **$1000 from IEEE** in the academic year 2009 -2010.
- Produced **University Rank holders** in Embedded System Technologies.
- Received Certificate of Appreciation from VCET for having produced best results in the university examinations during the academic year 2010-2011 &2015-2016.
- Completed B.E degree with 75.6% (First class with Distinction).

RESEARCH INTEREST AND ACCOMPLISHMENTS

- **Mathematics** – Vedic Multiplication (Simplify the complexity in Multiplication)
- **VLSI Technology** – Multiplier Design.
- **Embedded System** – Intelligent Building.
- **Digital Systems** – Synchronous & Asynchronous Techniques.

Post Graduate project work:

- **Automated Reader System** (Automatically detects when level variation changes in tank and sends the report to the data logger due to this human error has been reduced during the measurement of level change)
- **Communication using I^2C** protocol (Controlling of a system by transferring data using I^2C protocol)

Undergraduate project work:

- **Microprocessor Based Telephone Interfacing Unit** (Telephone interfacing unit through power line communication.)

Guided 11 Post graduate projects & more than 15 under graduate projects.

PUBLICATIONS:

National / International Journals:

- **ManikandanSK**&Palanisamy C,(2016), 'Design of Binary Vedic Multiplier based on Nikhilam sutra using bit-reduction technique', Sylwan, vol.160, no.2, pp. 106-117, ISSN: 0039-7660 (updated list of Journal 2016).

- **Manikandan SK**&Palanisamy C,(2016), 'Design of High Speed Vedic Multiplier Based on Nikhilam Sutra Algorithm using Successive Approximation method', Asian Journal of Research in Social sciences and Humanities, vol. 6, no. 6, pp. 608-622, ISSN :2249-7315 (updated list of Journal 2016).

- **Manikandan SK**&Palanisamy C,(2016), 'Design of an efficient Binary Vedic Multiplier for High Speed Applications Using Vedic Mathematics with Bit Reduction Technique', Circuits and Systems, vol. 7, pp. 2593-2602. (updated list of Journal 2016)

- PalanisamyC,**ManikandanSK**&Aiswarya R, (2015),'Application of high speed LFSR in bch encoder and rs encoder', International Journal of Applied Engineering Research, vol. 10, no.9, pp. 9003-9009.

- Nisha Angeline, M, Palanisamy, C, **Manikandan, SK**&Sharmitha, EK 2013, 'High Throughput LFSR Design for BCH Encoder using Sample Period Reduction Technique for MLC NAND based Flash Memories', International Journal of Computer Applications, vol. 66, no.10, pp. 33-39.

- **Manikandan, SK**,Sharmitha, E K, Nisha Angeline, M &Palanisamy C 2013, 'A High-Speed LFSR Design by the Application of Sample Period Reduction Technique for BCH Encoder', IOSR Journal of VLSI and Signal Processing (IOSR-JVSP), vol. 1, no. 5, pp. 17-24, ISSN: 2319-4200.

- **Manikandan, SK**, &Palanisamy C 2012, 'VLSI Design of Cache Compression in Microprocessor Using Pattern Matching Technique', IOSR Journal of Electronics and Communication Engineering (*IOSRJECE*), vol.1, no.6,pp-63-75, ISSN:2278 – 2834.

- **Manikandan, SK**, 2015,'Smart sensors for Industrial monitoring in IoTenvironment',International Journal of Electrical and Electronics Research, Vol. 3, Issue 2, pp: 545-550, ISSN:2348-6988.

- **Manikandan, SK** &Sindhu, M 2016, 'Smart Bin', International Journal of Advanced Research Trends in Engineering and Technology (IJARTET), Vol.No.3, Spl. ,Issue 24, pg. no.230-234, ISSN:2394-3777.

National / International Conference:

- **S.K.Manikandan**, 'Smart Bin', International Conference on Recent Trends in Electronics, Communication and Computation Technologies, Sri Ramanathan Engineering College, 6th – 7th April 2016.

- **S.K.Manikandan**&Vimala. R.'Developing an Android application for College Management System', International conference of Electrical and Electronics & computer Engineering ICEECE, Vivekanandha College of Engg. For women, March 2016.

- **S.K.Manikandan**&S.Aravindh, 'High Resolution Monitoring of Atmospheric pollutants using a system of Low-cost sensors' , International Conference in Synergistic Evolutions in Engineering, Surya Engineering College, March 2015.

- **S.K.Manikandan**'Design of Vedic Multiplier Using Nikhilam Sutra Method', National Conference on Trends in Engineering Applications and Management, Velalar College of Engineering and Technology, Erode, March 2015.

- **S.K.Manikandan**'Design of High Throughput VLSI Architecture forLinear Feedback shift Register', National Conference on Trends in Engineering Applications and Management, Velalar College of Engineering and Technology, Erode, March 2015.

- **S.K.Manikandan,** 'Application of high speed LFSR in BCH encoderand RS encoderInternational Conference on Engineering Technology and Science (ICETS'15), Muthayammal College of Engineering,Rasipuram, March 5th& 6th2015.

- **S.K.Manikandan,**'Analysis of BCH Encoding Technique for efficient Feed Back Method', 6th National Conference on Emerging Technologies and Applications in Electrical Sciences, Velalar College of Engg and Technology, Erode, March 15th 2014.

- **S.K.Manikandan,**'Design of Efficient Feedback Shift Register for BCH Encoder', 6th National Conference on Signals, Systems and Security, Bannari Amman Institute of Technology, Sathyamangalam, February 27 & 28 – 2014.

- **S.K.Manikandan,**'High Throughput LFSR Design for BCH encoder using Sample Period Reduction Technique for MLC NAND based Flash memory', Recent Advances in Communication

Power and Computuing Intelligence – RACPIC'13, Jay Shriram Group Of Institutions, Tirupur, March 2013.

- **S.K.Manikandan**,Dr.C.Palanisamy S.Pavithra "VLSI Implementation of Cellular Automata Based Encoder and Decoder for Multi-Byte Error Correction"in Innovations In Engineering And Technology For Sustainable Development IETSD-2012, September 3-5, 2012 at Bannari Amman Institute of Technology, Sathyamangalam.

- **S.K.Manikandan**,"Two Dimensional Cellular Automata based VLSI Architecture for Compression', Recent Advancement & Challenges in Electrical Engineering and Applications, Institute of Road and Transport Technology, Chithode, 29th Feb& 1st March 2012

Details of the Patents

Career Development Programs Organised / Attended

(i) Details of Seminars / Workshops /FDP etc.,

S. No.	Period		Event Name	Topic / Title	Organized by	Participated / presented
	From	To				
1.	25.01.08	26.01.08	Workshop	Wireless Embedded Systems	IIPC & TBI at Kongu Engineering College	Participated
2.	13.03.08	-	Workshop	Latest Measurement Technique in RF & Microwave Engineering	Velalar College of Engg& Technology in Association with M/s.Agilent Technologies	Participated
3.	17.03.08	28.03.08	Short Term Programme	AICTE Sponsored Induction Training Programme for Faculty of Engineering Colleges	National Institute of Technical Teachers' Training and Research Ministry of Human Resource Development, Govt. of India	Participated
4.	06.09.08	-	Workshop	Algorithm in Image Processing	Velalar College of Engg& Technology, Thindal	Participated
5.	21.04.09	25.04.09	Faculty Empowerment Program	Mission 10X	ISTE & Wipro at Velalar College of Engg& Technology, Thindal	Participated
6.	21.05.09	22.05.09	Workshop	Computational tool for Engineering Application	KonguEngg. College, Perundurai	Participated

7.	07.12.09	09.12.09	Seminar	Industrial Automation Using PLC and DCS	KonguEngg. College, Perundurai	Participated
8.	30.08.10		Seminar	Renewable Energy Sources	Velalar College of Engg& Technology, Thindal	Participated
9.	07.09.12		Workshop	Renesas 16 BIT RL78 Microcontroller	Velalar College of Engg& Technology, Thindal	Participated
10.	06.05.13	16.05.13	FDP	AICTE Sponsored FDP on "Research and Reconfigurable Embedded System for Medical Science"	KSR Institute for Engineering and Technology, Thiruchencode.	Participated
11.	29.01.14	30.01.14	Workshop	Biomedical Signal & Image Processing using MATLAB	Velalar College of Engg& Technology, Thindal	Participated
12.	07.02.14		Workshop	Project based Learning with MATLAB & SIMULINK	Velalar College of Engg& Technology, Thindal	Participated
13.	07.02.14		Workshop	Project based Learning with MATLAB & SIMULINK	Velalar College of Engg& Technology, Thindal	Participated
14.	16.06.15	20.06.15	FDP	Digital Principles and Design using Verilog HDL	Velalar College of Engg& Technology, Thindal	Participated
15.	21-09-15	26-09-15	Workshop	Design of Printed Circuit Board(PCB) & Mini Projects	Velalar College of Engg& Technology, Thindal	Presented
16.	03-10-15		Workshop	Light Emitting Diode Products Designing And Assembling	Velalar College of Engg& Technology, Thindal	Presented
17.	16.12.15	18.12.15	FDP	EE-6602 Embedded Systems	Velalar College of Engg& Technology, Thindal	Presented
18.	17.09.16	-	Workshop	Outcome Based Education	KSR College of Engineering	Participated

19.	16.12.16	17.12.16	DST – SERB Sponsored National Seminar	Recent Trends and challenges in Power Electronics Converters for Smart Grid Interface of Renewable Energy System	Nandha Engineering College	Participated

TEACHING INTEREST AND SUBJECT TAUGHT

For M.E Programme:

- MCDA (Microcontroller system Design and Applications)
- ESD (Embedded System Design)
- RTS (Real Time Systems)
- CNS (Cryptography and Network Security)
- ADSD (Advanced Digital System Design)
- Embedded System Lab-I
- Embedded System Lab-II
- EN (Embedded Networking)

For B.Tech/B.E Programme:

- Networking protocols
- Digital logic and principles
- Electronics Devices and Circuits
- Linear and Digital Integrated Circuits lab.
- Computer architecture and organization
- Engineering Practice Lab (Electrical)
- EE (Electrical Engineering)

Various Responsibilities:

- **PG Time Table Coordinator from** 2007-2011.
- **Class Mentor** for 2008-2012 Batch.
- **Embedded System Lab Incharge**2011 to till Date.
- **UG & PG Time Table Coordinator** 2012-2013.
- **NBA – Criteria 4 Coordinator** for the academic year 2012-2013.
- **NBA Coordinator** from 2013- till date.
- **NAAC Coordinator** from 2013 to till date.
- **Class Mentor** for 2013-2017 Batch.

- **Board of Studies Member** for VCET from 2016 to till date.
- **Anna University Representative** in the academic year 2015-16.
- College **NBA Internal Audit member**.

ADMINISTRATIVE COMPONENTS:

S.No	Name of the Programme	Sponsored by	Duration
1.	Hands on training program on Embedded Software Tools	VCET	20-05-2009
2.	Foundational Course on Embedded Systems	VCET	13-04-2012
3.	Design of Printed Circuit Board(PCB) & Mini Projects	VCET	21-09-15 - 26-09-15
4.	Light Emitting Diode" Products Designing And Assembling	VCET	03-10-15
5.	EE-6602 Embedded Systems	SELFSPONSORED	16.12.2015 - 18.12.2015

Personal Details:

Father's Name :S.R.Kandasamy (Late)

Mother's Name :N.Palaniammal

Date of Birth :7th June 1982

Present Address : F1, Nataraja Complex, Mel Thindal, Erode - 638012

Sex : Male

Languages Known : English, Tamil.

Nationality : Indian

Marital Status :Married

DEPENDENT DETAILS:

Spouse Name and Qualification :M.Nisha Angeline, B.E., M.E.,(Ph.D)

Son's Name : M.Shanjai (DOB:13.09.2009)

PASSPORT DETAILS:

PASSPORT NUMBER :

REFERENCES:

Dr.C.Palanisamy,

Professor & Head,
Department of Information Technology,
Bannari Amman Institute of Technology,
Sathyamangalam – 638415
cp_samy@yahoo.com
+91 9965120967

DECLARATION:

I hereby declare that the above details are true to my knowledge.

Date :

Place: VCET

(S.K.Manikandan)

NET's

NAVODAYA INSTITUTE OF TECHNOLOGY, RAICHUR

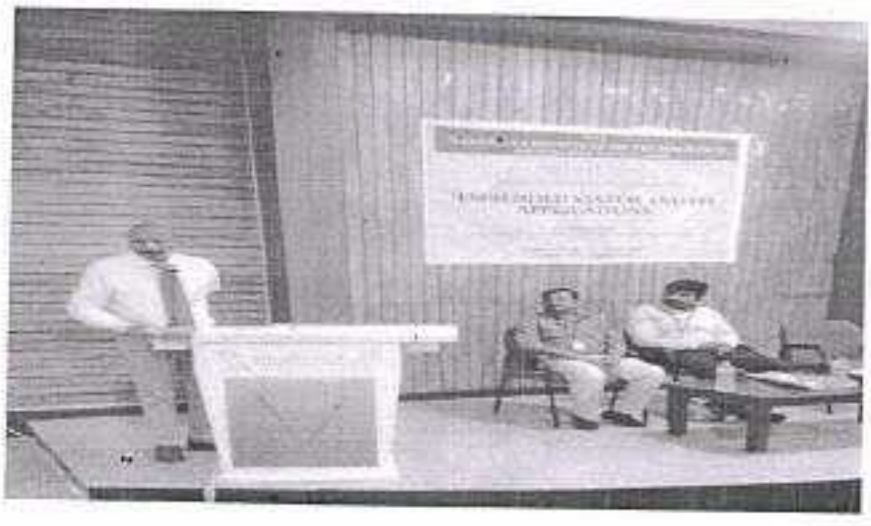
DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

SUMMARY OF FACULTY DEVELOPMENT PROGRAMME ON

"EMBEDDED SYSTEM AND ITS APPLICATIONS"

On 12th and 13th of April 2017, Department of EEE has conducted a FDP on "Embedded system and its applications", with a view to provide the essential details about embedded system. Asound 10 external participants from various Colleges and 20 internal participants attended the workshop. .Dr.M.Srinivasan, Head, Department of EEE welcomed the gathering.. Dr.Sivaprakash.C.K. Principal gave a presidential address and facilitated the Chief Guests with mementos. The function was inaugurated by the Chief Guest Dr.R.Mohanasundaram, Professor, School of Computer Science and Engineering, Vellore Institute of Technology, Vellore.

On 12.04.2017 Dr.R.Mohanasundaram, handled the first day all sessions on Basic embedded system, Architecture, Applications and Altera *Quartus* II which is helpful for the designing of digital gates and architecture. The second day participants learnt the software about Keil IDE and on 13.04.2017, Dr.S.K.Manikandan, Professor, Department of EEE, Velalar college of Engineering and Technology explained Vedic Mathematics Architecture which was useful to design of digital multiplication and design of digital architecture. All the sessions was very interactive. Participants gained lot of knowledge regarding the design of embedded system and its applications.



Dr.Shivaprakash C.K, Dean and Principal, NIT Raichur delivering Presidential address

13/4/17

Head of the Department
Department of Electrical and
Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103, Karnataka

NAVODAYA INSTITUTE OF TECHNOLOGY * RAICHUR



Dr.R.Mohanasundaram, Professor, School of Computer Science and Engineering, Vellore Institute of Technology, Vellore. Delivering content on Emerging Trends in Embedded system.



Dr.S.K.Manikandan, Professor, Department of EEE, Velalar college of Engineering and Technology issuing certificate to Dr.M.Srinivasan HOD/EEE NIT-Raichur.

NAVODAYA INSTITUTE OF TECHNOLOGY • RAICHUR

Head of the Department
Department of Electrical and Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103, Karnataka

NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

FDP - FEEDBACK

DEPARTMENT: EEE DATE: 13/04/2017

SEMESTER : —

Name of the speaker(s)	:	Dr. R. Mohanasundaram
Designation	:	Professor
Institution/University/Organization	:	Vellore Institute of Technology, Vellore
Title of the Lecture	:	Embeded system & its Application
Date & Time	:	12/04/2017 – 13/04/2017
venue	:	NIT, Auditorium.
Beneficiary	:	Kiran Kumar T.H, Asst. Prof. NIT, Raichur
Comments Speaker(s)	:	All FDP's Sessions are informative.
Signature	:	

Arranged by : Dept. of EEE

Date: 13/04/2017

HEAD OF THE DEPARTMENT

Head of Department
Department of Electrical and
Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584135, Karnataka

NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

FDP - FEEDBACK

DEPARTMENT: EEE DATE: 13/04/2017

SEMESTER :

Name of the speaker(s)	: Dr. R. Mohanasundaram	
Designation	: Professor	
Institution/University/Organization	: Vellore Institute of Technology, Vellore.	
Title of the Lecture	: Embedded S/m & IIs application	
Date & Time	: 12/04/2017 — 13/04/2017.	
venue	: NIT, Raichur	
Beneficiary	: Mr. Shamiuddin Nadaf, Asst Prof., ECE	
Comments Speaker(s)	: The purpose of the FDP was served properly.	
Signature		

Arranged by : EEE

Date: 13/04/2017

13/4/17

HEAD OF THE DEPARTMENT

Head of the Department
Department of Electrical and
Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103, Karnataka

NAVODAYA INSTITUTE OF TECHNOLOGY • RAICHUR

NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

FDP - FEEDBACK

DEPARTMENT: EEE DATE: 13/04/2017

SEMESTER :

Name of the speaker(s)	: Dr. R. Mohana Sundaram	
Designation	: Professor	
Institution/University/Organization	: Vellore Institute of Tech., Vellore.	
Title of the Lecture	: Embedded system & its applications	
Date & Time	: 12/04/2017 – 13/04/2017	
venue	: NIT. Auditorium	
Beneficiary	: B. Madhu Babu, Asst. professor, Dept. of EEE	
Comments Speaker(s)	: All FDP's sessions are good & informative	
Signature	:	

Arranged by : Dept. of EEE.

Date: 13/04/2017

HEAD OF THE DEPARTMENT
Head of the Department
Department of Electrical and
Electronics Engineering
Navodaya ... logy,
RAICHur ... Karnataka

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

FDP ATTENDED-INTERNAL

DEPARTMENT: EEE DATE: 13.04.2017

TITLE OF PROGRAMME: "EMBEDDED SYSTEMS AND ITS APPLICATIONS"

RESOURCE PERSONS: Dr. R. MOHANASUNDARAM, Professor.

INSTITUTION/ORGANIZATION: School of computer science and Engineering

ADDRESS: Vellore Institute of Technology, Vellore.

DATES: 12.04.2017 to 13.04.2017

SNO	NAME	DESIGNATION	SIGNATURE
1	Kiran Kumar T.H	Asst. Prof.	
2.	B. Madhu Babu	Asst. prof	
3	Sana Anjum	Asst Prof	
4	BATHIST KUMAR. KS	ASST. PROF	
5	VELU.A	Asst. Prof	
6	Geeta. K.M	Asst Prof	
7.	E. Chandra Veerayya	Asst. prof.	
8.	Md. Muzaib Ali	Asst Prof	
9.	Sachin Kumar	Asst. Prof	
10.	Madhumi Devicharay	Asst Prof	
11.	Gaurav Shetty	Asst. Prof.	
12	Sana Anjum	Asst Prof	
13	Swetha G	Asst Prof	
14	E. Shanthi	Asst. Professor	
15	Hanumesh	Asst Prof	
16	Vijaykumar Yadav	ASST Professor	
17.	Shivakumar Reddy	AP/CSE	

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

HEAD OF THE DEPARTMENT
Head of the Department
Department of Electrical and Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103, Karnataka

NET's

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR- 584103, KARNATAKA

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

CERTIFICATE

This is to certify Mr./~~Ms.~~/~~Dr.~~ Velu. A of NIT, Raichur.

has attended the faculty development programme on "**Embedded System and Its Applications**" conducted at Navodaya Institute of Technology, Raichur on 12-04-2017 and 13-04-2017.

Convener

Principal

NET's

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR- 584103, KARNATAKA

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

CERTIFICATE

This is to certify Mr./~~Ms.~~/~~Dr.~~ DANIEL. N of NIT, RAICHUR has attended the faculty development programme on "**Embedded System and Its Applications**" conducted at Navodaya Institute of Technology, Raichur on 12-04-2017 and 13-04-2017.

Convener

Principal

NET GROUP OF EDUCATIONAL INSTITUTIONS: RAICHUR
NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

~~*GUEST LECTURE/SEMINR/ADD-ON COURSE/*~~ *Faculty Development Programme* ~~*WORKSHOP /CONFERENCE/SHORT TERM TRAINING PROGRAMME/ANY OTHER PROPOSAL*~~

Date: 02.07.2018

1	**ACADEMIC YEAR**	2017-18
2	**TYPE OF THE PROGRAMME PROPOSED**	***Faculty Development Programme***
3	**TITLE OF THE PROGRAMME:**	***"Operation and Maintenance of Solar Electric Systems"***
4	**BROAD OBJECTIVE OF THE PROGRAMME:**	The renewable energy resource base is sufficient to meet several times the present world energy demand and potentially even 10 to 100 times this demand. Solar Energy Sources have a huge potential to provide energy services for the world. The objective of the workshop is to impart knowledge to the UG and PG Students /Research Scholars / Developers / Consultants / Manufactures and Suppliers of solar systems regarding operation and Maintenance of solar PV plants in the country and give an insight regarding the varioustechnologies of solar power, Also this workshop trains the faculty members, students and industrialists to safely and effectively perform O&M tasks, including inspections, commissioning, performance verification and troubleshooting.
5	**NAME OF THE INSTITUTION**	NETs NAVODAYA INSTITUTE OF TECHNOLOGY
6	**NAME OF THE DEPARTMENT(S)**	ELECTRICAL AND ELECTRONICS ENGINEERING
7	**BENEFICIARY**	Faculty Members
8	**PROPOSED DATE OF THE PROGRAMME**	23.07.2018 to 27.07.2018
9	**ELIGIBILITY CRITERIA SET:**	Faculties from EEE, ECE and Mechanical Engineering.
10	**NUMBER OF DAYS REQUIRED TO ORGANISE THE PROGRAMME**	Five Day FDP
11	**PROPOSED DURATION OF THE PROGRAMME**	30hours
12	**TOTAL NO. OF PERIODS REQUIRED FOR THE PROGRAMME**	30 Periods

14	**LOCATION DETAILS:**	NETs NIT Auditorium
15	**MINIMUM NO OF PARTICIPANTS:**	40
16	**DETAILS OF SPONSORERS:**	-
17	**DETAILS OF RESOURCE PERSONS:**	1. Er.S. Selvakumar, Power System Engineer, ABB Global industries Ltd., Chennai. 2. Er.Gandhirajan, Power System Engineer, Power Projects Ltd., Chennai. 3. Er.Karthick C, Power System Engineer, Power Projects Ltd., Chennai.
18	**REGISTRATION FEE WITH CERTIFICATION:**	Rs 200 per participant
19	**REGISTRATION FEE WITHOUT CERTIFICATION:**	Rs 100 per participant.
20	**FACULTY COORDINATOR:**	1.Mr.Daniel N 2.Mr.Velu A
21	**EVENT ORGANISERS (STUDENTS):**	Mr.MD SHAKEER AHMED Ms.ANAGHA DESAI
22	**PROGRAMME PROPOSED BY:**	Dept. of EEE
23	**ANY OTHER RELEVANT MATTER:**	Nil
24	**ESTIMATED BUDGET IN RS:**	Rs.60,000

This is to the Honorable Chairman of NIT for kind approval:

PRINCIPAL

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

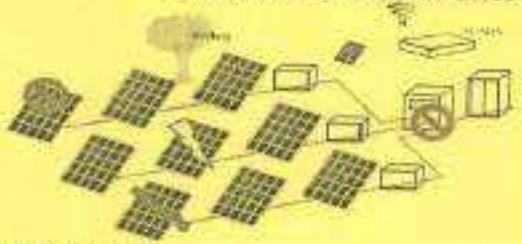
HOD 25/7/2018

Head of the Department
Department of Electrical and
Electronics Engineering
Navodaya Institute of Technology
RAICHUR-584 103. Karnataka

APPROVED/NOTAPPROVED

EVENT COORDINATOR
02/07/2018

Operations & Maintenance



ABOUT RAICHUR:

Raichur, popularly known as 'Cotton City' is bounded by Krishna River on the North and Tungabhadra River on the south. A world famous Hatti Gold Mines is 75 kms away. RTPS, contributing major portion of state's power requirement is situated at Shakti Nagar about 20 kms from the city. Famous pilgrimage centres Mantralayam and Panchamukhi are within a radius of about 35 kms.

HOW TO REACH RAICHUR:

Raichur is well connected by Road and Railway to all nearby and important cities like Bangaluru, Hyderabad, Chennai, Mumbai, Tirupathi, Kalaburgi, Hubballi, and Ballary and so on.

ABOUT THE INSTITUTION

Navodaya Institute of Technology, Raichur, Karnataka is a self-financing co-educational institution established in the year 2008 with all the necessary infrastructural facilities provided through the vision of Navodaya Education Trust (NET®) that ably governs the institution. The college is approved by AICTE, New Delhi and is affiliated to Visvesvaraya Technological University (Belgaum). The institution has completed Eight years of dedicated and excellent service in the field of technical education.

The guiding spirit and the driving force for all the progress and achievements of the Institution is our Honorable chairman of Navodaya Education Trust (NET®), Sri S. R. Reddy. NIT focuses on promoting quality technical education in north Karnataka region and Hyderabad-Karnataka region in particular.

This institution offers five UG courses in CIVIL, MECH., EEE, ECE, and CSE. Two PG courses (M.Tech) in Thermal Power Engineering and Digital Communication & Networking programmes.

The Vision of this college aims at developing NIT as the most respected institution of higher learning renowned for its excellence in Science and Technology and for its commitment to produce high quality engineers who are versatile, innovative and globally competitive.

ABOUT THE DEPARTMENT

The Department was started in the year 2008 with an intake of 60 students with the vision to provide the quality technical education. The department of EEE has well equipped laboratories, one well established computer center with internet connectivity, excellent library, experienced & highly qualified faculty members to impart knowledge in the areas of Electrical Machines, Power Systems, Power Electronics and Drives, Controls Systems, High Voltage Engineering.

ABOUT THE FDP

The renewable energy resource base is sufficient to meet several times the present world energy demand and potentially even 10 to 100 times this demand. Solar Energy Sources have a huge potential to provide energy services for the world. The objective of the FDP is to impart knowledge to the UG and PG Students /Research Scholars / Developers / Consultants / Manufactures and Suppliers of solar systems regarding operation and Maintenance of solar PV plants in the country and give an insight regarding the various technologies of solar power. Also this FDP trains the faculty members, students and industrialists to safely and effectively perform O&M tasks, including inspections, commissioning, performance verification and troubleshooting.

NAVODAYA INSTITUTE OF TECHNOLOGY

Five day
Faculty Development Programme
On
"OPERATION AND MAINTENANCE OF SOLAR ELECTRIC SYSTEMS"

On

23rd to 27th July 2018

Convenor

Dr.M.Srinivasan,

Professor and Head/EEE

Coordinators

Prof.Daniel.N

Asst.Prof./EEE

Prof.Velu.A

Asst.Prof./EEE

INSTITUTE OF TECHNOLOGY * NAVODAYA * RAICHUR

ORGANIZED BY

Department of Electrical & Electronics Engineering

Navodaya Education Trust®

NAVODAYA INSTITUTE OF TECHNOLOGY

Navodaya Nagar, Bijenegera Road,

Raichur-584103, Karnataka, India.

E-mail:

hodeee.nit@navodaya.engg.ac.in

Website: www.navodaya.edu.in

Registration Form

(Photocopy may be used if required)

Name (in BLOCK letters):

Academic Qualification:

Experience:

Designation &
Department:
College Address:

Name of Affiliated University:
E mail :
Contact Number:
DD Number/ Date

Declaration:

The information furnished above is true to the best of my knowledge.

Date:
Place: Signature of the Applicant

Mr./Ms./Dr. _____ is an employee (faculty)/PG student of our institute. He / She will be permitted to attend the programme if selected.

Date:
Place:

Signature & Seal
HOD/Principal/Head of the Organization

Chief Patron

Shri. S. R. Reddy, Hon'ble Chairman
Navodaya Education Trust (NET®),
Raichur.

Patron

Dr.M.V.Mallikarjuna Principal, NIT, Raichur

Convener

Dr. M.Srinivasan, HOD, EEE, NIT, Raichur

Coordinators

Prof.Daniel.N
Assistant Professor
Department of EEE-NITR
Contact no:7892635921
Email Id: daniel.n@navodaya.edu.in

Prof.Velu A
Assistant Professor
Department of EEE-NITR
Contact no:9591330675
Email Id:velu.a@navodaya.edu.in

Address for communication:

The Principal
Navodaya Institute of Technology (NIT),
Navodaya Nagar, Bijangera Road, Raichur- 584 103.

Organizing Committee

1. Prof.Mazumdar B.K
2. Prof.Geeta.K.M
3. Prof.Satishkumar.K.S
4. Prof.Sudhakar Kothuru
5. Mr.MadhuBabu.B
6. Ms.SanaAnjum
7. Mr.KiranKumar

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

COURSE CONTENT

- Solar Photovoltaic Solar Cell, Solar PV module, Solar PV Module Arrays.
- Batteries Types of Batteries, Battery parameters, How to select Battery, Batteries for Photo voltaic System, Application of Batteries in Solar PV system.
- Charge Controller , MPPT and Inverter AC to DC Converter, DC to AC Converter, DC to DC power converter, Charge controller.
- Maximum Power Point Tracking Solar PV System Design.

Resource Persons:

1. Er.S. Selvakumar,
   Power System Engineer,
   ABB Global Industries Ltd., Chennai.
2. Er.Gandhirajan,
   Power System Engineer,
   Power Projects Ltd., Chennai,
3. Er.Karthick C,
   Power System Engineer,
   Power Projects Ltd., Chennai.

Registration Fee:

PG Students : Rs.200/-
Academicians / Research Scholars : Rs.500/-
Industry Personnel's :Rs.1000/-

Lunch will be arranged during the programme. Accommodation will be provided in the campus hostels to the out station participants on prior intimation.

Important dates

FDP date : 23rd to 27th July, 2018
Last date for Registration : 21st July, 2018

Registration fee may be paid in the form of DD in favour of **Principal, Navodaya Institute of Technology**, payable at **SBI, NET campus, Raichur.**

NET's
NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR
Tel : +91 8532-200115, 240334, Fax : +91-8532-240054
E-Mail :hodeee.nit@navodaya.edu.in

CIRCULAR

Chief Patron
Shri. S. R. Reddy, Hon'ble Chairman,
Navodaya Education Trust (NET®),
Raichur.

Patron
Shri. N. Bharath Reddy., Hon'ble Trustee.
Navodaya Education Trust (NET®),
Raichur.

Chief Convener
Dr.M.V.Mallikarjuna
Principal,NIT, Raichur

Convener
Dr.M.Srinivasan,
HOD/ EEE, NIT. Raichur

Coordinator
Prof.Daniel N,EEE, NIT
E-Mail: Daniel.nixon@navodaya.edu.in
Contact no. : +91 – 9986247085

Co-Coordinator
Prof.Velu A,EEE, NIT
E-Mail: velu.a@navodaya.edu.in
Contact no. : +91 - 9591330675

Organizing Committee
1. Prof.B.K.Mazumdar
2. Prof.Geeta K.M
3. Prof.Sathishkumar K.S
4. Prof.Sudhakar.K.
5. Prof.Madhubabu B
6. Prof.Sana Anjum
7. Prof.Kiran Kumar

21.07.2018

Respected Sir / Madam,
Greetings from Navodaya Institute of Technology, Raichur…

The department of Electrical & Electronics Engineering is organizing Five day FDP on ***Operation and Maintenance of Solar Electric Systems***. The attached brochure may kindly be circulated among the students and faculty members / colleagues.

The main objective of the renewable energy resource base is sufficient to meet several times the present world energy demand and potentially even 10 to 100 times this demand.

Solar Energy Sources have a huge potential to provide energy services for the world. The objective of the FDP is to impart knowledge to the UG and PG Students /Research Scholars / Developers / Consultants / Manufactures and Suppliers of solar systems regarding operation and Maintenance of solar PV plants in the country and give an insight regarding the various technologies of solar power, Also this FDP trains the faculty members, students and industrialists to safely and effectively perform O&M tasks, including inspections, commissioning, performance verification and troubleshooting.

We are pleased to inform that the department of Electrical and Electronics Engineering, Navodaya Institute of Technology is organizing a Faculty Development Programme during 23rd July – 27th July 2018.

In this context, we look forward your support and request you to encourage participation of professionals by circulating the attached brochure.

Thanking you,
Yours sincerely,

NAVODAYA INSTITUTE OF TECHNOLOGY * RAICHUR

Dr.M.Srinivasan
Convener

NAVODAYA INSTITUTE OF TECHNOLOGY

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Invitation

Five day FDP

On

"Operation and Maintenance of Solar Electric Systems"

BY

Er.S.SelvaKumar,

Power System Engineer,

ABB global Industries LTD., Chennai

Er.Gandhirajan,

Power System Engineer,

Power Projects Ltd., Chennai.

&

Er.Karthick C,

Power System Engineer,

Power Projects Ltd., Chennai.

On

23.07.2018 to 27.07.2018

Venue: NIT - Auditorium

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Inaugural function Agenda

1. *Introduction*
2. *Invocation song*
3. *Welcome address by Dr.M.Srinivasan -HOD/EEE*
5. *Introduction of Resource person*
6. *Chief Guest Address*
7. *Presidential address*
8. *FDP- Session - I*

Five day FDP on

OPERATION AND MAINTENANCE OF SOLAR ELECTRIC SYSTEMS

Valedictory Function Agenda

1. *Introduction*
2. *Feedback - Faculty participants*
3. *Feedback by HOD*
5. *Feedback by Coordinator*
6. *Feedback by Resource person/Chief Guest*
7. *Certificate distribution*
8. *Presidential address*
9. *Vote of Thanks*

NAVODAYA INSTITUTE OF TECHNOLOGY * RAICHUR

NAVODAYA INSTITUTE OF TECHNOLOGY

Faculty development programme on

OPERATION AND MAINTENANCE OF SOLAR ELECTRIC SYSTEMS

Schedule on 23.07.2018 to 27.07.2018

Day	Session-1 (09.30 am to 11.00am)		Tea Break (11.00am To 11.15 am)	Session-2 (11.15 am to 12.45pm)	Lunch break (12.45pm to 02.00pm)	Session-2 (2.00 pm to 3.30pm)	Tea Break (03.30pm To 03.45 pm)	Session-3 (3.45pm to 5.15pm)
Monday (23.07.2018)	Registration 09.00 to 10.00	Inauguration and Key note address by Er.E.Selvakumar ABB Global industries Ltd., Chennai.		Er.S. Selvakumar, Power System Engineer, ABB Global industries Ltd., Chennai. (Introduction-Solar Photovoltaic Solar Cell, Solar PV module, Solar PV Module Arrays)		Er.Gandhirajan, Power System Engineer, Power Projects Ltd., Chennai. (Batteries Types of Batteries, Battery parameters)		Er.Gandhirajan, Power System Engineer, Power Projects Ltd., Chennai. (How to select Battery for particular application.)
Tuesday (24.07.2018)	Er.Karthick C, Power System Engineer, Power Projects Ltd., Chennai. (Application of Batteries in Solar PV system)			Er.S. Selvakumar, Power System Engineer, ABB Global industries Ltd., Chennai. (AC to DC Converter, DC to AC Converter)		Er.E.Selvakumar ABB Global industries Ltd., Chennai. (DC to DC power converter, Charge controller)		Er.Karthick C, Power System Engineer, Power Projects Ltd., Chennai. (MPPT Maximum Power Point Tracking)

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Head of the Department
Department of Electronics Engineering
Navodaya Institute of Technology
RAICHUR 584 103, Karnataka

Day	Session-1 (09.30 am to 11.00am)		Session-2 (11.15 am to 12.45pm)		Session-3 (2.00 pm to 3.30pm)		Session-4 (3.45pm to 5.15pm)
Wednesday (25.07.2018)	**Er.Gandhirajan,** Power System Engineer, Power Projects Ltd., Chennai. (Batteries for Photo voltaic System)	Tea Break (11.00am To 11.15 am)	**Er.Karthick C,** Power System Engineer, Power Projects Ltd., Chennai. (Different types of Charge Controller)	Lunch break (12.45pm to 02.00pm)	**Er.S. Selvakumar,** Power System Engineer, ABB Global industries Ltd., Chennai. (DC to AC Converter in PV applications)	Tea Break (03.30pm To 03.45 pm)	**Er.Karthick C,** Power System Engineer, Power Projects Ltd., Chennai. (Solar PV System Design)
Thursday (26.07.2018)	**Er.Gandhirajan,** Power System Engineer, Power Projects Ltd., Chennai. (Various types of batteries in solar PV systems)		**Er.S. Selvakumar,** Power System Engineer, ABB Global industries Ltd., Chennai. (DC to AC Converter design using MATLAB simulation)		**Er.Karthick C,** Power System Engineer, Power Projects Ltd., Chennai. (MATLAB Simulation on Charge Controller)		**Er.S. Selvakumar,** Power System Engineer, ABB Global industries Ltd., Chennai. (Importance of PV syst software for PV design)

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Head of the Department
Department of Electrical and Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103. Karnataka

Day	Session-1 (09.30 am to 11.00am)		Session-2 (11.15 am to 12.45pm)		Session-3 (2.00 pm to 3.30pm)		Session-4 (3.45pm to 5.15pm)
Friday (27.07.2018)	**Er.E.Selvakumar** ABB Global industries Ltd., Chennai. : (Different MPPT implementation using MATLAB software both coding and simulation)	*Tea Break (11.00am To 11.15 am)*	**Er.Gandhirajan,** Power System Engineer, Power Projects Ltd., Chennai. : (PV syst software to find the different angles for installation)	*Lunch break (12.45pm to 02.00pm)*	**Er.S. Selvakumar,** Power System Engineer, ABB Global industries Ltd., Chennai. (DC to DC Converter design using MATLAB simulation)	*Tea Break (03.30pm To 03.45 pm)*	**Er.S. Selvakumar,** Power System Engineer, ABB Global industries Ltd., Chennai. (operation and maintenance of PV system)

23/7/18

NAVODAYA INSTITUTE OF TECHNOLOGY * RAICHUR

Head of the Department
Department of Electrical and Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103. Karnataka

NET's

NAVODAYA INSTITUTE OF TECHNOLOGY, RAICHUR

Department of Electrical and Electronics Engineering

Summary of Faculty Development Programme on

"OPERATIONAND MAINTENANCE OF SOLAR ELECTRIC SYSTEMS"

On 23rd July to27th July2018, Department of EEE has conducted a FDP on "**OPERATION AND MAINTENANCE OF SOLAR ELECTRIC SYSTEMS**", with a view to provide the essential details about operation and maintenance of solar electric systems. Around 07 external participants from various Colleges and 40 internal participants attended the FDP.

Dr.M.Srinivasan M, Head, Department of EEE, welcomed the gathering.Dr.Mallikarjun Principal gave a presidential address and facilitated the Chief Guests with mementos. The function was inaugurated by the Chief GuestEr.S.SelvaKumar, Power System Engineer, ABB global Industries LTD., Chennai.

On First day (23.07.2018),Er.S.SelvaKumar**handled the first day morning sessions on**Solar Photovoltaic Solar Cell, Solar PV module, Solar PV Module Arrays.

Er. Gandhirajan**handled the Afternoon sessions on**Charge Controller, MPPT and Inverter AC to DC Converter.

The Second day (24.07.2018),Er.Karthick C and Er.S.SelvaKumar**handled the all sessions on**DC to AC Converter, DC to DC power converter with MATLAB SIMULINK.

On Third day (25.07.2018),Er.S.SelvaKumar,Er. Gandhirajan&Er.Karthick **Chandled the all sessions on** Charge controller.Batteries Types of Batteries, Battery parameters, How to select Battery.

On Fourth day (26.07.2018),Er.S.SelvaKumar,Er.Karthick C &Er.Gandhirajan**handled the all sessions on**Batteries for Photo voltaic System, Application of Batteries in Solar PV system,Design methodology for SPV system. Wires & Cables,

On Fifth day (27.07.2018),Er.S.SelvaKumar&Er.Karthick **Chandled the all sessions on** Maximum Power Point Tracking (Different types and its algorithm)Solar PV System Design and Integration Types of Solar PV System, Mechanical structure Design Installing.

All the sessions were very interactive. Participants gained lot of knowledge regarding the design of embedded system and its applications.

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

27/7/2018

Head of the Department
Department of Electrical and
Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103, Karnataka



Er .Selva Kumar, Power System Engineer handled the topic "DC to AC converter design in PV applications uses MATLAB" on 25.07.2018.



Er.Karthick C, Power System Engineer delivers the content "Charge Controller design for PV applications" on 26.07.2018.

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Head of the Department
Department of Electrical and Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103, Karnataka.



Er.SelvaKumar and Er.Gandhirajan, deliver the content on practical consideration of PV installation to participants on 27.07.2018.

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

27/7/2018

Head of the Department
Department of Electrical and Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103, Karnataka

Er.S Selvakumar,
Power System Engineer,
ABB Global industries Ltd.,
Chennai

He has completed his Bachelor degree in Electrical & Electronics Engineering at RVS College of Engineering and Technology in 2006 with first class distinction. He is also Gold Medalist in his Master's degree in Power Systems at Anna University, Chennai in 2015. After his Master's degree he has joined in Power Research and Development Consultants (PRDC) Ltd, Bangalore as Engineer – Power Systems. He has completed many challenging assignments in the Power Evacuation, Power System studies, Control & Protection, Relay settings and coordination, Subsynchronous Resonance etc., He has handled many projects, TATA Steel 10 MTPA Expansion, Power Evacuation studies for Suzlon in southern grid, Impact of Monopolar operation of Talchar – Kolar HVDC are to name a few. Ultra Tech Cement plant at Tadipatri commissioned without nuisance tripping of relays where he designed the relay settings for complete plant.

In 2008 he has joined in TATA Consulting Engineers Ltd Bangalore. He has involved in many prestigious projects including Mundra 4000 MW UMPP (First in India), 4.5 MW hydro plant at Gokak, 2 * 660 MW Mutiara thermal power plant at Tuticorin. He has involved in designing the power systems of many Steel, Cement, Paper industries, Commercial buildings, Power plants and substations.

He has joined ABB Global industries ltd Chennai in 2011 after completing three year service in TATA Consulting Engineers Ltd, Bangalore. He has completed many challenging assignments including 570 MVA three phase three winding transformer design for combined cycle plant in Bangladesh, Design 120 MW combined cycle plant in Indonesia etc. Presently he is handling 334 MW combined cycle plant in Bangladesh & 105 MW biomass plant in Singapore. He has received "Extra Miler Award" for his excellent contribution for ETAP Training and ISO implementation.

He has a vast experience in Load flow studies, Reactive Power Compensation studies, Voltage dip drop studies, Short circuit studies, Transient studies, motor acceleration studies, Over current and Earth fault relay co-ordination, sub synchronous resonance, Harmonic study etc.. He also have expertise in designing power plants, substation and

process industries. He is also famous in industrial sector for his training on Power System softwares like Mipower , ETAP, Easy Power, Digsilent Power factory, Power World.

His international exposure includes, Bangladesh, Singapore, Malaysia, Qatar, Kuwait, Dubai, Saudi, Australia, Sweeden, Canada, Philiphines, Thailand, Indonesia etc. He is associate editor for journal "Transactions on Engineering and Sciences". He is also Industry Expert Member in Technical Advisory Committee of many academic institutions.

He has guided many projects for Electrical students in Bachelor level and Master level. He has conducted seminars and workshops in various academie institutions and industries.

His areas of interests are Grid Planning, Power System operation and control, Protection, Power Plant & Substation design, Integration of Renewables (Wind, Solar, and Small Hydro), System studies & Smart grid.

Er.Gandhirajan

Power System Engineer,
Power Projects Ltd., Chennai.

He has completed his Bachelor degree in Electrical & Electronics Engineering at Excel College of Engineering in 2014 with first class. After his bachelor degree he has joined in Power Projects, Chennai as Engineer – Power Systems. He has completed many challenging assignments in the Power Evacuation, Power System studies, Control & Protection, Relay settings and coordination etc., He has handled many projects, Surana Steel Industries 5 MTPA Expansion, Tasek Main Power Distribution (Update) in Malaysia, PEPSI Co Plants, Dammam, Jeddah & Riyadh Kingdom of Saudi Arabia, Power Evacuation studies for 108 MW Hydro plant, Johor in Malaysian grid, Power Evacuation studies for 50 MW Solar plant in Malaysian Sabah grid, Power Evacuation studies for 40 MW Diesel plant in Malaysian Sabah grid. Presently he is handling power evacuation studies for 50 MW & 49 MW Solar plant in Malaysia.

He has a vast experience in Load flow studies, Reactive Power Compensation studies, Voltage dip drop studies, Short circuit studies, Transient studies, motor acceleration studies, Over current and Earth fault relay co-ordination, Harmonic study etc. He also have expertise in designing power plants, substation and process industries. He is also famous in industrial sector for his training on Power System software's like PSSE, ETAP, Easy Power, Dig silent Power factory, Power World.

His international exposure includes, Bangladesh, Malaysia, Saudi, Australia, Philippines, Thailand, and Indonesia etc.

He has guided many projects for Electrical students in Bachelor level. He has conducted seminars and workshops in various academic institutions and industries.

His areas of interests are Power System operation and control, Protection, Power Plant & Integration of Renewables (Wind, Solar, and Small Hydro) & System studies.

Er.Karthick C
Power System Engineer,
Power Projects Ltd., Chennai.

He has completed his Bachelor degree in Electrical & Electronics Engineering at Easwari Engineering College in 2009 and Master degree in Power Systems Engineering in 2011 at Vellammal Engineering College. After his Master degree he has joined in Power Projects, Chennai as Engineer – Power Systems. He has completed many challenging assignments in the Power Evacuation, Power System studies, Control & Protection, Relay settings and coordination etc., He has promoted as Sr. Engineer Power System studies in 2014.

He has a vast experience in Load flow studies, Reactive Power Compensation studies, Voltage dip drop studies, Short circuit studies, Transient studies, motor acceleration studies, Over current and Earth fault relay co-ordination, Harmonic study etc. He also have expertise in designing power plants, substation and process industries. He is also famous in industrial sector for his training on Power System software's like PSSE, ETAP, Easy Power, Digsilent Powerfactory, Power World. He has over 5 years' experience in Power system studies.

His areas of interests are FACTS, HVDC, Integration of Renewables, Power System studies etc.

His exposure as follows:

DESIGN CALCULATIONS

- Calculations of the Cable sizing for the different voltage levels.
- Detailed calculations for Earthing system design, Earthing of the equipments, Lightning Protection Calculations, Battery and Charger Sizing Calculations.
- Sizing of Transformer, Bus Duct, Circuit Breaker, Bus bar, Cables, DG set , NGR,NGT,CT, VT, UPS and Batteries.
- Indoor and Outdoor Lighting Calculations using DIALUX Software.

- Preparing of Cable Schedule and giving clearance to the site for the BOQ for the cable Quantity.
- Sizing Calculation of Generator Circuit breaker.

POWER SYSTEM STUDIES

- SLD Preparation & Cable Sizing
- Load flow Studies
- Power factor correction studies at HT &LT Systems
- Motor Voltage Dip/ Drop Studies
- Short Circuit Studies
- Battery Sizing
- Transient stability and Relay Coordination
- Arc Flash Studies

PROJECTS WORKED

- **Rain 3.5 MTPA(Rain Industries Limited)**

  Studies carried out: ", Load Flow Analysis, Short-Circuit Analysis, Motor starting analysis, Relay coordination"

  In this system Grid has 132 KV, Motor Load 20 MW, Aux Load 14 MW.
- **HERO MOTOR CORP LTD – MANUFACTURING PLANT GUJARAT**

  Studies carried out: ", Load Flow Analysis, Short-Circuit Analysis, Motor starting analysis, Relay coordination"
- **TASEK CORPORATION BERHAD in Portland Drive Malaysia**

  Studies carried out: Short circuit analysis, Relay coordination, Arc flash analysis.
- **PEPSI CO DAMMAM in Saudi Arabia**

  Studies carried out: Load Flow Analysis, Short circuit analysis, Static and Dynamic motor acceleration Studies, Relay coordination.

21.07.2018

From
Dr.M.Srinivasan,
Professor and Head,
Navodaya Institute of Technology,
Raichur-584103

To
The Principal,
Navodaya Institute of Technology,
Raichur-584103

Sir

Subject: Req. for Auditorium hall to conduct FDP - Reg

We have proposed to conduct Five day FDP on the topic "**Operation and Maintenance of Solar Electric Systems**" on 23.07.2018 and 27.07.2018. For this Purpose, we need auditorium on 03.03.2015-Time-10.00AM-5.30PM.

Thanking You

Yours faithfully

21/7/18

[Dr.M.Srinivasan]

Head of the Department
Department of Electrical and
Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103. Karnataka

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

21/7/18

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

From

21.07.2018

Dr.M.Srinivasan,

HOD/EEE,

NIT, Raichur

To

The Principal,

NIT, Raichur.

Sir,

Sub: Requisition for Accommodation and Food -Reg

I bring to your kind notice that we are conducting Five day FDP on "**OPERATION AND MAINTENANCE OF SOLAR ELECTRIC SYSTEMS**" our department on 23rd July 2018to 27th July 2018. For that we want to arrange the accommodation and food for the Resource Persons in the above days. So kindly do the needful.

Resource Person Details,

1. Er.S. Selvakumar,
   Power System Engineer,
   ABB Global industries Ltd., Chennai.
2. Er.Gandhirajan,
   Power System Engineer,
   Power Projects Ltd., Chennai.
3. Er.Karthick C,
   Power System Engineer,
   Power Projects Ltd., Chennai.

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Thanking you,

HOD/EEE

To Manager for n/a

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Head of the Department
Department of Electrical and
Electronics Engineering
Navodaya Institute of Technology
RAICHUR-584 103, Karnataka

NAVODAYA INSTITUTE OF TECHNOLOGY

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Evaluation Form

FDP Title: Operation & Maintance of Solar Electric System

Date: 23/7/18 to 27/7/18 Time: 23

Please complete the following evaluation for the FDP you attended. Your feedback will help us evaluate the effectiveness of this program and allow us to make improvements in the future program. **Your feedback is important for planning future programming and services we offer.**

Thank you.

4=Strongly Agree 3=Agree · 2=Disagree 1=Strongly Disagree

	Circle your response			
1. The course met my expectations.	4	3	2	1
2. The content was helpful.	4	3	2	1
3. The level of the course was appropriate.	4	3	2	1
5. The speaker had a good understanding of the topics.	4	3	2	1
6. The handouts were helpful.	4	3	2	1
7. The course was worth my time.	4	3	2	1
8. The length of the course was appropriate.	4	3	2	1
9. I recommend that the course be repeated for other students.	4	3	2	1

What were the most useful aspects of the course?

It was good Program

What changes should be made to enhance/improve this program?

Nill

Additional comments:

Name of the Faculty: Geeta.K.M

Signature

NAVODAYA INSTITUTE OF TECHNOLOGY

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Evaluation Form

FDP Title: Operation & maintenance of Solar Electric Systems

Date: 23/07/18 to 27/07/18 Time: 9.30Am to 5.30PM.

Please complete the following evaluation for the FDP you attended. Your feedback will help us evaluate the effectiveness of this program and allow us to make improvements in the future program. **Your feedback is important for planning future programming and services we offer.**

Thank you.

4=Strongly Agree 3=Agree 2=Disagree 1=Strongly Disagree

	Circle your response			
1. The course met my expectations.	(4)	3	2	1
2. The content was helpful.	4	(3)	2	1
3. The level of the course was appropriate.	(4)	3	2	1
5. The speaker had a good understanding of the topics.	(4)	3	2	1
6. The handouts were helpful.	4	(3)	2	1
7. The course was worth my time.	(4)	3	2	1
8. The length of the course was appropriate.	4	(3)	2	1
9. I recommend that the course be repeated for other students.	(4)	3	2	1

What were the most useful aspects of the course?

What changes should be made to enhance/improve this program?

Additional comments:

Name of the Faculty: E. Chandraveeramma

Signature 27/07/18.

NAVODAYA INSTITUTE OF TECHNOLOGY

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Evaluation Form

FDP Title: Operation & Maintenance of Solar Electric Syst-

Date: 23/07/18 to 27/07/18 Time: 9:30 AM to 5:30 pm

Please complete the following evaluation for the FDP you attended. Your feedback will help us evaluate the effectiveness of this program and allow us to make improvements in the future program. **Your feedback is important for planning future programming and services we offer.**

Thank you.

4=Strongly Agree 3=Agree 2=Disagree 1=Strongly Disagree

	Circle your response			
1. The course met my expectations.	(4)	3	2	1
2. The content was helpful.	4	(3)	2	1
3. The level of the course was appropriate.	(4)	3	2	1
5. The speaker had a good understanding of the topics.	(4)	3	2	1
6. The handouts were helpful.	4	(3)	2	1
7. The course was worth my time.	(4)	3	2	1
8. The length of the course was appropriate.	4	(3)	2	1
9. I recommend that the course be repeated for other students.	(4)	3	2	1

What were the most useful aspects of the course?

What changes should be made to enhance/improve this program?

Additional comments: —

Name of the Faculty: Sachin Kumar

Signature

NAVODAYA INSTITUTE OF TECHNOLOGY

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Evaluation Form

FDP Title: OPERATION AND MAINTENANCE OF SOLAR ELECTRIC SYSTEM

Date: 23-07-2018 to 27-07-2018 Time: 9:30 AM - 5:30 PM

Please complete the following evaluation for the FDP you attended. Your feedback will help us evaluate the effectiveness of this program and allow us to make improvements in the future program. **Your feedback is important for planning future programming and services we offer.**

Thank you.

4=Strongly Agree 3=Agree 2=Disagree 1=Strongly Disagree

	Circle your response			
1. The course met my expectations.	4	(3)	2	1
2. The content was helpful.	(4)	3	2	1
3. The level of the course was appropriate.	(4)	3	2	1
5. The speaker had a good understanding of the topics.	4	(3)	2	1
6. The handouts were helpful.	(4)	3	2	1
7. The course was worth my time.	4	(3)	2	1
8. The length of the course was appropriate.	(4)	3	2	1
9. I recommend that the course be repeated for other students.	(4)	3	2	1

What were the most useful aspects of the course?

Simulation on hand-on was interesting & useful

What changes should be made to enhance/improve this program?

Need Basic type of programs for 10 days

Additional comments: - No -

Name of the Faculty: SATHISH KUMAR. KS

Signature

NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

Department of Electrical and Electronics Engineering

Faculty Development Programme (FDP) on

"Operation and Maintenance of Solar Electric Systems"

From 23rd July to 27th July 2018

S.No.	Name of the Faculty	Designation & Dept.	Signature
1.	VELU A	Asst Professor	
2.	SATHISH KUMAR. KS	ASSISTANT PROFESSOR (EEE)	
3.	Geeta. K. M	Assistant professor	
4.	E. Channa Veeranna	Asst. professor (ECE)	
5.	Sachin Kumar	Asst. prof ECE	
6.	Md. Shoaib Nadim	Asst Prof ECE	
7.	Madhuri Devi	Asst Prof ECE	
8.	Gauravi Shetty	Asst Professor	
9.	Sana Anjum	AP	
10.	Rammoorthy	Asst. Prof	
11.	Kiran kumar T.H	Asst. Prof.	
12.	B. Madhu Latha	Asst. prof	
13.	B K Mourmda	Asst prof	
14.	Srikar G. Kulkarni	Asst Prof	
15.	UHAR	Assistant Professor	
16.	Ravi Kulkarni	Asst professor	
17.	Imran Pasha	Asst professor	
18.	Rajashekarappa	Asst Prof	
19.	Vinayak	AP	
20.	Dr. Anandachar	visiting faculty	

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

27/7/18

HOD & Convener

Head of the Department
Department of Electrical and
Electronics Engineering
Navodaya Institute of Technology,
Raichur-584 103, Karnataka

NAVODAYA INSTITUTE OF TECHNOLOGY

RAICHUR- 584103, KARNATAKA

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

CERTIFICATE

This is to certify that Mr./Ms./Dr. SATHISH KUMAR KS of NIT- RAICHUR has attended Five day FDP on *"Operation and Maintenance of Solar Electric Systems"* conducted at Navodaya Institute of Technology, Raichur on 23-07-2018 to 27-07-2018.

Coordinator 27/7/18

HOD 27/7/18

Principal 27/7/18

NAVODAYA INSTITUTE OF TECHNOLOGY

RAICHUR- 584103, KARNATAKA

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

CERTIFICATE

This is to certify that Mr./Ms./Dr. SACHIN KUMAR of NIT-RAICHUR has attended Five day FDP on "***Operation and Maintenance of Solar Electric Systems***" conducted at Navodaya Institute of Technology, Raichur on 23-07-2018 to 27-07-2018.

Coordinator 27/7/18

HOD 27/7/18

Principal 27/7/18

NAVODAYA INSTITUTE OF TECHNOLOGY

RAICHUR- 584103, KARNATAKA

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

CERTIFICATE

This is to certify that Mr./Ms./Dr. E.CHANNAVEERAHMA of NIT. RAICHUR has attended Five day FDP on "***Operation and Maintenance of Solar Electric Systems***" conducted at Navodaya Institute of Technology, Raichur on 23-07-2018 to 27-07-2018.

Coordinator 27/7/18

HOD 27/7/18

Principal 27/7/18

NET GROUP OF EDUCATIONAL INSTITUTIONS: RAICHUR
NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

~~GUEST LECTURE/SEMINAR/ADD-ON COURSE/~~ *Faculty Development Programme* ~~/WORKSHOP/CONFERENCE/SHORT TERM TRAINING PROGRAMME/ANY OTHER PROPOSAL~~

Date: 11.01.2019

1	ACADEMIC YEAR	2018-19
2	TYPE OF THE PROGRAMME PROPOSED	*Faculty Development Programme*
3	TITLE OF THE PROGRAMME:	**"The application of artificial intelligence in electrical automation control".**
4	BROAD OBJECTIVE OF THE PROGRAMME:	With the development of science and technology, artificial intelligence and electrical automation control technology is innovating and developing. The application of artificial intelligence technology in the electrical automation control is more and more extensive, which provides the development of automation control technology a solid foundation and strong support. The application of artificial intelligence is introduced with their research directions, including expert system, machine learning, pattern recognition, artificial neural network and deep learning etc. At last, the applications of artificial intelligence are analyzed from several perspectives.
5	NAME OF THE INSTITUTION	NETs NAVODAYA INSTITUTE OF TECHNOLOGY
6	NAME OF THE DEPARTMENT(S)	ELECTRICAL AND ELECTRONICS ENGINEERING
7	BENEFICIARY	Faculty Members
8	PROPOSED DATE OF THE PROGRAMME	25.01.2019 to 29.01.2019
9	ELIGIBILITY CRITERIA SET:	Faculty members from EEE and ECE.
10	NUMBER OF DAYS REQUIRED TO ORGANISE THE PROGRAMME	Five Day FDP
11	PROPOSED DURATION OF THE PROGRAMME	30 hours
12	TOTAL NO. OF PERIODS REQUIRED FOR THE PROGRAMME	30 Periods

13	NO. OF PERIODS REQUIRED PER DAY	06 Periods
14	LOCATION DETAILS:	NETs NIT Auditorium
15	MINIMUM NO OF PARTICIPANTS:	30
16	DETAILS OF SPONSORERS:	-
17	DETAILS OF RESOURCE PERSONS:	Er.Malayappan, Senior R&D Engineer, PANTECH Group of companies, Chennai-600119. Er.Sreenivasalu R&D Engineer, PANTECH Group of companies, Hyderabad-500016. Er.Aravindh Swamy.S R&D Engineer, PANTECH Group of companies, Chennai-600119.
18	REGISTRATION FEE WITH CERTIFICATION:	Rs 200 per participant
19	REGISTRATION FEE WITHOUT CERTIFICATION:	Rs 100 per participant.
20	EVENT ORGANISERS (STUDENTS):	Mr.Shaik Asif Ms.Afsha Tabassum
21	FACULTY COORDINATOR:	Prof.Madhubabu B, Asst.Prof./EEE Prof.Kiran Kumar T H, Asst.Prof./EEE
22	PROGRAMME PROPOSED BY:	Dept. of EEE
23	ANY OTHER RELEVANT MATTER:	Nil
24	ESTIMATED BUDGET IN RS:	Rs.50,000

This is to the Honorable Chairman of NIT for kind approval:

11/1/2019

PRINCIPAL

PRINCIPAL
Jaya Institute of Technology (NIT)
RAICHUR-584 103

11/1/2019

HOD

Head of the Department
Department of Electrical and
Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103, Karnataka

11.1.2019

EVENT COORDINATOR

APPROVED/NOTAPPROVED

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

ABOUT RAICHUR:

Raichur, popularly known as 'Cotton City' is bounded by Krishna River on the North and Tungabhadra River on the South. A world famous Hatti Gold Mines is 75 kms away. RTPS, contributing major portion of state's power requirement is situated at Shakti Nagar about 20 kms from the city. Famous pilgrimage centres Mantralayam and Panchamukhi are within a radius of about 35 kms.

HOW TO REACH RAICHUR:

Raichur is well connected by Road and Railway to all nearby and important cities like Bangaluru, Hyderabad, Chennai, Mumbai, Tirupathi, Kalaburgi, Hubballi, and Ballari and so on.

ABOUT THE INSTITUTION:

Navodaya Institute of Technology (NIT), Raichur, established in 2008, is affiliated to Visvesvaraya Technological University (Belagavi). The institute is approved by All India Council for Technical Education (AICTE), New Delhi. The guiding spirit and the driving force for all the progress and achievements of the Institution is our Honorable chairman of Navodaya Education Trust (NET®), Sri S. R. Reddy. NIT focuses on promoting quality technical education in north Karnataka region and Hyderabad-Karnataka region in particular. The education is of paramount importance for socio-economic development. The institute focuses on integrated scientific training with value based education to enable overall development of the aspirants.

NIT is all-inclusive campus, with Administrative block, Library block, independent Academic blocks for each department, Separate Hostels for Boys and Girls, Canteen, Bank (SBI), in-campus ATM, Temple, Gym and Playground all housed in a vast campus of more than 30 acres land surrounded by hillocks and lake.

ABOUT THE DEPARTMENT:

The Department was started in the year 2008 with an intake of 60 students with the vision to provide the quality technical education. The department of EEE has well equipped laboratories, well established computer center with internet connectivity, excellent library, experienced & highly qualified faculty members to impart knowledge in the areas of Electrical Machines, Power Systems, Power Electronics and Drives, Controls Systems, High VoltageEngineering.

Vision

To become a centre of excellence in Electrical and Electronics Engineering, Producing innovative and creative Electrical and Electronics Engineers to meet the global challenges.

Mission

To provide a platform to the students towards attaining quality education in Electrical and Electronics Engineering.

To educate students about professional and ethical responsibilities and train them to build leadership and entrepreneurship qualities for their career development.

To create opportunities and guide students in acquiring career oriented jobs in the field of Electrical and Electronics Engineering.

ABOUT THE FDP:

The objective of the FDP is to impart knowledge to the Faculties / PG Students /Research Scholars, of the application of artificial intelligence technology in the electrical automation control is more and more extensive, which provides the development of automation control technology a solid foundation and strong support. At last, the applications of artificial intelligence are analyzed from several perspectives.

NAVODAYA INSTITUTE OF TECHNOLOGY

Five day
Faculty DevelopmentProgramme
On

"THE APPLICATION OF ARTIFICIAL INTELLIGENCE IN ELECTRICAL AUTOMATION CONTROL."

In association with

PANTECH SOLUTIONS
Technology Beyond The Dreams

25th January 2019 to 29th January 2019

Convenor
Dr.M.Srinivasan, Professor and Head/EEE

Coordinators
Prof.Daniel.N Asst.Prof./EEE
Prof. Sathish Kumar K.S Asst.Prof. /EEE

ORGANIZED BY
Department of Electrical & Electronics Engineering
Navodaya Education Trust®
NAVODAYA INSTITUTE OF TECHNOLOGY
Navodaya Nagar, Bijenegera Road, Raichur-584103, Karnataka, Indra.

E-mail:
hodeee.nit@navodaya.engg.ac.in
Website: www.navodaya.edu.in

Registration Form
(Photocopy may be used if required)

Name (in BLOCK letters):

Academic Qualification:

Experience:

Designation &
Department:

Name ofAffiliatedUniversity:

Email :

ContactNumber:

DD Number/Date:

Declaration:
The information furnished above is true to the best of my knowledge.

Date:
Place: Signature of the Applicant

Mr./Ms./Dr.

is an employee (faculty)/UG/PG student ofourinstitute. He / She will be permitted to attend the programme ifselected.

Date:
Place: Signature & Seal
HOD/Principal/Head of the Organization

Chief Patron
Shri. S. R. Reddy, Hon'ble Chairman
Navodaya Education Trust (NET®), Raichur.

Patron
Dr.M.V.Mallikarjuna Principal, NIT, Raichur

Convener
Dr. M.Srinivasan, HOD, EEE, NIT, Raichur

Coordinators
Prof.Madhubabu B
Assistant Professor
Department of EEE-NITR
Contact no:9700002442
Email Id: madhu.babu@navodaya.edu.in

Prof.Kiran Kumar T.H
Assistant Professor
Department of EEE-NITR
Contact no:9902641856
Email Id:kirankumar.th@navodaya.edu.in

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Address for communication:
The Principal
Navodaya Institute of Technology (NIT),
Navodaya Nagar, Bijangera Road, Raichur- 584 103.

Organizing Committee

1. Prof.Mazumdar B.K
2. Prof.Daniel N
3. Prof.Geeta.K.M
4. Prof.Sathish Kumar K S
5. Prof.Velu A
6. Prof.SudhakarKothuru
7. Ms.SanaAnjum

www.navodaya.edu.in/engineering
/navodayaeducationtrust

COURSE CONTENT

- Introduction to Renewable Energy System
- Power Electronics using RES
- Classical control design of RES
- Inverter Design
- Fuzzy Logic control
- AI techniques
- Electrical automation

Resource Persons:

1. Er.Malayappan,
   Senior R&D Engineer,
   PANTECH Group of companies,
   Chennai-600119.
2. Er.Sreenivasalu
   R&D Engineer,
   PANTECH Group of companies,
   Hyderabad-500016,
3. Er.AravindhSwamy.S
   R&D Engineer,
   PANTECH Group of companies,
   Chennai-600119.

Registration Fee:

UG/PGStudents	: Rs.500/-
Academicians /ResearchScholars	: Rs.750/-
IndustryPersonnel's	:Rs.1000/-

Note: Registration fees includes working lunch and tea without accommodation

Accommodation:
Limited accommodation for the participants will be provided in the institute hostels on prior request (Chargeable basis)

Important dates

FDP date : **January25th, 2019 to January 29th, 2019**
Last date for Registration : **23rdJanuary,2019**

Registration fee may be paid in the form of DD in favour of **Principal, Navodaya Institute of Technology**, payable at **SBI, NET campus, Raichur.**

NAVODAYA INSTITUTE OF TECHNOLOGY

Department Of Electrical and Electronics Engineering
Tel : +91 8532-200115, 240334, Fax : +91-8532-240054
E-Mail :hodeee.nit@navodaya.edu.in

CIRCULAR

Chief Patron
Shri. S. R. Reddy, Hon'ble Chairman,
Navodaya Education Trust (NET®),
Raichur.

Patron
Shri. N. Bharath Reddy., Hon'ble Trustee.
Navodaya Education Trust (NET®),
Raichur.

Chief Convener
Dr.M.V.Mallikarjuna
Principal, NIT, Raichur

Convener
Dr.M.Srinivasan,
HOD/ EEE, NIT. Raichur

Coordinator
Prof.Madhu Babu B,EEE, NIT
E-Mail: madhu.babu@navodaya.edu.in
Contact no. : +91 - 9700002442

Co-Coordinator
Prof.Kiran Kumar T.H, EEE, NIT
E-Mail: kirankumar.th@navodaya.edu.in
Contact no. : +91 - 9902641856

Organizing Committee
1. Prof.Mazumdar B.K
2. Prof.Geeta K.M
3. Prof.Sathish Kumar K.S
4. Prof.Velu.A
5. Prof.Sudhakar.K.
6. Prof.Sana Anjum

23.01.2019

Respected Sir / Madam,
Greetings from Navodaya Institute of Technology, Raichur...

The department of Electrical & Electronics Engineering is organizing Five day FDP on ***"THE APPLICATION OF ARTIFICIAL INTELLIGENCE IN ELECTRICAL AUTOMATION CONTROL"*** The attached brochure may kindly be circulated among the students and faculty members / colleagues.

With the development of science and technology, artificial intelligence and electrical automation control technology is innovating and developing. The application of artificial intelligence technology in the electrical automation control is more and more extensive, which provides the development of automation control technology a solid foundation and strong support. The application of artificial intelligence is introduced with their research directions, including expert system, machine learning, pattern recognition, artificial neural network and deep learning etc. At last, the applications of artificial intelligence are analyzed from several perspectives.

We are pleased to inform that the department of Electrical and Electronics Engineering, Navodaya Institute of Technology is organizing a Faculty Development Programme during 25.01.2019 to 29.01.2019.

In this context, we look forward your support and request you to encourage participation of professionals by circulating the attached brochure.

Thanking you,
Yours sincerely,

22/1/2019

Dr.M.Srinivasan
Convener

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

NAVODAYA INSTITUTE OF TECHNOLOGY

Faculty development programme on

The application of artificial intelligence in electrical automation control

Schedule on 25.01.2019 to 29.01.2019

Day	***Session-1 (09.30 am to 11.00am)***			***Session-2 (11.15 am to 12.45pm)***		***Session-2 (2.00 pm to 3.30pm)***		***Session-3 (3.45pm to 5.15pm)***
Friday (25.01.2019)	***Registration 09.00 to 10.00***	***Inauguration and Key note address by*** *Er.*Malayappan, Senior R&D Engineer PANTECH Group of companies, Chennai	***Tea Break (11.00am To 11.15 am)***	*Er.*Malayappan, Senior R&D Engineer PANTECH Group of companies, Chennai (Introduction to Power Electronics in Renewable Energy System.)	***Lunch break (12.45pm to 02.00pm)***	Er.AravindhSwamy, R&DEngineer PANTECH Group of companies, Chennai. (Classical control design of RES,Inverter Design.)	***Tea Break (03.30pm To 03.45 pm)***	Er.AravindhSwamy, &*Er.*Malayappan, PANTECH Group of companies, Chennai, (Multilevel Inverter design Using MATLAB)
Saturday (26.01.2019)	*Er.*Malayappan, Senior R&D Engineer PANTECH Group of companies, Chennai (Introduction on Fuzzy Logic control)			*Er.*Malayappan, Senior R&D Engineer PANTECH Group of companies, Chennai (Fuzzy Logic controlin control system and other fields)		Er.Sreenivasalu R&DEngineer PANTECH Group ofcompanies,Hyderabad (Introduction to Fuzzy Logic in Artificial intelligence)		Er.Sreenivasalu R&DEngineer PANTECH Group ofcompanies,Hyderabad (AI techniques in Electrical Automation)

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

23/1/19

Head of the Department
Department of Electrical and Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103, Karnataka

Day	Session-1 (09.30 am to 11.00am)		Session-2 (11.15 am to 12.45pm)		Session-2 (2.00 pm to 3.30pm)		Session-3 (3.45pm to 5.15pm)
Sunday (27.01.2019)	Er.AravindhSwamy, R&DEngineer PANTECH Group of companies, Chennai. (Introduction on PLC and SCADA)	Tea Break (11.00am To 11.15 am)	Er.AravindhSwamy, R&DEngineer PANTECH Group of companies, Chennai. (PLC's AB with Ladder logic programming)	Lunch break (12.45pm to 02.00pm)	Er.Sreenivasalu R&DEngineer PANTECH Group ofcompanies,Hyderabad (PLC's ABBwith Ladder logic programming)	Tea Break (03.30pm To 03.45 pm)	Er,Sreenivasalu R&DEngineer PANTECH Group ofcompanies,Hyderabad (Ladder Logic programming for real time applications-AB and ABB PLC)
Monday (28.01.2019)	Er.Malayappan, Senior R&D Engineer PANTECH Group of companies, Chennai (PLC's OMRON with Ladder logic programming)		Er.Malayappan, Senior R&D Engineer PANTECH Group of companies, Chennai (Ladder Logic programming for OMRON PLC)		Er.Sreenivasalu R&DEngineer PANTECH Group ofcompanies,Hyderabad (PLC's KEYENCE with Ladder logic programming)		Er.Sreenivasalu R&DEngineer PANTECH Group ofcompanies,Hyderabad (Real time examples using Keyence PLC)

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

23/1/19

Head of the Department
Department of Electrical and Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103. Karnataka

Day	Session-1 (09.30 am to 11.00am)		Session-2 (11.15 am to 12.45pm)		Session-2 (2.00 pm to 3.30pm)		Session-3 (3.45pm to 5.15pm)
Tuesday (29.01.2019)	Er. Malayappan, Senior R&D Engineer PANTECH Group of companies, Chennai. (PLC's GE Fanuc with Ladder logic programming)	Tea Break (11.00am To 11.15 am)	Er. Malayappan, Senior R&D Engineer PANTECH Group of companies, Chennai. (PLC's Siemens with Ladder logic programming)	Lunch break (12.45pm to 02.00pm)	Er. AravindhSwamy, R&D Engineer PANTECH Group of companies, Chennai. (SCADA Wonder ware touch)	Tea Break (03.30pm To 03.45 pm)	Er. AravindhSwamy, R&D Engineer PANTECH Group of companies, Chennai. (Fix 32, Honeywell DCS)



23-1-19

Head of the Department
Department of Electrical and
Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103. Karnataka

23.01.2019

From
Dr.M.Srinivasan,
Professor and Head,
Navodaya Institute of Technology,
Raichur-584103.

To
The Principal,
Navodaya Institute of Technology,
Raichur-584103.

Sir

Subject: Req. for Auditorium hall to conduct FDP - Reg

We have proposed to conduct the FDPon the topic "***THE APPLICATION OF ARTIFICIAL INTELLIGENCE IN ELECTRICAL AUTOMATION CONTROL.***" on 25.01.2019 and 29.01.2019. For this Purpose, we need auditorium from 25.01.2019 onwards -Time-09.00AM-5.30PM.

Thanking You

Yours faithfully

23/1/19

[Dr.M.Srinivasan]

Head of the Department
Department of Electrical and
Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103. Karnataka

To Mr. Anil for
S.S.
23/1/19

NAVODAYA INSTITUTE OF TECHNOLOGY * RAICHUR

From

Dr.M.Srinivasan,

HOD/EEE,

NIT, Raichur

23.01.2019

To

The Principal,

NIT, Raichur.

Sir,

Sub: Requisition for Accommodation and Food -Reg

I bring to your kind notice that we are conducting One Week FDP on "**THE APPLICATION OF ARTIFICIAL INTELLIGENCE IN ELECTRICAL AUTOMATION CONTROL**" our department on 25thJanuary 2019to 29th January2019. For that we want to arrange the accommodation and food for the Resource Persons in the above days. So kindly do the needful.

Resource Person Details,

1. Er.Malayappan,

Senior R&D Engineer,

PANTECH Group of companies,

Chennai-600119.

2.Er.Sreenivasalu

R&D Engineer,

PANTECH Group of companies,

Hyderabad-500016.

3.Er.AravindhSwamy.S

R&D Engineer,

PANTECH Group of companies,

Chennai-600119.

Thanking you,

To Mgt. Ravi Teja 23/1/19

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

23/1/19

HOD/EEE

Head of the Department
Department of Electrical and Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103, Karnataka



Malaiyappan M

Power Electronics Engineer

EDUCATION

ME Power Electronics & Drives | Passed out-2004
College Name
College of Engineering Guindy, Anna University, Chennai-25

B E EEE | Passed out-2002
College Name
Government College of Engineering, Bargur

CONTACT
#8, NATARAJAN STREET,
NOOKAMPALAYAM LINK ROAD,
CEMMANCHERRY, CHENNAI-119
malaiyappan.m@pantechmail.com
9840974408

EXPERIENCE

Senior RD Engineer 2004-2019
Company name
Pantech Group of Company
Electrical Group

PROJECTS

Successfully Completed MATLAB Integrated Power Electronics Development Application Project for Texas Instruments |2018

Organized and Delivered 5 Days International Workshop in Power Converter Design at National Institute of Technology-Karnataka | 2018

Successfully Installed Open Source 5KW Solar Power System with Thin Film Solar PV Panel in Government College of Technology - Coimbatore| 2017

Involved DC Smart Home Project Development in Coimbatore Institute of Technology|2014

Successfully Installed Open Source 6KW Solar Power System for Non-Linear Loads in Coimbatore Institute of Technology - Coimbatore| 2013

Closed Loop Control Design for SRM/PMSM/BLDC/PMDC with DSP | 2010

Organized and Delivered 2 Days Workshop in Power Converter Design at College of Engineering Guindy, Anna University Chennai|2008.

Vector Control of 3Phase Induction Motor | 2007

FPGA based Controller design and Implementation for 3Phase 5 Level Multilevel

SKILLS

MAGNET	●●●○○
CCS	●●●●○
CADENCE	●●●○○
MATLAB	●●●●○

LANGUAGES

TAMIL

English

HOBBIES &



hyd.electrical@pantechmail.com

Experience of 3+ years in MATLAB SIMULINK domain and having well knowledge in POWER ELECTRONICS, POWER SYSTEM AND HARDWARE Development of Projects with upgrade Technologies. Having a Good Technical Strength In Implementation of Projects & Conducting Work Shops in Universities....

EDUCATION:

B.Tech, Dr.NGP Institute of Technology, Coimbatore, Anna University.

TECHNICAL SKILLS

Programming Languages: C,"C++, M-File

Firmware: MATLAB SIMULINK

WORK EXPERIENCE:

Drive application

- Working on research and development of speed control of BLDC by using fuzzy and PID Controllers.

Renewable energy application

- Worked on research and development of modified sepic converter with mppt(maximum power point tracker)

Power quality

- Working on research and development of Multilevel Inverter Based UPQC for Power Quality Improvement.

Micro grid

- Working on research and development of designed DG based Power Quality Conditioner for Micro Grid applications.

Electrical vehicle application

- Working on research and development of Integrating a solar PV (Photovoltaic) with battery storage is presented to a single phase grid for residential and electric vehicle application.

Inverter and converter

- Development of A Permanent Magnet BLDC Motor Drive Powered From A Solar Photovoltaic Array In Luo Converter Is Used For Maximum Power Point Tracking

CURRENT FIELD OF WORK: Hardware of the Loop and State Flow

NAVODAYA INSTITUTE OF TECHNOLOGY * RAICHUR

Head of the Department
Department of Electrical and
Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103. Karnataka

Seminars / Conference/ Workshops - Conducted / organized:

Recent workshops...

No.	Course	Location
1	Power electronics and renewable energy based on Matlab Simulink	Modern engineering college, pune.
2	Multilevel inverter and drives application using Matlab	CMRCET college ,Hyderabad
3	Renewable energy application by using Matlab	MREC, Hyderabad
4	Power electronics and drives by using Matlab Simulink	Methodist engineering college,hyderabad
5	Power electronics and system by using Matlab Simulink	GNITS college,hyderabad
6	Power converter design and control	Vignan university
7	Power Electronics Applications in Renewable Energy by using MATLAB and IOT	Matrusri Engineering College ,Hyderabad

Overall conducted 25+ workshops, 30+ seminars in various Colleges.

23/1/19

Head of the Department
Department of Electrical and
Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103, Karnataka

NET'S

NAVODAYA INSTITUTE OF TECHNOLOGY, RAICHUR

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

SUMMARY OF FACULTY DEVELOPMENT PROGRAMME ON

"THE APPLICATION OF ARTIFICIAL INTELLIGENCE IN ELECTRICAL AUTOMATION CONTROL"

On 25th January 2019 to29th January2019, Department of EEE has conducted aFive day FDP on ***"THE APPLICATION OF ARTIFICIAL INTELLIGENCE IN ELECTRICAL AUTOMATION CONTROL"***, with a view to provide the essential details about Artificial Intelligence and Automation . 20 internal participants attended the FDP.

Dr.Srinivasan M, Head, Department of EEE, welcomed the gathering.

Dr.Mallikarjuna M.V Principal gave a presidential address and facilitated the Chief Guests with mementos. The function was inaugurated by the Chief Guest

Er.Malayappan, Senior R&D Engineer, PANTECH Group of companies, Chennai-600119.

Er.Aravindh Swamy, R&D Engineer, PANTECH Group of companies, Chennai-600119.

Er.Sreenivasalu, R&D Engineer, PANTECH Group of companies, Hyderabad-500016.

On First day **(25.01.2019),** Er.Malayappan, Senior R&D Engineerhandled the first day morning sessions on Introduction to Power Electronics in Renewable Energy System.Er.Aravindh Swamy,R&D Engineerhandled the afternoon sessions onClassical control design of RES,Inverter Design using Matlab software.

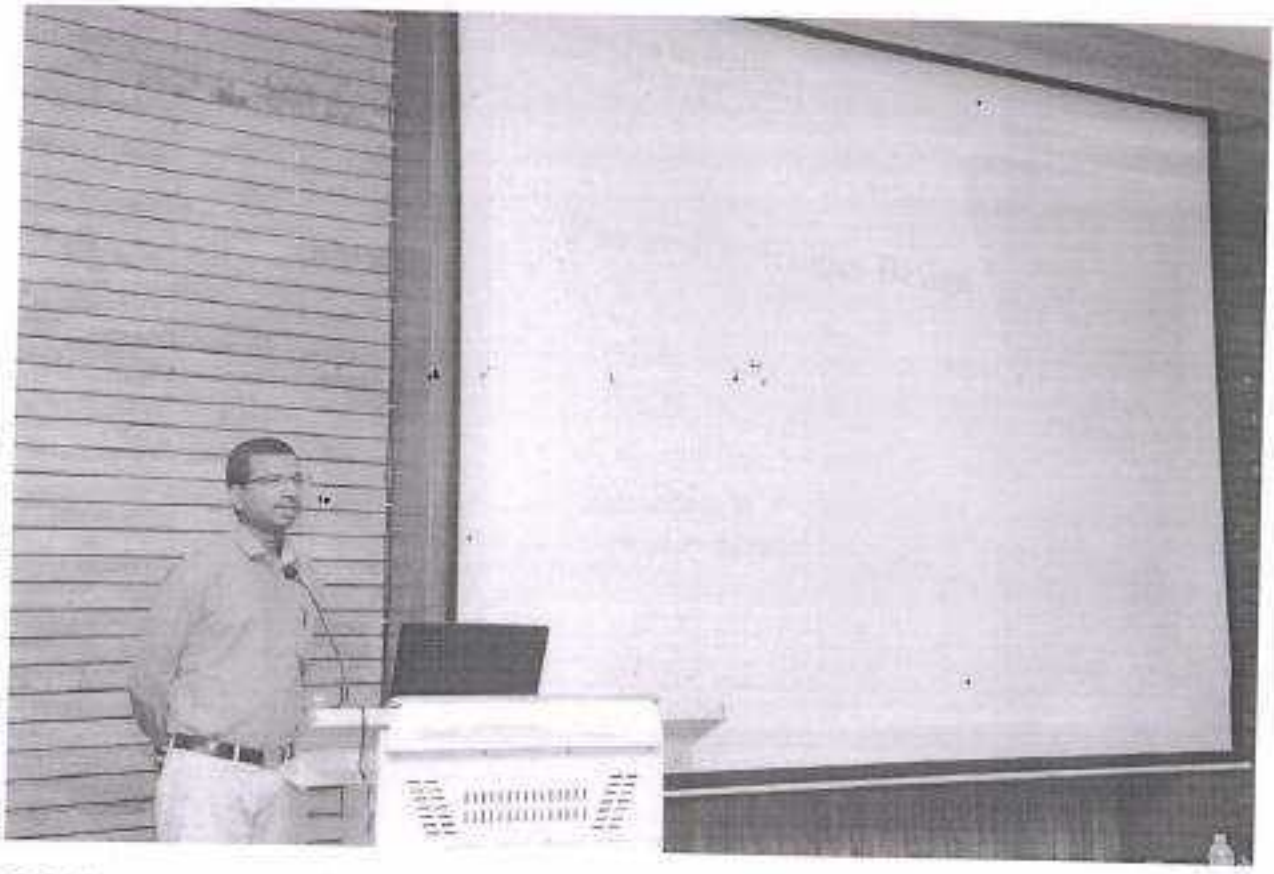
On Second day**(26.01.2019)**,Er.Malayappan and Er.Sreenivasalu handled the Second day all sessions onFuzzy Logic control and AI techniques in Electrical automation.

On Third day **(27.01.2019)**,Er.Sreenivasalu and Er.AravindhSwamy handled the third day all sessions on PLC's AB, ABB with Ladder logic programmingfor real time applications.

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

28/1/2019

Head of the Department
Department of Electrical and
Electronics Engineering
Navodaya Institute of Technology
RAICHUR-584 103, Karnataka



Er.Malayappan, Senior R&D Engineer, PANTECH Group of companies, Chennai, Delivering first session on Introduction to Power Electronics in Renewable Energy System.



Faculties of EEE and ECE attending the session on Introduction to Power Electronics in Renewable Energy System

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

29/1/2019

Head of the Department
Department of Electrical and
Electronics Engineering
Navodaya Institute of Technology
RAICHUR-584 103, Karnataka

On Fourth day **(28.01.2019)**Er.Malayappan and Er.Sreenivasalu handled the fourth day all sessions onPLC's OMRON, KEYENCE with Ladder logic programming.

On Fifth day**(29.01.2019)**Er.Malayappan and Er.Aravindh Swamy,handled the Sixth day all sessions on GE FANUC & SIEMENS,SCADA Wonder ware touch, Fix 32, Honeywell DCS.

All the sessions were very interactive. Participants gained lot of knowledge regarding the design of embedded system and its applications.

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

29.1.2019

Head of the Department
Department of Electrical and
Electronics Engineering
Navodaya Institute of Technology
RAICHUR-584 103, Karnataka

NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

Department of Electrical and Electronics Engineering

Faculty Development Programme (FDP) on

"The application of artificial intelligence in electrical automation control"

From 25th January to 29th January 2019

S.No.	Name of the Faculty	Designation & Dept.
1.	Dr. K. Venkatachalam	HOD & ECE
2.	Dr.M.Srinivasan	HOD& EEE
3.	MrsMadhuri Devi	Assistant Professor & ECE
4.	Mr. Rammurthy D	Assistant Professor & ECE
5.	Ms. VeenasreeHiremath	Assistant Professor & ECE
6.	Ms.PreetiPattankude	Assistant Professor & ECE
7.	Mr. Lakshmi Kanth	Assistant Professor & ECE
8.	Ms. GauraviShetty	Assistant Professor & ECE
9.	Mr.Mazumdar B.K	Associate Professor & EEE
10.	Mr.MadhuBabu	Assistant Professor & EEE
11.	Mr.A.Velu	Assistant Professor & EEE
12.	Ms.Geeta K. M	Assistant Professor & EEE
13.	Ms.SanaAnjum	Assistant Professor & EEE
14.	Mr. Kiran Kumar T.H	Assistant Professor & EEE

25/1/2019

HOD & Convener

Head of the Department
Department of Electrical and
Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103, Karnataka

NET's

NAVODAYA INSTITUTE OF TECHNOLOGY

(Affiliated to Visvesvaraya Technological University, Belgavi, approved by AICTE, New Delhi & recognized by Government of Karnataka)

Navodaya Nagar, Bejingera Road, Raichur-584103, Karnataka

Website: https://engineering.navodaya.edu.in/

Certificate of Participation

This is to certify that Mr./Ms./Mrs. MADHU BABU B

From NIT - RAICHUR

has participated in a five day Faculty Development Program on "The application of artificial intelligence in electrical automation control", organized by Department of Electrical and Electrical Engineering, Navodaya Institute of Technology, Raichur from 25th to 29th January 2019.

29/1/19
Coordinator

29/1/19
Convener/HoD-EEE

Principal

NET's

NAVODAYA INSTITUTE OF TECHNOLOGY

(Affiliated to Visvesvaraya Technological University, Belgavi, approved by AICTE, New Delhi & recognized by Government of Karnataka)
Navodaya Nagar, Bejingera Road, Raichur-584103, Karnataka
Website: https://engineering.novodaya.edu.in/

Certificate of Participation

This is to certify that Mr./Ms./Mrs. KIRAN KUMAR T.H

From NIT- RAICHUR

has participated in a five day Faculty Development Program on "The application of artificial intelligence in electrical automation control", organized by Department of Electrical and Electrical Engineering, Navodaya Institute of Technology, Raichur from 25th to 29th January 2019.

29/1/19
Coordinator

29/1/19
Convener/HoD-EEE

Principal

NAVODAYA INSTITUTE OF TECHNOLOGY

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Evaluation Form

FDP Title: The application of AI in Electrical automation control

Date: 25.01.19 to 29.01.19 Time: 9.30 Am to 5.30 pm

Please complete the following evaluation for the FDP you attended. Your feedback will help us evaluate the effectiveness of this program and allow us to make improvements in the future program. **Your feedback is important for planning future programming and services we offer.**

Thank you.

4=Strongly Agree 3=Agree 2=Disagree 1=Strongly Disagree

	Circle your response			
1. The course met my expectations.	4	(3)	2	1
2. The content was helpful.	(4)	3	2	1
3. The level of the course was appropriate.	4	(3)	2	1
5. The speaker had a good understanding of the topics.	(4)	3	2	1
6. The handouts were helpful.	4	(3)	2	1
7. The course was worth my time.	(4)	3	2	1
8. The length of the course was appropriate.	4	(3)	2	1
9. I recommend that the course be repeated for other students.	(4)	3	2	1

What were the most useful aspects of the course?

AI Part with simulation were helpful.

What changes should be made to enhance/improve this program?

Additional comments: NIL

Name of the Faculty: Kiran Kumar T-H

Signature 29.1.19.

NAVODAYA INSTITUTE OF TECHNOLOGY

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Evaluation Form

FDP Title: The application of Artificial Intelligence in Electric automation control

Date: 25.01.2019 to 29.01.2019 Time: 9:30 AM to 5:30 pm

Please complete the following evaluation for the FDP you attended. Your feedback will help us evaluate the effectiveness of this program and allow us to make improvements in the future program. **Your feedback is important for planning future programming and services we offer.**

Thank you.

4=Strongly Agree 3=Agree 2=Disagree 1=Strongly Disagree

	Circle your response			
1. The course met my expectations.	(4)	3	2	1
2. The content was helpful.	4	(3)	2	1
3. The level of the course was appropriate.	(4)	3	2	1
5. The speaker had a good understanding of the topics.	4	(3)	2	1
6. The handouts were helpful.	(4)	3	2	1
7. The course was worth my time.	(4)	3	2	1
8. The length of the course was appropriate.	(4)	3	2	1
9. I recommend that the course be repeated for other students.	4	(3)	2	1

What were the most useful aspects of the course?

All the sessions was useful.

What changes should be made to enhance/improve this program?

No

Additional comments: Thank you for organizing this event.

Name of the Faculty: VELU.A

Signature 29/1/2019.

NET GROUP OF EDUCATIONAL INSTITUTIONS: RAICHUR
NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

~~GUEST LECTURE/SEMINR/ADD-ON COURSE/~~ *Faculty Development Programme/* ~~WORKSHOP /CONFERENCE/SHORT TERM TRAINING PROGRAMME/ANY OTHER PROPOSAL~~

Date: 20.01.2020

1	**ACADEMIC YEAR**	2019-20
2	**TYPE OF THE PROGRAMME PROPOSED**	*Faculty Development Programme*
3	**TITLE OF THE PROGRAMME:**	**"RESEARCH METHODOLOGY TOOLS AND ITS APPLICATION TECHNIQUES"**
4	**BROAD OBJECTIVE OF THE PROGRAMME:**	The main objectives of the FDP are to enhance the **Qualitative methods**- use data collection methodologies such as interviewing, observation, discussions and review of documents (e.g. diaries, historical documents). **Quantitative methods** involve the collection and analysis of objective data, often in numerical form. The research design is determined prior to the start of data collection and is not flexible. The research process, interventions and data collection tools (e.g. questionnaires) are standardized to minimize or control possible bias. All study instruments (quantitative and qualitative) should be pre-tested to check the validity and reliability of data collection tools. **Pre-testing** allows the research team to check whether the research instructions and questions are clear, context specific, and that adequate time has been allowed to administer the questionnaire, etc.
5	**NAME OF THE INSTITUTION**	NETs NAVODAYA INSTITUTE OF TECHNOLOGY
6	**NAME OF THE DEPARTMENT(S)**	At Institution Level
7	**BENEFICIARY**	Faculty of NIT
8	**DATEOF THE PROGRAMME.**	06.02.2020 to 10.02.2020
9	**ELIGIBILITY CRITERIA SET:**	Faculties having less than 8 years of experience.
10	**NUMBER OF DAYS REQUIRED TO ORGANISE THE PROGRAMME**	Five day FDP
11	**PROPOSED DURATION OF THE PROGRAMME**	30 hours
12	**TOTAL NO. OF PERIODS REQUIRED FOR THE PROGRAMME**	30 Periods
13	**NO. OF PERIODS REQUIRED PER DAY**	06 Periods

14	**LOCATION DETAILS:**	NETs NIT Auditorium
15	**MINIMUM NO OF PARTICIPANTS:**	50-60 permitted
16	**DETAILS OF SPONSORERS:**	NET Trust
17	**DETAILS OF RESOURCE PERSONS:**	**Dr.M.Srinivasan** **HOD/EEE** **NIT-Raichur** **Dr.Venkatachalam** **HOD/ECE** **NIT-Raichur**
18	**REGISTRATION FEE WITH CERTIFICATION:**	NIL
19	**PROGRAMME PROPOSED BY:**	Dept. of EEE
20	**FACULTY COORDINATOR**	Mr.Daniel N
21	**BUDGET FOR THE PROGRAMME:**	**Rs:10,000/-** **(Ten Thousand Rupees only)**

This is to the Honorable Chairman of NIT for kind approval:

PRINCIPAL

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Head of the Department
Department of Electrical and
Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103, Karnataka

EVENT COORDINATOR

APPROVED/NOTAPPROVED

NAVODAYA TECHNOLOGY * RAICHUR

NAVODAYA INSTITUTE OF TECHNOLOGY

Tel : +91 8532-200115, 240334, Fax : +91-8532-240054
E-Mail :hodeee.nit@navodaya.edu.in

CIRCULAR

Chief Patron
Shri. S. R. Reddy, Hon'ble Chairman,
Navodaya Education Trust (NET®),
Raichur.

Patron
Shri. N. Bharath Reddy., Hon'ble Trustee,
Navodaya Education Trust (NET®),
Raichur.

Chief Convener
Dr.M.V.Mallikarjuna,
Principal,NIT, Raichur

Convener
Dr.M.Srinivasan,
HOD/ EEE, NIT, Raichur

Coordinator
Prof.Daniel N,EEE, NIT
E-Mail: Daniel.nixon@navodaya.edu.in
Contact no. : +91 – 9986247085

Co-Coordinator
Prof.Velu A,EEE, NIT
E-Mail: velu.a@navodaya.edu.in
Contact no. : +91 - 9591330675

Organizing Committee
1. Prof.Mazumdar B.K
2. Prof.Geeta K.M
3. Prof.Sathishkumar K.S
4. Prof.Madhubabu B
5. Prof.Sana Anjum
6. Prof.Kiran Kumar

27.01.2020

Respected Sir / Madam,
Greetings from Navodaya Institute of Technology, Raichur…

The department of Electrical & Electronics Engineering is organizing Five day FDP on **"RESEARCH METHODOLOGY TOOLS AND ITS APPLICATION TECHNIQUES"** The attached invitation may kindly be circulated among the faculty members / colleagues. The main objectives of the FDP are to enhance the Qualitative methods- use data collection methodologies such as interviewing, observation, discussions and review of documents (e.g. diaries, historical documents). Quantitative methods involve the collection and analysis of objective data, often in numerical form. The research design is determined prior to the start of data collection and is not flexible. The research process, interventions and data collection tools (e.g. questionnaires) are standardized to minimize or control possible bias. All study instruments (quantitative and qualitative) should be pre-tested to check the validity and reliability of data collection tools. Pre-testing allows the research team to check whether the research instructions and questions are clear, context specific, and that adequate time has been allowed to administer the questionnaire, etc.

We are pleased to inform that the department of Electrical and Electronics Engineering, Navodaya Institute of Technology is organizing a Faculty Development Programme during 06th February-10th February 2020.

In this context, we look forward your support and request you to encourage participation of professionals by circulating the attached invitation.

Thanking you,
Yours sincerely,

27/1/2020

Dr.M.Srinivasan
Convener

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

NAVODAYA INSTITUTE OF TECHNOLOGY

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Invitation

Five day FDP

On

"RESEARCH METHODOLOGY TOOLS AND ITS "APPLICATION TECHNIQUES"

BY

Dr.M.Srinivasan

HOD/EEE

NIT-Raichur

Dr.Venkatachalam

HOD/ECE

NIT-Raichur

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

On

06.02.2020 to 10.02.2020

Venue: NIT - Auditorium

NAVODAYA INSTITUTE OF TECHNOLOGY

Faculty development programme on

RESEARCH METHODOLOGY TOOLS AND ITS APPLICATION TECHNIQUES

Schedule on **01.02.2020 to 05.02.2020**

Day	*Session-1 (09.30 am to 11.00am)*			*Session-2 (11.15 am to 12.45pm)*		*Session-2 (2.00 pm to 3.30pm)*		*Session-3 (3.45pm to 5.15pm)*
Thursday (06.02.2020)	*Registration 09.00 to 10.00*	*Inauguration and Key note address by* Dr.M.Srinivasan HOD/EEE NIT-Raichur (Foundation on Research)	*Tea Break (11.00am To 11.15 am)*	Dr.Venkatachalam HOD/ECE NIT-Raichur (Literature survey & Problem identification)	*Lunch break (12.45pm to 02.00pm)*	Dr.M.Srinivasan HOD/EEE NIT-Raichur Framing Research objectives	*Tea Break (03.30pm To 03.45 pm)*	Dr.Venkatachalam HOD/ECE NIT-Raichur Ways to Collection of data and analyze he data
Friday (07.02.2020)	Dr.M.Srinivasan HOD/EEE NIT-Raichur (Research Designs)			Dr.Venkatachalam HOD/ECE NIT-Raichur (Research Methods)		Dr.M.Srinivasan HOD/EEE NIT-Raichur (Plagiarism- useful link for research scholars)		Dr.Venkatachalam HOD/ECE NIT-Raichur (Ways to write Review papers)

27/1/2020

Head of the Department
Department of Electrical and Electronics Engineering
Navodaya Institute of Technology
RAICHUR-584 103, Karnataka

Day	Session-1 (09.30 am to 11.00am)		Session-2 (11.15 am to 12.45pm)		Session-2 (2.00 pm to 3.30pm)		Session-3 (3.45pm to 5.15pm)
Saturday (08.02.2020)	Dr.Venkatachalam HOD/ECE (Overview on LATEX-Important tools for research.)	Tea Break (11.00am To 11.15 am)	Dr.M.Srinivasan HOD/EEE NIT-Raichur (Concept on Intellectual property rights)	Lunch break (12.45pm to 02.00pm)	Dr.Venkatachalam HOD/ECE NIT-Raichur (Effective ways to write research articles)	Tea Break (03.30pm To 03.45 pm)	Dr.M.Srinivasan HOD/EEE NIT-Raichur (Do and Don'ts while submitting research papers)
Sunday (09.02.2020)	Dr.M.Srinivasan HOD/EEE NIT-Raichur (Guidelines on Ph.d thesis writing)		Dr.Venkatachalam HOD/ECE NIT-Raichur (Selection of Objectives and Formulation of Hypothesis)		Dr.M.Srinivasan HOD/EEE NIT-Raichur (Research Design, Qualitative)		Dr.Venkatachalam HOD/ECE NIT-Raichur (Quantitative and Mixed Methods)

27/1/2020

Head of the Department
Department of Electrical and
Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103. Karnataka

Day	Session-1 (09.30 am to 11.00am)		Session-2 (11.15 am to 12.45pm)		Session-2 (2.00 pm to 3.30pm)		Session-3 (3.45pm to 5.15pm)
Monday (10.02.2020)	Dr.Venkatachalam HOD/ECE NIT-Raichur (Modern Tools and Techniques of Data Collection)	Tea Break (11.00am To 11.15 am)	Dr.M.Srinivasan HOD/EEE NIT-Raichur (Content Analysis and Effective Research Report)	Lunch break (12.45pm to 02.00pm)	Dr.Venkatachalam HOD/ECE NIT-Raichur (Writing and Refgrencing)	Tea Break (03.30pm To 03.45 pm)	Dr.M.Srinivasan HOD/EEE NIT-Raichur (Pre-testing)

27.1.2020

Head of the Department
Department of Electrical and
Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103, Karnataka

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

NET'S

NAVODAYA INSTITUTE OF TECHNOLOGY, RAICHUR

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

SUMMARY OF FACULTY DEVELOPMENT PROGRAMME ON

"RESEARCH METHODOLOGY TOOLS AND ITS APPLICATION TECHNIQUES"

On 06th February to 10th February 2020, Department of EEE has conducted a FDP on "RESEARCH METHODOLOGY TOOLS AND ITS APPLICATION TECHNIQUES", with a view to provide the essential details about Research activities. Around 60 internal participants attended the FDP.

Dr.Srinivasan M, Head, Department of EEE, welcomed the gathering.**Dr.Mallikarjuna M.V**, Principal gave a presidential address.

On First day **(06.02.2020)**,Dr.Srinivasan M and Dr.Venkatachalam handled the first day forenoon sessions onFoundation on Research and (Literature survey & Problem identification).

Handled the Afternoon session - Framing Research objectives and Ways to Collection of data and analyze the data.

On Second day (07.02.2020),Dr.Srinivasan M and Dr.Venkatachalam, handled the Second day sessions on(Research Designs) .(Research Methods) (Plagiarism- useful link for research scholars) (Ways to write Review papers)

On Third day (08.02.2020),Dr.Srinivasan M and Dr.Venkatachalam, handled the Third day sessions on(Overview on LATEX-Important tools for research.) (Concept on Intellectual property rights) (Effective ways to write research articles) (Do and Don'ts while submitting research papers)

On Fourth day (09.02.2020) Dr.Srinivasan M and Dr.Venkatachalam, handled the Fourth day sessions on**(Guidelines on Ph.d thesis writing) (Selection of Objectives and Formulation of Hypothesis) (Research Design, Qualitative) (Quantitative and Mixed Methods)**

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

10/2/2020

Head of the Department
Department of Electrical and
Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103, Karnataka



Dr.Srinivasan M, Handled the session on Foundation on Research to the faculty members of NIT, Raichur



Dr.Venkatachelam, Handled the session on Modern Tools and Techniques of Data Collection Content Analysis to the faculty members of NIT Raichur

On Fifth day (10.02.2020) Dr.Srinivasan M and Dr.Venkatachalam, handled the Fifth day sessions on **Modern Tools and Techniques of Data Collection Content Analysis and Effective Research Report, Writing and Referencing, Pre-testing.**

All the sessions were very interactive. Participants gained lot of knowledge regarding the design of embedded system and its applications.

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Head of the Department
Department of Electrical and
Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103, Karnataka

27.01.2020

From
Dr.M.Srinivasan,
Professor and Head,
Navodaya Institute of Technology,
Raichur-584103.

To
The Principal,
Navodaya Institute of Technology,
Raichur-584103.

Sir

Subject: Req. for Auditorium hall to conduct FDP - Reg

We have proposed to conduct the FDPon the topic **"RESEARCH METHODOLOGY TOOLS AND ITS APPLICATION TECHNIQUES"** on 06.02.2020 and 10.02.2020. For this Purpose, we need auditorium on 06.02.2020-Time-09.00AM-5.30PM.

Thanking You

Yours faithfully

27/1/2020

[Dr.M.Srinivasan]

Head of the Department
Department of Electrical and Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103, Karnataka

27/1/20

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

NET's

NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

Department of Electrical and Electronics Engineering

Faculty Development Programme (FDP) on

"RESEARCH METHODOLOGY TOOLS AND ITS APPLICATION TECHNIQUES"

From 06th February to 10th February 2020

S.No.	Name of the Faculty	Designation & Dept.
1	Mr. Md Adil	Assistant Professor & Civil
2	Mr. Ashish P Desai	Assistant Professor & Civil
3	Mr. Raj mohan	Assistant Professor & Civil
4	Ms. Swetha Patil	Assistant Professor & Civil
5	Mr. Vijay G	Assistant Professor & Civil
6	Mr. Nagesh Sugandi	Assistant Professor & Civil
7	Mr. Bharath Koner	Assistant Professor & Civil
8	Mr. Santhosh Kumar	Assistant Professor & Civil
9	Dr. Ratna Kumar P	HOD &Mech
10	Dr. Ananthachar	Professor & Mech
11	Mr. Md. Umar	Assistant Professor &Mech
12	Mr. SrikarKulkarni	Assistant Professor &Mech
13	Mr. Ravi Kulkarni	Assistant Professor &Mech
14	Mr. Imran Basha	Assistant Professor &Mech
15	Mr. Rajashekarappa	Assistant Professor &Mech
16	Mr. FaheemAkthar	Assistant Professor &Mech
17	Mr. Mallikarjuna G	Assistant Professor &Mech
18	Mr. VinayakManur	Assistant Professor &Mech
19	Mr. Pradeep Ilay	Assistant Professor &Mech
20	Mr. Zeeshan Ali	Assistant Professor &Mech
21	Mr. Priyankar D	Assistant Professor &Mech

22	Mr. B K Mazumdar	Associate Professor & EEE
23	Ms. Geeta K M	Assistant Professor & EEE
24	Mr. Daniel Nixon	Assistant Professor & EEE
25	Mr. Velu	Assistant Professor & EEE
26	Mr. Madhubabu	Assistant Professor & EEE
27	Ms. Sana Anjum	Assistant Professor & EEE
28	Mr. Kirankumar	Assistant Professor & EEE
29	Mr. RammurthyDasari	Assistant Professor & ECE
30	Ms. Channaveeramma R	Assistant Professor & ECE
31	Mr. Sachinkumar	Assistant Professor & ECE
32	Mr. Md. Shoibuddin madni	Assistant Professor & ECE
33	Ms. Gauravi Shetty	Assistant Professor & ECE
34	Ms. Preeti Pattankude	Assistant Professor & ECE
35	Dr. M N Faruk	HOD & CSE
36	Mr. ChetanGudi	Assistant Professor & CSE
37	Mr. Shiva Kumar Reddy	Assistant Professor & CSE
38	Ms. Shanthi E	Assistant Professor & CSE
39	Mr. Vijay Kumar Yadav	Assistant Professor & CSE
40	Mr. Hanumesh D	Assistant Professor & CSE
41	Ms. Jayashree	Assistant Professor & CSE
42	Mrs. SupriyaPurohit	Assistant Professor & CSE
43	Ms. ShaziyaAnjum	Assistant Professor & CSE
44	Mrs. Vishaka M	Assistant Professor & CSE
45	Dr. Suman Kumar	HOD & Humanities and sciences

46	Dr. Savitramma G	Associate professor &Maths
47	Dr. Senthil Kumar	Associate professor & Maths
48	Mr. Nagesh Babu Reddy	Assistant professor & Maths
49	Dr. SrikanthSetty	Assistant professor & Maths
50	Ms. Shalini K	Assistant professor & Physics
51	Mr. Gururaj	Assistant professor & Chemistry
52	Mr. SrikanthKulkarni	Assistant professor & Chemistry
53	Mr. Shekar Arkasali	Visiting Faculty & English
54	Ms. Rashmi Dasbhat	Assistant professor & Maths

10/12/2020

HOD & Convener

Head of the Department
Department of Electrical and Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103, Karnataka

NAVODAYA INSTITUTE OF TECHNOLOGY

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Evaluation Form

FDP Title: Research Methodology Tools and its Application Techniques

Date: 6-2-20 to 10-2-2020 Time: 9.30 AM to 5.30 PM

Please complete the following evaluation for the FDP you attended. Your feedback will help us evaluate the effectiveness of this program and allow us to make improvements in the future program. **Your feedback is important for planning future programming and services we offer.**

Thank you.

4=Strongly Agree 3=Agree 2=Disagree 1=Strongly Disagree

	Circle your response			
1. The course met my expectations.	(4)	3	2	1
2. The content was helpful.	4	(3)	2	1
3. The level of the course was appropriate.	(4)	3	2	1
5. The speaker had a good understanding of the topics.	4	(3)	2	1
6. The handouts were helpful.	(4)	3	2	1
7. The course was worth my time.	4	(3)	2	1
8. The length of the course was appropriate.	(4)	3	2	1
9. I recommend that the course be repeated for other students.	4	(3)	2	1

What were the most useful aspects of the course?

All the sessions were useful.

What changes should be made to enhance/improve this program?

NO

Additional comments: Thanks for management and organizers

Name ofthe Faculty: VELU.A/EEE

Signature 10/2/20

NAVODAYA INSTITUTE OF TECHNOLOGY

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Evaluation Form

FDP Title: Research methodology tools and its application

Date: 6.2.2020 to 10.2.2020 Time: 9:30 AM to 5:10 PM

Please complete the following evaluation for the FDP you attended. Your feedback will help us evaluate the effectiveness of this program and allow us to make improvements in the future program. **Your feedback is important for planning future programming and services we offer.**

Thank you.

4=Strongly Agree 3=Agree 2=Disagree 1=Strongly Disagree

	Circle your response			
1. The course met my expectations.	4	(3)	2	1
2. The content was helpful.	4	(3)	2	1
3. The level of the course was appropriate.	(4)	3	2	1
5. The speaker had a good understanding of the topics.	4	(3)	2	1
6. The handouts were helpful.	(4)	3	2	1
7. The course was worth my time.	4	(3)	2	1
8. The length of the course was appropriate.	(4)	3	2	1
9. I recommend that the course be repeated for other students.	4	(3)	2	1

What were the most useful aspects of the course?

All the sessions were good.

What changes should be made to enhance/improve this program?

—

Additional comments: NIL

Name of the Faculty: Geetha K M

Signature 10/2/2020

NET's

NAVODAYA INSTITUTE OF TECHNOLOGY

(Affiliated to Visveswaraya Technological University, Belgavi, approved by AICTE, New Delhi &recognized by Government of Karnataka)

Navodaya Nagar, Bejingera Road, Raichur-584103, Karnataka

FACULTY DEVELOPMENT PROGRAM

ON

"RESEARCH METHODOLOGY TOOLS AND ITS APPLICATION TECHNIQUES"

Certificate of Participation

This is to certify that Mr. /Ms./Mrs. GEETA K M

From NIT- RAICHUR

has participated in a five day Faculty Development Program on "RESEARCH METHODOLOGY TOOLS AND ITS APPLICATION TECHNIQUES", organized by Department of Electrical and Electronics Engineering, Navodaya Institute of Technology, Raichur(Karnataka)from 06th to 10th February 2020.

Coordinator	*Convener/HoD-EEE*	*Principal*

NET's

NAVODAYA INSTITUTE OF TECHNOLOGY

(Affiliated to Visveswaraya Technological University, Belgavi, approved by AICTE, New Delhi &recognized by Government of Karnataka)

Navodaya Nagar, Bejingera Road, Raichur-584103, Karnataka

FACULTY DEVELOPMENT PROGRAM

ON

"RESEARCH METHODOLOGY TOOLS AND ITS APPLICATION TECHNIQUES"

Certificate of Participation

This is to certify that Mr. /Ms./Mrs. VELU.A

From NIT-RAICHUR

has participated in a five day Faculty Development Program on "RESEARCH METHODOLOGY TOOLS AND ITS APPLICATION TECHNIQUES", organized by Department of Electrical and Electronics Engineering, Navodaya Institute of Technology, Raichur(Karnataka)from 06th to 10th February 2020.

10/2/20 — *Coordinator*

10/2/2020 — *Convener/HoD-EEE*

10/2/20 — *Principal*

NET GROUP OF EDUCATIONAL INSTITUTIONS: RAICHUR
NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

GUEST LECTURE/~~SEMINAR/ADD ON COURSE/WORKSHOP /CONFERENCE/SHORT TERM TRAINING PROGRAMME/ANY OTHER PROPOSAL~~

Date:16.09.2019

1	**ACADEMIC YEAR**	2019-20
2	**TYPE OF THE PROGRAMME PROPOSED**	GUEST LECTURE
3	**TITLE OF THE PROGRAMME:**	AI applications to power system protection
4	**BROAD OBJECTIVE OF THE PROGRAMME:**	Areas of **application of Artificial Intelligence(AI)** in power systems are planning of power system like generation expansion planning, power system reliability, transmission expansion planning, reactive power planning, control of power system like voltage control, stability control, power flow control, load frequency control. Recently, due to concerns about the liberalization of electricity supply, deregulation and global impact on the environment, securing a reliable power supply has become an important social need worldwide. To ensure this need is fulfilled, detailed investigations and developments are in progress on power distribution systems and the monitoring of apparatus.
5	**NAME OF THE INSTITUTION**	NETs NAVODAYA INSTITUTE OF TECHNOLOGY
6	**NAME OF THE DEPARTMENT(S)**	ELECTRICAL AND ELECTRONICS ENGINEERING
7	**BENEFICIARY**	Program: B.E YEAR: III/IV SEM: V/VII
8	**DATE/DURATION OF SUCH PROGRAMMES CONDUCTED IF ANY IN THIS ACADEMIC YEAR:**	No
9	**ELIGIBILITY CRITERIA SET:**	Faculty members, Research scholars & B.Tech/B.E Students.
10	**NUMBER OF DAYS REQUIRED TO ORGANISE THE PROGRAMME**	One Day
11	**PROPOSED DURATION OF THE PROGRAMME**	30.09.2019
12	**TOTAL NO. OF PERIODS REQUIRED FOR THE PROGRAMME**	3 Periods

13	**NO. OF PERIODS REQUIRED PER DAY**	3 Periods
14	**LOCATION DETAILS:**	NETs NIT Auditorium
15	**MINIMUM NO OF PARTICIPANTS:**	70
16	**DETAILS OF SPONSORERS:**	-
17	**DETAILS OF RESOURCE PERSONS:**	Dr.Ravikumar Bhimasingu. Associate Professor, Department of Electrical Engineering, **Indian Institute of Technology (IIT) Hyderabad, Kandi,** Sangareddy - 502285. Telangana, India.
18	**REGISTRATION FEE WITH CERTIFICATION:**	Rs __- per participant
19	**REGISTRATION FEE WITHOUT CERTIFICATION:**	Rs -___per participant.
20	**EVENT ORGANISERS (STUDENTS):**	1.Padmaja , Final Year EEE 2. Md. Irfan Khan, Final Year EEE
21	**FACULTY COORDINATOR:**	1.Dr.M.Srinivasan Professor & Head, NETS NIT, Raichur. 2. Mr.Velu.A. Assistant Professor, NETS NIT, Raichur.
22	**PROGRAMME PROPOSED BY:**	Dept. ofElectrical and Electronics Engineering
23	**ANY OTHER RELEVANT MATTER:**	Nil
24	**ESTIMATED BUDGET IN RS:**	Rs.15,000/-

This is to the Honorable CHAIRMAN for kind approval:

PRINCIPAL

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

HOD

Head of the Department
Department of Electrical and
Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103, Karnataka

EVENT COORDINATOR

APPROVED/NOTAPPROVED

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

Gmail Dr.Srinivasan M <srinimeha@gmail.com>

question of Guest Lecture - Reg

iessages

, **Vasan** <srinimeha@gmail.com> 9 August 2018 at 15:
avikumar@iith.ac.in

ear Sir,

e are pleased to inform you that Navodaya Institute of Technology, Raichur is a renowned technical institution offering five UG
id Two PG programmes.

'e planned to conduct a one day Guest Lecture on "AI techniques applications for power systems" in any one Saturday of
ugust/September month or suggest one date in September 2018 in our college. I request you to make favourable reply
, hodeee.nit@navodaya.edu.in / srinimeha@gmail.com.

lso for your information, will hire car for transport from Hyderabad to raichur. Journey time maximum 3.5 hours.

.Srinivasan
lobile: +91 72592 22765, +91 9442263511
lail Id: srinimeha@gmail.com

ncl.: Request Letter

Guest Lecture 2018-19.jpg
205K

vikumar Bhimasingu <ravikumar@iith.ac.in> 10 August 2018 at 10:
: "M.S. Vasan" <srinimeha@gmail.com>, hodeee.nit@navodaya.edu.in

Dear Dr. M.Srinivasan,
Thank you for the invitation to deliver a guest lecture at your Institution.
Please consider this e-mail as my acceptance to deliver a lecture.

I will let you know the exact date of the lecture in due course.

Thanks and regards,
Ravikumar B.
==========
[Quoted text hidden]
--
==========

Dr. Ravikumar Bhimasingu
Associate Professor
Department of Electrical Engineering
Indian Institute of Technology Hyderabad (IITH)
Kandi (Village), Sangareddy (Mandal), Medak(District)
Telangana, INDIA.
Pin Code: 502285
Office Room No: "C-409"
Phone: +91-9448848013 (Mobile)
Website: http://www.iith.ac.in/~ravikumar/

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

==========

: "M.S. Vasan" <srinimeha@gmail.com>

Dear Dr. Srinivas,
Thank you for your invitation to deliver a guest lecture.
I'll visit your institute and deliver a lecture on AI applications to power system protection improvements (tentatively). I will inform you about my the date of visit by the end of this week..

Thanks and regards,
Ravikumar B.
==========
Quoted text hidden]

---

vikumar Bhimasingu <ravikumar@iith.ac.in> 10 September 2019 at 10:
Srinivas m <hodeee.nit@navodaya.edu.in>
: "M.S. Vasan" <srinimeha@gmail.com>

Dear Dr Srinivas,
There is delay in informing you about my visit to your institute.
Can we have the guest lecture on
28-09-2019 (Saturday)??
Otherwise, let me know if you need any other date of our mutual convenience..

Thanks and Regards,
tavikumar B.
==========
Quoted text hidden]

Srinivasan M <srinimeha@gmail.com> 10 September 2019 at 10:
Ravikumar Bhimasingu <ravikumar@iith.ac.in>
Srinivas m <hodeee.nit@navodaya.edu.in>

ear sir
n 28.09.2019 mahalaya amavasya college may be leave . Tomorrow i will conform and possible dates. Thank you sir
uoted text hidden]

---

ikumar Bhimasingu <ravikumar@iith.ac.in> 10 September 2019 at 10:
"M.S. Vasan" <srinimeha@gmail.com>
Srinivas m <hodeee.nit@navodaya.edu.in>

ure Dr Srinivas..
hank you..
egards,
avikumar B.
========
uoted text hidden]



Dear sir
Thanking you for accepting the invitation to deliver a guest lecture.
Further i am waiting for your mail to update and fixing the date.

Thanks and regards
Dr.M.Srinivasan
[Quoted text hidden]

Srinivas m <hodeee.nit@navodaya.edu.in> Mon, Sep 2, 2019 at 7:02 PM
To: Ravikumar Bhimasingu <ravikumar@iith.ac.in>
Cc: "M.S. Vasan" <srinimeha@gmail.com>

Dear Sir,
Good Evening.
I am pleased to remind, last time we invited for guest lecture from our institution. But we unable to conduct guest lecture due to examination schedule.This time i assure that surely not happen.
I kindly request you to accept the guest lecture. Title and date is your wish.

Thanking you,

Dr.M.SRINIVASAN
HoD/EEE
Navodaya institute of Technology
RAICHUR.

[Quoted text hidden]

Ravikumar Bhimasingu <ravikumar@iith.ac.in> Tue, Sep 3, 2019 at 12:08 AM
To: Srinivas m <hodeee.nit@navodaya.edu.in>
Cc: "M.S. Vasan" <srinimeha@gmail.com>

Dear Dr. Srinivas,
Thank you for your invitation to deliver a guest lecture.
I'll visit your institute and deliver a lecture on AI applications to power system protection improvements (tentatively). I will inform you about my the date of visit by the end of this week..

Thanks and regards,
Ravikumar B.
==========
[Quoted text hidden]

Ravikumar Bhimasingu <ravikumar@iith.ac.in> Tue, Sep 10, 2019 at 10:24 AM
To: Srinivas m <hodeee.nit@navodaya.edu.in>
Cc: "M.S. Vasan" <srinimeha@gmail.com>

Dear Dr Srinivas,
There is delay in informing you about my visit to your institute.
Can we have the guest lecture on
28-09-2019 (Saturday)??
Otherwise, let me know if you need any other date of our mutual convenience..

Thanks and Regards,
Ravikumar B.
==========
[Quoted text hidden]

NAVODAYA INSTITUTE OF TECHNOLOGY * RAICHUR

Dr.Srinivasan M <srinimeha@gmail.com> Tue, Sep 10, 2019 at 10:33 AM
To: Ravikumar Bhimasingu <ravikumar@iith.ac.in>
Cc: Srinivas m <hodeee.nit@navodaya.edu.in>

Dear sir
On 28.09.2019 mahalaya amavasya college may be leave . Tomorrow i will conform and possible dates.
Thank you sir
[Quoted text hidden]

Ravikumar Bhimasingu <ravikumar@iith.ac.in> Tue, Sep 10, 2019 at 10:45 AM
To: "M.S. Vasan" <srinimeha@gmail.com>
Cc: Srinivas m <hodeee.nit@navodaya.edu.in>

Sure Dr Srinivas..
Thank you..
Regards,
Ravikumar B.
==========
[Quoted text hidden]

Srinivas m <hodeee.nit@navodaya.edu.in> Wed, Sep 11, 2019 at 3:58 PM
To: Ravikumar Bhimasingu <ravikumar@iith.ac.in>

Dear sir,
In this month 21.09.2019(Saturday) , Next month Saturdays 05.10.2019, 12.10.2019, 19.10.2019 are working days.These are the possible dates or any other working days except the following dates:19.09.2019 , 20.09.2019, 26.09.2019 and 27.09.2019. Let me know your convenient date. Thank you sir.
Regards
Dr.M.Srinivasan
[Quoted text hidden]

Ravikumar Bhimasingu <ravikumar@iith.ac.in> Sun, Sep 15, 2019 at 10:03 PM
To: Srinivas m <hodeee.nit@navodaya.edu.in>

Dear Dr. Srinivas,
Can we have it on 30th September 2019 (Monday)? If the date is fine with you, then let us fix the date.

Thanks and regards,
Ravikumar B.
==========
[Quoted text hidden]

Srinivas m <hodeee.nit@navodaya.edu.in> Mon, Sep 16, 2019 at 5:42 PM
To: Ravikumar Bhimasingu <ravikumar@iith.ac.in>

Dear sir
Today i discussed with principal sir, on 30.09.2019 confirmed the guest lecture on the title AI applications to power system protection.
Sir, pl. mention the mode of the transport.
Shall i book in the train?

Thank you sir

Dr.M.Srinivasan



[Quoted text hidden]

NAVODAYA EDUCATION TRUST

NAVODAYA INSTITUTE OF TECHNOLOGY, RAICHUR

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

NAVODAYA

Invitation Letter

Guest lecture on

"AI APPLICATIONS TO POWER SYSTEM PROTECTION"

BY

Dr. RAVIKUMAR BHIMASINGU
Associate Professor,
Department of Electrical Engineering,
IIT Hyderabad, Kandi, TS.

On

30th September 2019

at 10.00 AM

Venue: NIT, AUDITORIUM

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

19.09.2019

From
Dr.M.Srinivasan,
Professor and Head,
Navodaya Institute of Technology,
Raichur-584103

To
The Principal,
Navodaya Institute of Technology,
Raichur-584103

Sir

Subject: Req. for seminar Hall to conductthe Guest Lecture - Reg

We have proposed to conduct the Guest Lecture on the topic "AI applications to power system protection" on 30.09.2019. For this Purpose, we need Seminar Hall onSeptember30th, 2019(Monday), Time-10.00AM-1.30PM.

Thanking You

To Mr ... 19/9

Yours faithfully

19/9/19

[Dr.M.SRINIVASAN]

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR



Ravikumar Bhimasingu, Ph.D.

Associate Professor,
Department of Electrical Engineering,
Indian Institute of Technology Hyderabad, India.

Research Areas

- Computer-aided power system analysis and modeling
- Integration of Renewable Energy Sources
- AI techniques applications for power systems security improvement
- Power System protection and optimization
- Distribution system automation
- Wide Area Monitoring, Protection and Control
- Protection and Control of Micro Grids

Courses Taught

- Power System Protection (3 Credits)
- Steady State Power System Analysis (3 Credits)
- Electromechanical Energy Conversion (3 Credits)
- Power Electronics (1 Credit)
- Electrical Machines Lab (2 Credits)

Professional Activities

Reviewer for

- IEEE Transactions on Power Delivery
- IEEE Transactions on Power Systems
- IEEE Transactions on Power Electronics
- IEEE Transactions of Smart Grids
- Electric Power Components and Systems
- IET Generation Transmission and Distribution Journal
- Ain Shams Engineering Journal (ELSEVIER) and
- various reputed National and International Conferences

10.10.2019

From

Dr.M.Srinivasan
Professor & Head,
Department of EEE,
Navodaya Institute of Technology,
Raichur.

To

The Principal,
Navodaya Institute of Technology,
Raichur.

Respected Sir,

Sub: Bill settlement - Guest Lecture - Reg.

We have conducted the Guest Lecture on 'Artificial Intelligence - Applications to Power System Protection" in our department on 30th September 2019. The budget of Rs.15,000/- for this event is approved by the honorable chairman. Details of expenditure for guest lecture are enclosed herewith. I request you to kindly sanction the amount spent for the guest lecture.

Thanking You,

Yours faithfully,

S. No.	Description of the Item	Amount (Rs.)
1.	Honorarium including TA and DA Dr.Ravikumar Bhimasingu , Associate Professor, Dept. of Electrical Engineering, Indian Institute of Technology, Hyderabad.	11,000.00
2.	Banner (4X6) 2Nos.	430.00
3.	Refreshments for participants (Tea & snacks)	420.00
4.	Bouquets	200.00
5.	Petrol	200.00
6.	Accommodation and Food for Guest	1848.00
7.	Miscellaneous	320.00
	Total	**14,418.00**
(Rupees Fourteen Thousand and Four Hundred and Eighteen only)		

Coordinator (Mr.Velu)

HOD/EEE (Dr.M.Srinivasan)

To
Mr. Venkatramana
for the needful.

NAVODAYA INSTITUTE OF TECHNOLOGY * RAICHUR

NAVODAYA INSTITUTE OF TECHNOLOGY:RAICHUR

(Approved by AICTE, New Delhi & Affiliated to VTU, Belgaum)

GUEST LECTURE-FEEDBACK

DATE: 30-09-2019

DEPARTMENT: ELECTRICAL AND ELECTRONICS ENGINEERING

NAME OF THE RESOURCE PERSON	Dr.Ravikumar Bhimasingu
DESIGNATION	Associate Professor
INSTITUTE/ORGANISATION/INDUSTRY	Indian Institute of Technology Hyderabad
TITLE OF LECTURE	" Artificial Intelligence application to power system protection "
DATE	30.09.2019
VENUE	NIT-AUDITORIUM
COMMENTS	Good, It was useful

NAVODAYA INSTITUTE OF TECHNOLOGY:RAICHUR

(Approved by AICTE, New Delhi & Affiliated to VTU, Belgaum)

GUEST LECTURE-FEEDBACK

DATE: 30-09-2019

DEPARTMENT: ELECTRICAL AND ELECTRONICS ENGINEERING

NAME OF THE RESOURCE PERSON	Dr.Ravikumar Bhimasingu
DESIGNATION	Associate Professor
INSTITUTE/ORGANISATION/INDUSTRY	Indian Institute of Technology Hyderabad
TITLE OF LECTURE	" Artificial Intelligence application to power system protection "
DATE	30.09.2019
VENUE	NIT-AUDITORIUM
COMMENTS	Excellent Lecture by Resource Person.

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

NET'S NAVODAYA INSTITUTE OF TECHNOLOGY, RAICHUR

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

NAVODAYA

SUMMARY OF GUEST LECTURE ON "ARTIFICIAL INTELLEGENCE APPLICATIONS TO POWER SYSTEM PROTECTION"



On 30th of September 2019, Department of EEE has conducted a Guest Lecture on "Artificial Intelligence Applications to Power System Protection", which was presented by Dr.Ravikumar Bhimasingu, Associate Professor, Department of EEE, Indian Institute of Technology, Hyderabad. The session was inaugurated with prayer song followed by the welcome speech by the Head of the Department, Dr.M.Srinivasan.First a brief introduction about basic structure of power system.Then guest speaker explained using the single line diagram, methods to clear the fault using artificial intelligence.

In addition to that he briefly explained the concept of support vector machine (SVM). Our students gained knowledge and interacted with the IIT Professor.Finally students know about the AI techniques and got some basic ideas about recent trends in power system protection.

Dr.M.V Mallikarjuna, Principal and Dr.Suman Kumar, Vice Principal felicitated the gathering. Management appreciated all the faculty members and students for their participation and successful conduction of this event

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

NET's
NAVODAYA
Institute of Technology
RAICHUR
(Affiliated to Visvesvaraya Technological University (VTU) Belagavi
Affiliated by AICTE, New Delhi and Recognised by the Govt. of Karnataka)

Navodaya Nagar, Bijanagera Road
RAICHUR - 584 103, Karnataka, INDIA
Ph : +91 8532 240334
Fax : 240054 / 240061
E-mail : principal.nit@navodaya.edu.in
Web : www.navodayaengg.in





ನವೋದಯ ತಾಂತ್ರಿಕ ಮಹಾವಿದ್ಯಾಲಯದ ಎಲೆಕ್ಟ್ರಿಕಲ್ ಆ್ಯಂಡ್ ಎಲೆಕ್ಟ್ರಾನಿಕ್ಸ ವಿಭಾಗದಲ್ಲಿ ರ್ಟಿಫೀಷಿಯಲ್ ಇಂಟಲಿಜೆನ್ಸ್ ಆಪ್ಲಿಕೇಶನ್ ಟು ಪವರ್ ಸಿಸ್ಟಮ್ ಪ್ರೋಟೆಕ್ಷನ್" ವಿಷಯದಲ್ಲಿ ವಿಶೇಷ ಪನ್ಯಾಸವನ್ನು ಆಯೋಜಿಸಲಾಗಿತ್ತು.

ಉಪನ್ಯಾಸಕರಾದ ಡಾ.ರವಿಕುಮಾರ್ ಭೀಮಸಿಂಗು,ಅಸೋಸಿಯೇಟ್ ಪ್ರೋಫೆಸರ್, ಐ.ಐ.ಟಿ, ದರಾಬಾದ್ ಇವರು ವಿಸ್ತಾರವಾಗಿ ಉಪನ್ಯಾಸವನ್ನು ನೀಡಿ.ವಿದ್ಯಾರ್ಥಿಗಳಲ್ಲಿ ನವೀನ ವಿಷಯವಾದ ರ್ಟಿಫೀಷಿಯಲ್ ಇಂಟಲಿಜೆನ್ಸ್ ಕುರಿತು ಉತ್ಸಾಹ ತುಂಬಿದರು.

ಉಪನ್ಯಾಸದಲ್ಲಿ ೩ನೇ ಮತ್ತು ೪ನೇ ವರ್ಷದ ಎಲೆಕ್ಟ್ರಿಕಲ್ ಆ್ಯಂಡ್ ಎಲೆಕ್ಟ್ರಾನಿಕ್ಸ ಹಾಗೂ ಕಂಪ್ಯೂಟರ್ ಸ್ಸ ವಿಭಾಗದ ವಿದ್ಯಾರ್ಥಿಗಳು ಪಾಲ್ಗೊಂಡಿದ್ದರು.

ಉಪನ್ಯಾಸದಲ್ಲಿ ಸಂಸ್ಥೆಯ ಪ್ರಾಂಶುಪಾಲರು ಎಲೆಕ್ಟ್ರಿಕಲ್ ವಿಭಾಗದ ಮುಖ್ಯಸ್ಥರು ಮತ್ತು ಎಲ್ಲಾ ಧ್ಯಾಪಕರು ಉಪಸ್ಥಿತರಿದ್ದರು.

ಉಪನ್ಯಾಸಕರ ಕಾರ್ಯಕ್ರಮವನ್ನು ಯಶ್ವಸಿಯಾಗಿ ಆಚರಿಸಿಕೋವುದಕ್ಕಾಗಿ ವಿಭಾಗದ ನವೋದಯ ಖ್ಯಸ್ಥರಿಗೆ, ಉಪನ್ಯಾಸಕರಿಗೆ ಮತ್ತು ವಿದ್ಯಾರ್ಥಿಗಳಿಗೆ,ಆಡಳಿತ ಮಂಡಳಿಯು ತಮ್ಮ ಮೆಚ್ಚುಗೆಯನ್ನು ತಿಳಿಪಡಿಸಿದ್ದಾರೆ

20/9/19
PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

GUEST LECTURE ON "ARTIFICIAL INTELLIGENCE APPLICATIONS TO POWER SYSTEM PROTECTION" by Dr.Ravikumar Bhimasingu, AssocProf, IIT-Hyderabad., organized by Electrical and Electronics departmant, Navodaya Institute of Technology, Raichur campus on 30th September 2019.



INSTITUTE OF TECHNOLOGY
RAICHUR

NET GROUP OF EDUCATIONAL INSTITUTIONS: RAICHUR
NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

GUEST LECTURE/SEMINAR/ADD-ON COURSE/WORKSHOP /CONFERENCE/SHORT TERM TRAINING PROGRAMME/ANY OTHER PROPOSAL

Date:21.08.2017

1	**ACADEMIC YEAR**	2017-18
2	**TYPE OF THE PROGRAMME PROPOSED**	GUEST LECTURE
3	**TITLE OF THE PROGRAMME:**	Role of Power Electronic converters in Renewable applications
4	**BROAD OBJECTIVE OF THE PROGRAMME:**	The main objective of this Guest Lecture is to focus on the integration of the alternative sources of energy, including wind power, photovoltaic, gas, biomass and energy storage for those systems. The output of many renewable generators like photovoltaic cells, fuel cells, etc. is variable dc, which requires a dc/ac conversion stage for grid connection. For other renewable generators with variable ac output, like wind or sea-wave generators, one can at first convert the variable ac to dc and then do dc-ac conversion to connect to the utility line or use a direct ac-ac conversion.
5	**NAME OF THE INSTITUTION**	NETs NAVODAYA INSTITUTE OF TECHNOLOGY
6	**NAME OF THE DEPARTMENT(S)**	ELECTRICAL AND ELECTRONICS ENGINEERING
7	**BENEFICIERY**	Program: B.E YEAR: II/III/IV SEM: III / V/VII
8	**DATE/DURATION OF SUCH PROGRAMMES CONDUCTED IF ANY IN THIS ACADEMIC YEAR:**	No
9	**ELIGIBILITY CRITERIA SET:**	**Faculty Members/All B.Tech/B.E Students**
10	**NUMBER OF DAYS REQUIRED TO ORGANISE THE PROGRAMME**	One Day
11	**PROPOSED DURATION OF THE PROGRAMME**	09.09.2017

12	**TOTAL NO. OF PERIODS REQUIRED FOR THE PROGRAMME**	3 Periods
13	**NO. OF PERIODS REQUIRED PER DAY**	3Periods
14	**LOCATION DETAILS:**	NETs NIT Auditorium
15	**MINIMUM NO OF PARTICIPANTS:**	100
16	**DETAILS OF SPONSORERS:**	-
17	**DETAILS OF RESOURCE PERSONS:**	Dr.Siva Kumar.K. Associate Professor, Department of Electrical Engineering, Indian Institute of Technology (IIT) Hyderabad, Kandi, Sangareddy - 502285, Telangana, India.
18	**REGISTRATION FEE WITH CERTIFICATION:**	Rs ___- per participant
19	**REGISTRATION FEE WITHOUT CERTIFICATION:**	Rs ___per participant.
20	**EVENT ORGANISERS (STUDENTS):**	1. Sameer , Final Year EEE 2.M Shakeer Ahmed, Final Year EEE
21	**FACULTY COORDINATOR:**	1.Dr.M.Srinivasan Professor & Head, NETS NIT, Raichur. 2. Mr.Velu.A. Assistant Professor, NETS NIT, Raichur.
22	**PROGRAMME PROPOSED BY:**	Dept. of Electrical and Electronics Engineering
23	**ANY OTHER RELEVANT MATTER:**	Nil
24	**ESTIMATED BUDGET IN RS:**	Rs.15,000/-

This is to the Honorable CEO of NIT for kind approval:

PRINCIPAL

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

HOD

Head of the Department
Department of Electrical and
Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103, Karnataka

EVENT COORDINATOR

APPROVED/NOTAPPROVED

NAVODAYA INSTITUTE OF TECHNOLOGY * RAICHUR

NETs NAVODAYA INSTITUTE OF TECHNOLOGY, RAICHUR

DEPARTMENT OF ELECTRICAL AND ELCTRONICS ENGINEERING

BUDGET FOR GUEST LECTURE

21.08.2017

S.No.	Description of the item	Qty.	Amount in Rs.
1.	Banner	2 Nos.	800
2.	Shawl	1 No.	500
3.	Tea & Biscuits,	100	700
4.	Remuneration to Guest speaker	-	8,000
5.	Travelling Allowance	-	5,000
		Total	15,000

(Rupees Fifteen Thousand Only)

Coordinator 21/8/17

HOD

Head of the Department
Department of Electrical and
Electronics Engineering
Navodaya Institute of Technology
RAICHUR-584 103, Karnataka

Respected CEO Sir
Forwarded for
kind approval

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

[Quoted text hidden]

Ir. Siva Kumar K <ksiva@iith.ac.in> 15 August 2017 at 07:
o: "M.S. Vasan" <srinimeha@gmail.com>

Dear Dr. Srinivasan,

You may contact me on 9494750750

siva
[Quoted text hidden]

I.S. Vasan <srinimeha@gmail.com> 18 August 2017 at 15:
o: "Dr. Siva Kumar K" <ksiva@iith.ac.in>

Dear sir,

We confirmed the date for guest lecture on 09.09.2017(Saturday). Kindly suggest one suitable topic for guest lecture (probably for 2 Hours) as your wish.
Pl. confirm as soon as possible.

Thanking you,

Dr.M.Srinivasan
[Quoted text hidden]

---

r. Siva Kumar K <ksiva@iith.ac.in> 20 August 2017 at 17:
o: "M.S. Vasan" <srinimeha@gmail.com>

Dear Sir,

I will be happy to give a talk on "Role of Power Electronic converters in Renewable applications"

If any other information required let me know

with regards
sivakumar
[Quoted text hidden]

S. Vasan <srinimeha@gmail.com> 24 August 2017 at 11:
: "Dr. Siva Kumar K" <ksiva@iith.ac.in>

Dear sir,

We are happy about the title "Role of Power Electronic converters in Renewable applications".

Further we informed that, we will arrange LCD projector, hall arrangement. and all other common facilities.

We are arranging transportation from your place. You have any other idea for mode of transportation, let us know we will proceed accordingly.

If any thing required more pl. inform sir.

Thanking you

M.Srinivasan

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

[Quoted text hidden]

Dr. Siva Kumar K

Dr. Siva Kumar K
Associate Professor,
Department of Electrical Engineering,
IIT Hyderabad, Kandi,
Sangareddy - 502285,
Telangana, India.
ksiva [at] iith.ac.in
+91-40-2301-6119

- Education

  Ph.D from Centre for Electronics Design and Technology (CEDT), Indian Institute of Science Bangalore (2010)
  M.Tech (Power Electronics and Drives) from NIT Warangal (2006)
  B.Tech (EEE) from Sri Venkateswara University, Tirupati (2004)

- Areas of Interest

  Multilevel inverters, open-end winding induction motor drives, Switched Mode Power Conversion, microgrids, Power quality and control.

- Professional Experience

  Associate Professor, Department of Electrical Engineering, IIT Hyderabad (Nov 2016 - present)Assistant Professor, Department of Electrical Engineering, IIT Hyderabad (March 2011 - Nov 2016)
  Design Engineer, American Power Conversion (India) Pvt. Ltd., Bangalore (December 2010 - March 2011)
  Adhoc faculty, Department of Electrical and Electronics Engineering, NIT Warangal (July 2010 - December 2010)

- Projects

  "A fault tolerant multilevel inverter configuration for islanded mode photovoltaic generation system" - SERB, GOI
  "Investigation on novel multilevel inverter topology for induction motor drive"
  "Modeling, analysis and simulation of electro-mechanical actuation system with Dual stator-dual rotor Radial flux BLDC motor"

NAVODAYA
Institute of Technology

Invitation Letter

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Guest lecture on

"Role of Power Electronic converters in Renewable applications"

BY

Dr. Siva Kumar K
Associate Professor,
Department of Electrical Engineering,
IIT Hyderabad, Kandi, TS.

On

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

09th September 2017

At 10.30 AM

Venue: NIT, SEMINAR HALL

NAVODAYA
Institute of Technology
RAICHUR

Date: 24-08-2017

Forwarded to the Honorable Chairman of NET through Respected CEO of NIT, Raichur for kind approval:

Requisition No.:	
Academic year:	2017-18
Date of Request:	21-08-2017
Subject:	Guest Lecture on Role of Power Electronic Converters In Renewable Applications on 09-09-2017.
Resource Person:	Dr.Sivakumar.K Prof. IIT-Hyderabad
Amount:	Rs.15, 000/-
Priority:	Urgent / Ordinary
Contact Person:	HOD - EEE -NIT-Raichur
Domain:	Academics Co-Curricular Activities
If any other please specify:	

Remarks of the Principal:

The programme is beneficial to the 3rd and 4 th year students., The resource person is from IIT-Hyderabad. He has been asked to interact with Faculty also. Hence it is recommended for your kind approval.

Signature of the Principal

Remarks of the C.E.O

Approved

5/9/17

Signature of the CEO

Remarks of the Chairman:

Maybe approved

7/9/17

Signature of the Chairman

NET/NIT/DW/2017-18/1676

ETHN/DW/2012-14/0997

7/9/17

NETHOD/DW/2012-14/0998

7/9/17

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

NAVODAYA
Institute of Technology
RAICHUR

Date: 21-08-2017

Forwarded to the Honorable Chairman of NET through Respected CEO of NIT, Raichur for kind approval:

Requisition No.:	
Academic year:	2017-18
Date of Request:	21-08-2017
Subject:	**Guest Lecture** on Role of Power Electronic Converters In Renewable Applications on **09-09-2017**
Resource Person:	**Dr.Sivakumar.K Prof. IIT-Hyderabad**
Amount:	Rs.15, 000/-
Priority:	Urgent / Ordinary
Contact Person:	HOD - EEE -NIT-Raichur
Domain:	Academics – Co-Curricular Activities
If any other please specify:	

Remarks of the Principal:

The programme is beneficial to the 3rd and 4 th year students. The resource person is from IIT-Hyderabad. He has been asked to interact with Faculty also. Hence it is recommended for your kind approval.

Signature of the Principal

Remarks of the C.E.O

Approved

5/9/17

Signature of the CEO

Remarks of the Chairman:

Signature of the Chairman

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

11.09.2017

Dr.M.Srinivasan
Professor & Head,
Department of EEE,
Navodaya Institute of Technology,
Raichur.

To

The Principal,
Navodaya Institute of Technology,
Raichur.

Respected Sir,

Sub: Submission of bills-Guest Lecture- Reg.

I bring to your kind notice that we are conducted the Guest Lecture on **'Role of Power Electronic Converters in Renewable Applications'** in our department on 09th September 2017. The approved budget of Guest Lecture is **Rs.15, 000/-**(Rupees Fifteen Thousand only). Details of actual expenditure for guest lecture are enclosed herewith. I request you to accept the balance amount.

Thanking You,

Yours faithfully,
11/9/17
(Dr.M.Srinivasan)

S. No.	Description of the Item	Amount (Rs.)
1.	Remuneration including TA and DA of resource person: 1. Dr.Siva Kumar.K. Associate Professor, Department of Electrical Engineering, Indian Institute of Technology, Hyderabad.	12,000.00
2.	Banner - flex printing	500.00
3.	Bouquets - flowers	250.00
4.	Gift to resource person	250.00
5.	Lunch	1,347.00
	Total amount spent for Guest Lecture	14347.00

Total amount spent for Guest Lecture	14,347.00
Amount received from office	15,000.00
Amount to be return to office (Rs.15000-14347)	653.00

Verified
11/9/17

Accounts Verified 12/9/17

12/9/17

14/09/17

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

SUMMARY OF GUEST LECTURE ON "ROLE OF POWER ELECTRONIC CONVERTERS IN RENEWABLE APPLICATIONS"

On 09th of September 2017, Department of EEE has conducted a Guest Lecture on "Role of Power Electronic Converters in Renewable Applications", which was presented by Dr.Siva Kumar, Associate Professor, Department of EEE, Indian Institute of Technology, Hyderabad. The session was inaugurated with prayer song followed by the welcome speech by the Head of the department, Dr.M.Srinivasan.

First a brief introduction about basic structure of power converters and micro grid. Then discussed about how to design the power converters to renewable applications i.e PV Solar system and wind power generation systems. At last a presentation which shows the future power plant and converters in the world with technology has been displayed. Our students gained knowledge and interact with the IIT Professor. Finally students know about power converters and got some basic ideas about micro grid.



NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR





Guest lecture "Role of Power Electronic converters in Renewable applications" Chief Guest Dr. Siva Kumar K Associate Professor ,Department of Electrical Engineering, IIT Hyderabad, Kandi, TS Date:09.09.2017

VIJAYA INSTITUTE OF TECHNOLOGY
RAICHUR

NAVODAYA Institute of Technology RAICHUR

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Evaluation Form

Workshop/Guest Lecture Title: Role of Power Electronics in Renewable Applications

Date: 9.9.2017 Time: 10.00 AM 1.00 PM

Please complete the following evaluation for the Workshop/Guest Lecture you attended. Your feedback will help us evaluate the effectiveness of this program and allow us to make improvements in the future program. **Your feedback is important for planning future programming and services we offer.**

Thank you.

4=Strongly Agree 3=Agree 2=Disagree 1=Strongly Disagree

	Circle your response			
1. The course met my expectations.	(4)	3	2	1
2. The content was helpful.	4	(3)	2	1
3. The level of the course was appropriate.	(4)	3	2	1
5. The speaker had a good understanding of the topics.	4	(3)	2	1
6. The handouts were helpful.	(4)	3	2	1
7. The course was worth my time.	4	(3)	2	1
8. The length of the course was appropriate.	(4)	3	2	1
9. I recommend that the course be repeated for other students.	4	(3)	2	1

What were the most useful aspects of the course?

Power technique was informative, Pulse generation session.

What changes should be made to enhance/improve this program?

No

Additional comments: Thank you for management for organising this event.

Name of the Faculty: VELVA, AP/EEE

Signature 9/9/2017

NAVODAYA
Institute of Technology
RAICHUR

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Evaluation Form

Workshop/Guest Lecture Title: Role of Power Electronics in Renewable applications

Date: 9.9.2017 Time: 10.00 to 1.00pm

Please complete the following evaluation for the Workshop/Guest Lecture you attended. Your feedback will help us evaluate the effectiveness of this program and allow us to make improvements in the future program. **Your feedback is important for planning future programming and services we offer.**

Thank you.

4=Strongly Agree 3=Agree 2=Disagree 1=Strongly Disagree

	Circle your response			
1. The course met my expectations.	(4)	3	2	1
2. The content was helpful.	4	(3)	2	1
3. The level of the course was appropriate.	(4)	3	2	1
5. The speaker had a good understanding of the topics.	4	(3)	2	1
6. The handouts were helpful.	(4)	3	2	1
7. The course was worth my time.	(4)	3	2	1
8. The length of the course was appropriate.	(4)	3	2	1
9. I recommend that the course be repeated for other students.	4	(3)	2	1

What were the most useful aspects of the course?

Contents It was good session & very informative

What changes should be made to enhance/improve this program?

-NIL-

Additional comments:

Name of the Faculty: Geeta.K

Signature 9/9/2017

NAVODAYA Institute of Technology RAICHUR

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Evaluation Form

Workshop/Guest Lecture Title: Role of power Electronics in Renewable Application

Date: 9-9-2017 Time: 10:00 Am to 1:00pm

Please complete the following evaluation for the Workshop/Guest Lecture you attended. Your feedback will help us evaluate the effectiveness of this program and allow us to make improvements in the future program. **Your feedback is important for planning future programming and services we offer.**

Thank you.

4=Strongly Agree 3=Agree 2=Disagree 1=Strongly Disagree

	Circle your response			
1. The course met my expectations.	(4)	3	2	1
2. The content was helpful.	4	(3)	2	1
3. The level of the course was appropriate.	(4)	3	2	1
5. The speaker had a good understanding of the topics.	(4)	3	2	1
6. The handouts were helpful.	(4)	3	2	1
7. The course was worth my time.	(4)	3	2	1
8. The length of the course was appropriate.	4	(3)	2	1
9. I recommend that the course be repeated for other students.	(4)	3	2	1

What were the most useful aspects of the course?

Solar & Inverter

What changes should be made to enhance/improve this program?

NO

Additional comments:

Name of the Faculty:

SATHISH KUMAR. KS

Signature 9/9/2017

NET GROUP OF EDUCATIONAL INSTITUTIONS: RAICHUR
NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

SEMINAR PROPOSAL

Dt: 24.01.2019

1	ACADEMIC YEAR	2018-19
2	TYPE OF THE PROGRAMME PROPOSED	SEMINAR
3	TITLE OF THE PROGRAMME:	**Wireless Communication Technologies in 5G**
4	BROAD OBJECTIVE OF THE PROGRAMME:	The fourth generation wireless communication (4G) systems have been deployed or are soon to be deployed in many countries. However, with an explosion of wireless mobile devices and services, there are still some challenges that cannot be accommodated even by 4G, such as the spectrum crises and high energy consumption. Wireless system designers have been facing the continuously increasing demand for high data rates and mobility required by new wireless applications and therefore has started research on fifth generation (5G) wireless systems that are expected to be deployed beyond 2020. The main purpose of 5G is planned to design the best wireless world that is free from limitations and hindrance of the previous generations. 5G is going to change the way most high bandwidth users access their mobile radio communication.
5	NAME OF THE INSTITUTION	(NETs) NAVODAYA INSTITUTE OF TECHNOLOGY
6	NAME OF THE DEPARTMENT(S)	ELECTRONICS AND COMMUNICATION ENGINEERING
7	TARGETED GROUP OF STUDENTS	Program: B.E Departments: ECE / EEE & OTHER COLLEGES YEAR: III/IV
8	DATE/DURATION OF SUCH PROGRAMMES CONDUCTED IF ANY IN THIS ACADEMIC YEAR:	No
9	ELIGIBILITY CRITERIA SET:	B.E-ECE & EEE
10	NUMBER OF DAYS REQUIRED TO ORGANISE THE PROGRAMME	One Day
11	PROPOSED DURATION OF THE PROGRAMME	01.03.2019
12	TOTAL NO. OF PERIODS REQUIRED FOR THE PROGRAMME	6 Periods (3 Periods Lecture and 3 Periods Interaction with faculties & Students)
13	NO. OF PERIODS REQUIRED PER DAY	7 periods



14	LOCATION DETAILS:	NETs NIT Auditorium
15	MINIMUM NO OF PARTICIPANTS:	100
16	DETAILS OF SPONSORERS:	NIT Management
17	DETAILS OF RESOURCE PERSONS:	Dr. Naveen Department of Electrical Engineering Indian Institute of Technology Dharwad WALMI campus Karnataka 580011. Contact No: 836-2212-854 Email: naveen @ iitdh.ac.in (Enclosed 1 page bio-data)
18	REGISTRATION FEE WITH CERTIFICATION:	Nil
19	REGISTRATION FEE WITHOUT CERTIFICATION:	Nil
20	EVENT ORGANISERS (STUDENTS):	1. Reenu , Final Year ECE 2. Naveen Kumar, Final Year ECE 3. Shik Ashif, Final Year EEE
21	FACULTY COORDINATOR:	1. Mr. Sholabuddin, Assistant Professor/ECE 2. Mr. A. Velu, Assistant Professor/EEE
22	PROGRAMME PROPOSED BY:	Dept. of ECE
23	ANY OTHER RELEVANT MATTER:	Nil

NAVODAYA INSTITUTE OF TECHNOLOGY * RAICHUR

24	ESTIMATED BUDGET IN RS:	S.No	Description of Work	Required Amount (Rs.)
		1	Banner	800.00
		2	Bouquet (1)	200.00
		3	Shawl & 1 Memento	500.00
		4	Honorarium	6000.00
		5	TA (Dharwad to Raichur) 3 AC Tier Train & Local Transport Fare	2000.00
		6	Accommodation	2000.00
		7	Tea & Snacks (Two Sessions)	1500.00
		8	Brochure	1000.00
		9	Certificates	750.00
		9	Others	500.00
			Total	15,250.00

This is to the Honorable CEO of NIT for kind approval:

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

VICE-PRINCIPAL

HOD

Department of
Electronics & Communication Engineering
Navodaya Institute of Technology
RAICHUR-584 103.

EVENT COORDINATOR

HOD/ECE

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

APPROVED/NOTAPPROVED

C.E.O OF NIT

Dr. Venkatachalam ECE HOD <hodece.nit@navodaya.edu.in>

Invitation for one day seminar

3 messages

Dr. Venkatachalam ECE HOD <hodece.nit@navodaya.edu.in> Fri, Feb 8, 2019 at 3:44 PM
To: naveenmb@iitdh.ac.in
Bcc: shoaib madni <shoaib.madni7@gmail.com>, Venkatachalam K <venkatachalamece@gmail.com>

Dear Sir,

As per the communication with our Assistant Professor MD. Shoaibuddin Madni. I thank you for accepting our invitation to present a seminar on "Wireless Communication Technologies in 5G" in NITR on 01-03-2019.
Its my privilege to invite you for this event. Kindly grace the invitation.

I also herewith attached the brochure of this seminar.

Thank You.

--
With Regards
Dr. K. Venkatachalam,
Professor & Head,
Department of Electronics and Communication Engineering,
Navodaya Institute of Technology,
Raichur-584103.
Cell:6381922171

WIRELESS 5G SEMINAR.pdf
207K

Naveen M B <naveenmb@iitdh.ac.in> Fri, Feb 8, 2019 at 4:13 PM
To: "Dr. Venkatachalam ECE HOD" <hodece.nit@navodaya.edu.in>

Dear Sir,

Thank you for the invitation. I confirm my participation as the speaker for the seminar. I will let you know about my travel plans by the end of next week.

Thanks,

Naveen

NAVODAYA INSTITUTE OF TECHNOLOGY * RAICHUR

Dr. Naveen M.B,

15:49

Dr. Venkatachalam... 08/02/2019
to naveenmb, bcc: me

From Dr. Venkatachalam ECE HOD • hodece.nit@navodaya.edu.in
To naveenmb@iitdh.ac.in
Bcc shoaib.madni7@gmail.com
Date 8 Feb 2019, 15:44
Standard encryption (TLS).
See security details

Dear Sir,

As per the communication with our Assistant Professor MD. Shoaibuddin Madni. I thank you for accepting our invitation to present a seminar on "Wireless Communication Technologies in 5G" in NITR on 01-03-2019.
Its my privilege to invite you for this event. Kindly grace the invitation.

I also herewith attached the brochure of this seminar.

Thank You.

NAVODAYA INSTITUTE OF TECHNOLOGY • RAICHUR

Assistant Professor, Dept. of Electrical Engineering

Indian Institute of Technology (IIT) Dharwad,

Dharwad, Karnataka, India. PIN: 580011

E-mail: naveenmb@iitdh.ac.in

[Quoted text hidden]

Dr. Venkatachalam ECE HOD <hodece.nit@navodaya.edu.in> Fri, Feb 8, 2019 at 4:23 PM
To: Naveen M B <naveenmb@iitdh.ac.in>

Ok Sir. Thank you.
[Quoted text hidden]



Date: 30.1. 2019

From

Dr.K.Venkatachalam
Professor and Head/ECE,
Navodaya Institute of Technology,
Raichur-584103.

To

The Principal,
Navodaya Institute of Technology,
Raichur-584103.

Dear Sir,

Subject: Permission for conducting one day Seminar on 01.3.2019-Reg

We have proposed to conduct a one day seminar on "**Wireless Communication Technologies in 5G**" for ECE students on 01.3.2019, 10.30 am - 5.00 pm.

Please provide permission to do the same.

Thanking You

Encl: Budget Proposal

Yours Faithfully

30/1/19

Department of
Electronics & Communication Engineering
Navodaya Institute of Technology,
RAICHUR-584 103

To be put in request file for approval

30/1/19

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

Navodaya Institute of Technology

Raichur

Date:-01-02-2019

Forwarded to the Honorable Chairman through respected C.E.O of NIT, Raichur for kind approval:

Requisition No.:

Academic year: 2018-19

Date of Request: 01-02-2019

Subject: ECE: Permission for conducting one day Seminar on Wireless Communication Technologies in 5G " for ECE students on 01st March 2019. Resource person is Dr. Naveen- IIT Dharwad.

Amount: Rs. 15,250.00 (Rupees Fifteen Thousand Two Hundred Fifty only)

Priority: Urgent / Ordinary

Contact Person: HOD-ECE-NIT, Raichur

Domain: Seminar- ECE

Remarks of the Principal: For your kind approval.

Sir, opportunity has given to other colleges also to bring awareness on this topic so students of other colleges can participate as per our terms & conditions. Hence it for your kind approval

Signature of the Principal 5/2/19

Remarks of the C.E.O

Signature of the C.E.O.

Remarks of the Chairman

Signature of the Chairman

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Navodaya Institute of Technology

Raichur

Date:-01-02-2019

Forwarded to the Honorable Chairman through respected C.E.O of NIT, Raichur for kind approval:

Requisition No.:	
Academic year:	2018-19
Date of Request:	01-02-2019
Subject:	ECE: Permission for conducting one day Seminar on Wireless Communication Technologies in 5th "" for ECE students on 01st March 2019. Resource person is Dr. Naveen HT Bharwad.
Amount:	Rs. 15,250.00 (Rupees Fifteen Thousand Two Hundred Fifty only)
Priority:	Urgent / Ordinary
Contact Person:	HOD-ECE-NIT, Raichur
Domain:	Seminar ECE

Remarks of the Principal: For your kind approval.

Sir, opportunity has given to other colleges also to bring awareness on this topic so students of other colleges can participate as per our terms & conditions. This is for your kind approval

Signature of the Principal 3/2/19

Remarks of the C.E.O

Please ensure that there are sufficient outside participants and collect from them Attnarel

Signature of the C.E.O 4/2/19

Remarks of the Chairman

May be allowed

Signature of the Chairman

NET/NIT/Feb2019/1946

4/2/19

NET/HOD/2018-19/1683

4/2/19

NET/HOD/2018-19/2094

6/2/19

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

NET's
NAVODAYA INSTITUTE OF TECHNOLOGY,
RAICHUR
DEPARTMENT OF EEE & ECE

NAVODAYA

25 YEARS
Since 1992
NAVODAYA EDUCATION TRUST

INVITATION

The Management, Principal, Vice Principal,HODs, Faculty and Students

cordially invite you for the One day Seminar

On

"Wireless communication Technologies in 5G"

By

Dr.Naveen Mysore Balasubramanya

Assistant Professor, Department of Electrical Engineering,
Indian Institute of Technology(IIT), Dharwad.

INSTITUTE OF TECHNOLOGY RAICHUR NAVODAYA

Date: 01st March 2019 **Time: 10.00 AM**

Venue: NIT - Auditorium

Registration Form

(photocopy may be used if required)

Name (in BLOCK letters): _____

_____

Academic Qualification : _____

Experience: _____

Designation & Department: _____

College Address: _____

_____

_____

Name of Affiliated University: _____

E mail : _____

Contact Number: _____

DD Number/ Date _____

Declaration:

The information furnished above is true to the best of my knowledge.

Date:

Place; Signature of the Applicant

Mr./Ms./Dr. _____ is an employee (faculty)/PG student of our Institute. He / She will be permitted to attend the programme if selected.

Date:

Place:

Signature & Seal
HOD/Head of the Institution

Chief Patron
Sri. S. R. Reddy, Hon'ble Chairman
Navodaya Education Trust (NET®), Raichur.

Patron
Sri. N. Bharath Reddy, Trustee
Navodaya Education Trust (NET®), Raichur.

Chief Convener
Dr. Mallikarjun Sharma. Principal, NIT, Raichur.

Convener
Dr. K. Venkatachalam, HOD,ECE, NITR
Dr. M.Srinivasan,HOD,EEE,NITR

Coordinators

Prof. Md Shoaibuddin Madni
Assistant Professor Dept of ECE, NIT
Email: shoaibuddin.madni@navodaya.edu.in
shoaib.madni7@gmail.com
Contact no. : +91- 8792374743

Prof.Velu.A
Assistant Professor, Dept of EEE, NITR.
Email:velu.a@navodaya.edu.in
velu.inventions@gmail.com
Contact no. : +91- 9591330675

Organizing Committee

Faculties of ECE & EEE Department

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Navodaya Institute of Technology



One-day Seminar On

Wireless CommunicationTechnologies in 5G

March 1st 2019

ORGANIZED BY

Department of Electronics & Communication Engineering.

Department of Electrical & Electronics Engineering.

Navodaya Education Trust®
NAVODAYA INSTITUTE OF TECHNOLOGY
Navodaya Nagar, Bijiangera Road,
Raichur-584103, Karnataka, India.

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

&

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

ONE DAY SEMINAR ON WIRELESS COMMUNICATION TECHNOLOGIES IN 5G

DATE: 01/03/2019

S.NO	ROLES AND RESPONSIBILITIES	FACULTY	SIGN
1	Certificate and Banner Preparation	Mr.Md.Shoaibuddin Madni, Asst. Prof/ECE Mr.Velu, Asst. Prof/EEE	
2	Banner Fixing and Auditorium Hall Arrangements(Mic, Laptop, Projector etc)	Mr.Amarappa Pagi, Asst. Prof/ECE Mr.Madhubabu, Asst. Prof/EEE	
3	Certificate Writing	Mrs.Gururavi Shetty, Asst. Prof/ECE Ms Sana, Asst. Prof/EEE	
4	Lighting /Lamp Arrangements	Ms.Veenashree Hiremath, Asst. Prof/ECE, Ms.Preeti Sagar Pattankude Asst. Prof/ECE	
5	Registration and Reception	Ms.Preeti Sagar Pattankude, Asst. Prof/ECE Ms.Geeta, Asst. Prof/EEE	
6	Accommodation/Tea/Snacks/Lunch Arrangements	Mr.Sachin Kumar, Asst. Prof/ECE Mr.Satish, Asst. Prof/EEE	
7	Advertising in media (Newspaper)	Mr.Kiran, Asst. Prof/EEE Ms.Veenashree Hiremath, Asst. Prof/ECE	

Program Coordinator

HOD

Principal

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Navodaya Education Trust®

Navodaya Institute of Technology

NAVODAYA
THE CAMPUS OF POSSIBILITIES

Raichur-Koppal Road, Navodaya Nagar, Raichur - 584 101, Karnataka
CET Code : E176

NAVODAYA EDUCATION TRUST

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

&

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

ONE DAY SEMINAR ON WIRELESS COMMUNICATION TECHNOLOGIES IN 5G

DATE: 01/03/2019

Program Schedule

- **Master of ceremony - Mrs.Reshma Arasikere, Asst. Prof/ECE, Ms.Sana, Asst. Prof/EEE**
- **Welcoming guest on the Dias**
- **Invocation song** → Bouquet distribution.
- **Lightning Lamp**
- **Welcome address by HOD EEE**
- **Throwing light on program by HOD ECE**
- ~~**Bouquet presentation**~~
- **Introduction of chief guest - Mrs.Madhuri Devi CH, Asst. Prof/ECE**
- **Chief guest addressing**
- **Vice principal addressing**
- **Principal addressing**
- **Vote of Thanks Mrs.Channaveeramma, Asst. Prof/ECE**

* After - Seminar
* Feedback
* National Anthem

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

&

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

ONE DAY SEMINAR ON WIRELESS COMMUNICATION TECHNOLOGIES IN 5G

DATE: 01/03/2019

Welcome address
by
Dr. M. Srinivasan, HOD/EEE

Throwing light on the program
by
Dr. K. Venkatachalam, HOD/ECE

Address
by
Dr. B. Suman Kumar, Vice principal

Chief Guest Address
by
Dr. Naveen Mysore Balasubramanya
Assistant Professor/EEE
IIT Dharwad

Presidential Address
by
Dr.M .V. Mallikarjuna, Principal

Vote of Thanks
by
Mrs. Channaveeramma, Assistant Professor/ECE

***

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

A one day seminar on "Wireless communication Technologies in 5G" was organized by Department of Electrical and Eelectronics Engineering and Electronics and Communication Engineering at our esteemed institution Navodaya Institute of technology, Raichur on 01.03.2019.









The seminar was conducted by the Chief guest Dr.Naveen M.B, Assistant professor in the department of Electrical Engineering of the Indian Institute of Technology. He gave a magnificent speech on the topic.

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR



(http

Naveen

M.B

ABOUT

RESEAF

PUBLIC.

PATENT

TEACHI

Naveen Mysore Balasubramanya

Current Position

Assistant Professor, Department of Electrical Engineering, IIT Dharwad (http://www.iitdh.ac.in/).

Contact

Department of Electrical Engineering, IIT Dharwad,
WALMI Campus, Belur Industrial Area,
Near High Court, PB Road, Anjaneya Nagar,
Karnataka, India, PIN: 580011.
Email: naveenmb@iitdh.ac.in.

Research Area

- Communication and signal processing with focus wireless communication and the Internet of things (IoT).

Open Research Positions

Looking for a highly motivated Ph.D. student with sound background in the fundamentals of communication, probability theory and digital signal processing.

Interested applicants should apply through IIT Dharwad Doctoral Admission Procedure (http://www.iitdh.ac.in/academics_phd.php).
Deadline: 31st October 2018.

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

© Designed by Naveen M.B using 2016 Nitro Free HTML5. FreeHTML5.co (http://freehtml5.co/)

NAVODAYA EDUCATION TRUST

NAVODAYA INSTITUTE OF TECHNOLOGY

CERTIFICATE

This is to certify that Mr./Ms./Br. Muneeb Ajzal . C of NIT, Raichur. has attended one day seminar on

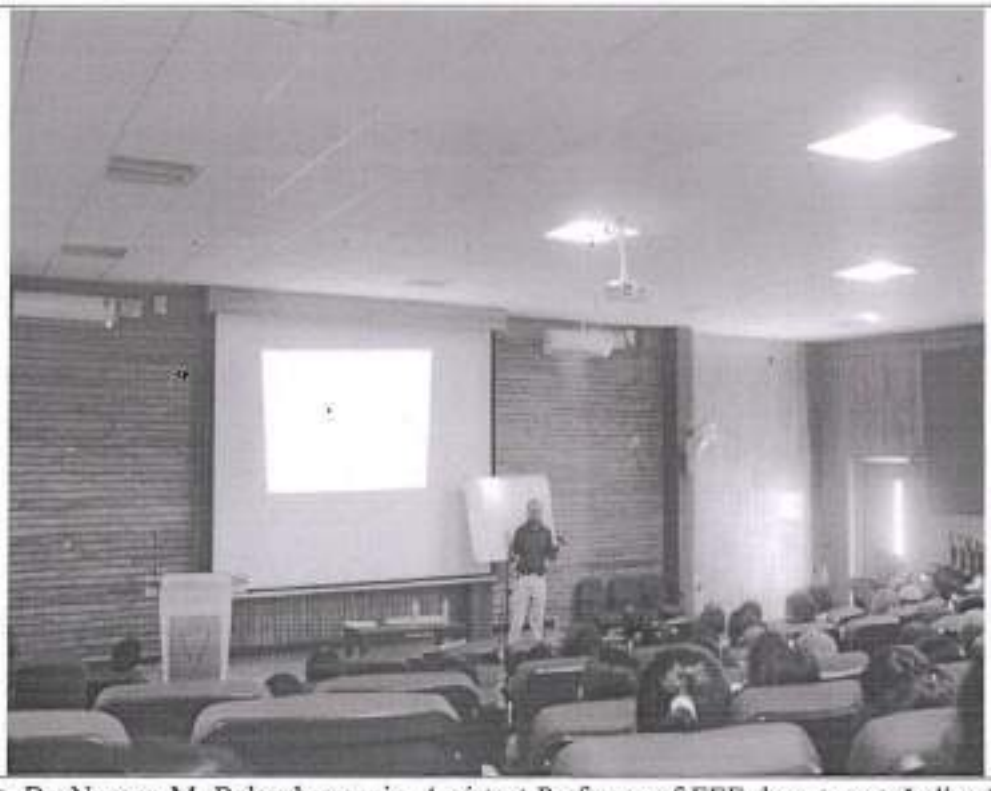
"WIRELESS COMMUNICATION TECHNOLOGIES IN 5G "

organized at Navodaya Institute of Technology, Raichur on 01.03.2019

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Coordinator **HOD** **Principal**

Scanned with CamScanner



Chief guest Dr. Naveen M. Balasubramania ,Assistant Professor of EEE department, Indian Institute of Technology, Dharwad ECE &EEE STUDENTS AND EXTERNAL PARTICIPANTS (48 Nos.) The faculties and students from GEC, PG Center, Raichur and VNEC, Shorpur.

TUTE OF TECHNOLOGY
RAICHUR

NET GROUP OF EDUCATIONAL INSTITUTIONS: RAICHUR
NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

~~GUEST LECTURE/SEMINAR/ADD-ON-COURSE/WORKSHOP /CONFERENCE/SHORT TERM TRAINING PROGRAMME/ANY OTHER PROPOSAL~~

Date: 19.10.2019

1	ACADEMIC YEAR	2019-20
2	TYPE OF THE PROGRAMME PROPOSED	WORKSHOP
3	TITLE OF THE PROGRAMME:	POWER ELECTRONICS APPLICATION IN RENEWABLE ENERGY BY USING MATLAB
4	BROAD OBJECTIVE OF THE PROGRAMME:	➢ To understand the concept of different renewable energy technologies. ➢ To understand the modeling, simulation and virtual experimentation of renewable energy system. ➢ To demonstrate and explain the components of a solar cell system, equivalent circuit of a solar cell, its characteristics and applications. ➢ To discuss the wind turbine models, wind resources, site selection for wind turbine. ➢ To train the students, construct the MATLAB/Simulink model of wind and solar PV system. ➢ To demonstrate the power electronics circuits and the applications through MATLAB for the analysis of Solar PV and Wind Energy conversion.
5	NAME OF THE INSTITUTION	NETs NAVODAYA INSTITUTE OF TECHNOLOGY
6	NAME OF THE DEPARTMENT(S)	ELECTRICAL AND ELECTRONICS ENGINEERING
7	TARGETED GROUP OF STUDENTS	Program: B.E., (III & IV Year) , M.E.\M.Tech., and Research scholars.
8	DATE/DURATION OF SUCH PROGRAMMES CONDUCTED IF ANY IN THIS ACADEMIC YEAR:	No
9	ELIGIBILITY CRITERIA SET:	All B.Tech/B.E Students, PG Students, Faculty members and Research Scholars,
10	NUMBER OF DAYS REQUIRED TO ORGANISE THE PROGRAMME	Two days
11	PROPOSED DURATION OF THE PROGRAMME	11.11.2019 and 12.11.2019

12	TOTAL NO. OF PERIODS REQUIRED FOR THE PROGRAMME	14 Periods (3 Periods Lecture and 11 Periods Hands on Training)
13	NO. OF PERIODS REQUIRED PER DAY	7 periods
14	LOCATION DETAILS:	NIT Auditorium & Electrical Computer Lab
15	MINIMUM NO OF PARTICIPANTS:	60
16	DETAILS OF SPONSORERS:	NIT Management
17	DETAILS OF RESOURCE PERSONS:	Day I (11.11.2019) and Day2 (12.11.2019): Mr. Malayappan, Senior R&D Engineer, (16 Years' Experience) Pan Tech Group of companies, Ameerpet, Hyderabad – 500 016. Aravindh Swamy.S R&D Engineer, (6 Years' Experience) Pan Tech Group of companies, Ameerpet, Hyderabad – 500 016.
18	REGISTRATION FEE WITH CERTIFICATION:	Rs. 500/- (Include two lunch) + Accomodation expenses
19	REGISTRATION FEE WITHOUT CERTIFICATION:	-
20	EVENT ORGANISERS (STUDENTS):	1. Padmaja , Final Year EEE 2. Md. Irfan Khan , Final Year EEE
21	FACULTY COORDINATOR:	1. Mr.N.Daniel, Assistant Professor/EEE 2. Mr.Velu.A., Assistant Professor / EEE
22	PROGRAMME PROPOSED BY:	40 internal students + 20 external students
23	ANY OTHER RELEVANT MATTER:	Nil
24	ESTIMATED BUDGET IN RS:	Rs. 36,000/-

This is to the Honorable Chairman of NET for kind approval:

PRINCIPAL 19/10/19

HOD 19/10/19

for 19/10/19
EVENT COORDINATOR

APPROVED/~~NOT APPROVED~~

26/10/19
CHAIRMAN OF NET

13/10/2019

PRINCIPAL
Navodaya Institute of Technology (NIT),
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

ABOUT RAICHUR:

Raichur, popularly known as 'Cotton City' is bounded by Krishna River on the North and Tungabhadra River on the South. A world famous Hatti Gold Mines is 75 kms away. RTPS, contributing major portion of state's power requirement is situated at Shakti Nagar about 20 kms from the city. Famous pilgrimage centres Mantralayam and Panchamukhi are within a radius of about 35 kms.

HOW TO REACH RAICHUR:

Raichur is well connected by Road and Railway to all nearby and important cities like Bangaluru, Hyderabad, Chennai, Mumbai, Tirupathi, Kalaburgi, Hubballi, and Ballari and so on.

ABOUT THE INSTITUTION:

Navodaya Institute of Technology (NIT), Raichur, established in 2008, is affiliated to Visvesvaraya Technological University (Belagavi). The institute is approved by All India Council for Technical Education (AICTE), New Delhi. The guiding spirit and the driving force for all the progress and achievements of the Institution is our Honorable chairman of Navodaya Education Trust (NET®), Sri S. R. Reddy. NIT focuses on promoting quality technical education in north Karnataka region and Hyderabad-Karnataka region in particular. The education is of paramount importance for socio-economic development. The institute focuses on integrated scientific training with value based education to enable overall development of the aspirants.

NIT is all-inclusive campus, with Administrative block, Library block, independent Academic blocks for each department, Separate Hostels for Boys and Girls, Canteen, Bank (SBI), in-campus ATM, Temple, Gym and Playground all housed in a vast campus of more than 30 acres land surrounded by hillocks and lake.

ABOUT THE DEPARTMENT:

The Department was started in the year 2008 with an intake of 60 students with the vision to provide the quality technical education. The department of EEE has well equipped laboratories, well established computer center with internet connectivity, excellent library, experienced & highly qualified faculty members to impart knowledge in the areas of Electrical Machines, Power Systems, Power Electronics and Drives, Controls Systems, High Voltage Engineering.

Vision

To become a centre of excellence in Electrical and Electronics Engineering, Producing innovative and creative Electrical and Electronics Engineers to meet the global challenges.

Mission

To provide a platform to the students towards attaining quality education in Electrical and Electronics Engineering.

To educate students about professional and ethical responsibilities and train them to build leadership and entrepreneurship qualities for their career development.

To create opportunities and guide students in acquiring career oriented jobs in the field of Electrical and Electronics Engineering.

ABOUT THE WORKSHOP:

The objective of the workshop is to impart knowledge to the UG and PG Students /Research Scholars, Developers, Consultants, Manufactures and Suppliers of solar systems regarding power electronics application in renewable energy by using MATLAB and give an insight regarding the various technologies like solar power conversion system, wind energy conversion system, classical control design, fuzzy logic control, MPPT algorithm, Inverter design and Hybrid System.

NAVODAYA INSTITUTE OF TECHNOLOGY

Two Day Workshop
On
"POWER ELECTRONICS APPLICATION IN RENEWABLE ENERGY BY USING MATLAB"

in association with

11th & 12th November 2019

Convenor
Dr.M.Srinivasan, Professor and Head/EEE

Coordinators
Prof.Daniel.N Asst.Prof./ EEE
Prof.Velu.A Asst.Prof./EEE

ORGANIZED BY
Department of Electrical & Electronics Engineering
Navodaya Education Trust®
NAVODAYA INSTITUTE OF TECHNOLOGY
Navodaya Nagar, Bijenegera Road, Raichur-584103, Karnataka, India.

E-mail:
hodeee.nit@navodaya.engg.ac.in
Website: www.navodaya.edu.in

Registration Form
(Photocopy may be used if required)

Name (in BLOCK letters):

_____

Academic Qualification:

_____

Experience:

_____

Designation &
Department:_____

College Address:_____

_____

_____

Name of Affiliated University:_____

E mail :_____

Contact Number:_____

DD Number/ Date_____

Declaration:
The information furnished above is true to the best of my knowledge.

Date:
Place: Signature of the Applicant

Mr./Ms./Dr._____
is an employee (faculty)/UG/PG student of our institute. He / She will be permitted to attend the programme if selected.

Date:
Place:

Signature & Seal
HOD/Principal/Head of the Organization

Chief Patron
Shri. S. R. Reddy, Hon'ble Chairman
Navodaya Education Trust (NET®), Raichur.

Patron
Dr.M.V.Mallikarjuna Principal, NIT, Raichur

Convener
Dr. M.Srinivasan, HOD, EEE, NIT, Raichur

Coordinators
Prof.Daniel.N
Assistant Professor
Department of EEE-NITR
Contact no:7892635921
Email Id: daniel.n@navodaya.edu.in

Prof.Velu A
Assistant Professor
Department of EEE-NITR
Contact no:9591330675
Email Id:velu.a@navodaya.edu.in

Address for communication:
The Principal
Navodaya Institute of Technology (NIT),
Navodaya Nagar, Bijangera Road, Raichur- 584 103.

Organizing Committee
1. Prof.Mazumdar B.K
2. Prof.Geeta.K.M
3. Prof.Satishkumar.K.S
4. Mr.Madhu Babu.B
5. Ms.Sana Anjum
6. Mr.Kiran Kumar

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

www.navodaya.edu.in/engineering
/navodayaeducationtrust

COURSE CONTENT
- Introduction to Renewable Energy System
- Power Electronics using RES
- Classical control design of RES
- Inverter Design
- Fuzzy Logic control
- Maximum Power Point Tracking
- Wind Energy Conversion
- Hybrid System

Resource Persons:
1. Er. Malayappan,
   Senior R&D Engineer,
   PANTECH Group of companies,
   Chennai-600119

2. Er.Aravindh Swamy.S
   R&D Engineer,
   PANTECH Group of companies,
   Ameerpet, Hyderabad – 500 016.

Registration Fee:

UG/PG Students	: Rs.500/-
Academicians / Research Scholars	: Rs.750/-
Industry Personnel's	: Rs.1000/-

Note: Registration fees includes working lunch and tea without accommodation

Accommodation:
Limited accommodation for the participants will be provided in the institute hostels on prior request (Chargeable basis)

Important dates

Workshop date	**: November 11th to 12th, 2019**
Last date for Registration	**: 7th November 2019**

Registration fee may be paid in the form of DD in favour of **Principal, Navodaya Institute of Technology,** payable at **SBI, NET campus, Raichur.**

VENUE: NIT Auditorium, NIT, Raichur

Navodaya Education Trust ®

Navodaya Institute of Technology

Raichur

Date:-23-10-2019

Forwarded to the Honorable Chairman through respected Principal of NIT, Raichur for kind approval:

Requisition No.	:
Academic year	: 2019-20
Date of Request	: 23.10.2019
Subject	: Request to conduct WORKSHOP on:-Power Electronics Application in Renewable Energy by Using MATLAB to be held on 11th & 12th of November 2019 - Reg
Budget	: Rs.36, 000/- (Rupees Thirty Six Thousand Only)
Priority	: URGENT / Ordinary
Contact Person	: PRINCIPAL / HOD-EEE-NIT
Domain	: Academics

Remarks of the Principal: For your kind Approval Sir

23/10/19

Sir,

Details of Resource Person:-

1. Mr.Malaiyappan, Senior R & D Engineer.
   - Works at Pantech Group of Companies.
   - He is having 16 years of experience.
   - He has successfully completed MATLAB Integrated Power Electronics Development Application Project for Texas Instruments.
2. Mr. Aravindh Swamy S, R & D Engineer
   - Works at Pantech Group of Companies.
   - He is having 06 years of experience.
   - His Area of Interest is in MATLAB SIMULINK, Renewable Energy Application etc.

Objective of the Programme:-

- To understand the concept of different renewable energy technologies.
- To discuss the wind turbine models, wind resources, site selection for wind turbine.
- To train the students, construct the MATLAB / Simulink model of wind & solar PV system.

This is for your kind approval sir

23/10/19

Signature of the Principal

Remarks of the Chairman

May be approved

26/10/19 Signature of the Chairman

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

-/2019-20/1431

Auclow/2019-20/1942

Scanned by CamScanner

07.11.2019

From

Dr.M.Srinivasan
Head of the Department,
Department of Electrical and Electronics Engineering
Navodaya Institute of Technology, Raichur, Karnataka - 584103.

To

The Principal,
Navodaya Institute of Technology,
Raichur, Karnataka - 584103.

Respected sir,

Sub: - Requisition of Haonorarium to Chief Guest - reg.

The Department of Electrical and Electronics Engineering of NIT - R has planned to conduct Two Day Workshop " Power Electronics applications to Renewable Energy by using MATLAB" programme in NIT - R auditorium on 11th and 12th November 2019. The resource persons are invited from Pantech Pro labs Pvt. Ltd., Hyderabad. The chief guest of the above function is Mr.Srinivasan.N, Founder and Director, Pantech Prolabs Pvt. Ltd., Hyderabad. It was approved to give honorarium Rs.27, 000/-. (Rupees Twenty Seven Thousand Only) for chief guest and resource persons. The account details are given below:

1. Name of account holder : Mr.Srinivasan.N.
   Account Number : 000901588634
   Name of the Bank : ICICI Bank
   Branch : Nungambakkam Branch, Chennai
   IFSC code : ICIC0000009
   PAN Number : BGHPS 7420J

10000/-

2. Name of account holder : Mr.Chinnasamy.C
   Account Number : 00000032150182728
   Name of the Bank : State Bank of India (SBI)
   Branch : Nilakottai Branch, Dindigul
   IFSC code : SBIN0000791.
   PAN Number : DTZPS3479C

10000/-

3. Name of account holder : Mr.Senthilkumar
   Account Number : 602601532331
   Name of the Bank : ICICI Bank
   Branch : T.Nagar Branch, Chennai
   IFSC code : ICIC0006026
   PAN Number : BDBPS8718N

7000/-

Kindly permit to provide cheque / online transfer as honorarium to chief guest.

Thanking You,

Yours faithfully,

7.11.19

Place : Raichur
Date : 07.11.2019

7/11/19

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Dr. Srinivas M <hodeee.nit@navodaya.edu.i

Reg - Workshop Agenda & Proposal From Pantech ProEd Pvt Ltd

messages

hinnasamy <chinnasamy.c@pantechmail.com> Sun, Sep 15, 2019 at 1:48 F
o: hodeee.nit@navodaya.edu.in
c: Srinivasan N <srinivasan.n@pantechmail.com>

Dear Sir/Madam,

Herewith I have attached the Workshop Agenda & Proposal. please find the attachment and feel free to contact us if you have any queries.

Pantech ProEd Private Ltd.,
4th Floor, Delta Chambers,
Behind Chennai Shopping Mall,
Ameerpet, Hyderabad - 500 016, Telangana.
Mob:8374713618.
Phone: 040 40077960.
www.pantechsolutions.net
www.pantechproed.com

Tamilnadu | Andra Pradesh | Maharashtra | Kerala | Telangana

3 attachments

POWER EELCTRONICS APPLICATION IN RENEWABLE ENERGY BY USING MATLAB NIT.pdf
264K

GENERAL WORKSHOP PROPOSAL.doc
39K

Company Profile - PANTECH.pdf
838K

rinivas m <hodeee.nit@navodaya.edu.in> Thu, Oct 10, 2019 at 4:09 F
: Chinnasamy <chinnasamy.c@pantechmail.com>
: Srinivasan N <srinivasan.n@pantechmail.com>

Sir
Send the faculty handling profile

HOD/EEE
Quoted text hidden]

innasamy <chinnasamy.c@pantechmail.com> Thu, Oct 10, 2019 at 7:17 F
Srinivas m <hodeee.nit@navodaya.edu.in>

Pantech Solution
WORKSHOP

POWER EELCTRONICS APPLICATION IN RENEWABLE ENERGY BY USING MATLAB

2DAYS AGENDA

DAY-1		
Session	Time	Class Topics
I	10.00- 10.30	**INTRODUCTION TO ELECTRICAL TECHNOLOGY** • Definition | Market Trends | Classification | Application • Types of Semiconductor Devices • Hardware Design Considerations • MATLAB Introduction
II	10.30-12.00	**CLASSICAL CONTROL DESGIN** • Converter (Open Loop) • Closed loop PID Controller • Difference Between Open Loop And Closed Loop
	11.00-11.10	Break
III	12.00-1.00	**INVERTER** • Introduction Of Multilevel Inverter • 5-Level Inverter Based On Drive Application
	1.00-1.30	Lunch Break
IV	1.30-2.00	**FUZZY LOGIC CONTROL** • Introduction Of Fuzzy • Basic Design Of Fuzzy(Rules And Decision Making) Control The Speed Of Dc Motor Using Fuzzy Logic
V	2.00-4.00	• Control The Speed Of Dc Motor Using Fuzzy Logic • Task Question & Answer Sessions

INSTITUTE OF TECHNOLOGY TRICHUR

© 2019 Pantech ProEd Pvt Ltd

WORKSHOP PROPOSAL

Submitted: September 15th, 2019
Email: chinnasamy.c@pantechmail.com

Dear Sir / Madam,

Sub: Agenda & Offer for WORKSHOP for UG Students at Your Campus – Reg

Thank you for your interest in working with TechGurukul, the Training Arm of Pantech ProEd Pvt Ltd! Pursuant to our discussions, we have prepared the following detailed proposal for your review and consideration. Given our experience and specific expertise in technical training for college students, we are confident that we can work with you to achieve your aims and goals.

Further with reference to the discussions had with you, please find enclosed herewith the offer for the Workshop Program at your campus.

TechGurukul Advantages:

- Course Completion by Industry Experienced Engineers.
- Advanced technologies, with hands on training.
- Electronics – Embedded , VLSI , DIP & Network Domains
- Electrical-Power Electronics, Power System, Control Automation, PLC
- IT Development – Machine Learning, Java & Android Applications as per the Encl Agenda.

WORKSHOP Proposal

Batch Size	Fee Structure
70 students	Rs 250 per participant, (per day Per Student)

The details of the Training Programs can be availed in the Attachment. Detailed Syllabus has been enclosed along with.

BENEFITS TO THE STUDENTS:

- Certification on Workshop Completion- Both on behalf of the Institution and TECHGURUKUL
- Materials to all the Students.





Malaiyappan M

Power Electronics Engineer

EDUCATION

ME Power Electronics & Drives | Passed out-2004
College Name
College of Engineering Guindy, Anna University, Chennai-25

B E EEE | Passed out-2002
College Name
Government College of Engineering, Bargur

CONTACT
#8, NATARAJAN STREET,
NOOKAMPALAYAM LINK ROAD,
CEMMANCHERRY, CHENNAI-119
malaiyappan.m@pantechmail.com
9840974408

EXPERIENCE

Senior RD Engineer 2004-2019
Company name
Pantech Group of Company
Electrical Group

PROJECTS

Successfully Completed MATLAB Integrated Power Electronics Development Application Project for Texas Instruments | 2018

Organized and Delivered 5 Days International Workshop in Power Converter Design at National Institute of Technology-Karnataka | 2018

Successfully Installed Open Source 5KW Solar Power System with Thin Film Solar PV Panel in Government College of Technology - Coimbatore| 2017

Involved DC Smart Home Project Development in Coimbatore Institute of Technology| 2014

Successfully Installed Open Source 6KW Solar Power System for Non-Linear Loads in Coimbatore Institute of Technology - Coimbatore| 2013

Closed Loop Control Design for SRM/PMSM/BLDC/PMDC with DSP | 2010

Organized and Delivered 2 Days Workshop in Power Converter Design at College of Engineering Guindy, Anna University Chennai|2008.

Vector Control of 3Phase Induction Motor | 2007

FPGA based Controller design and Implementation for 3Phase 5 Level Multilevel

SKILLS

MAGNET	●●●○○
CCS	●●●●○
CADENCE	●●●○○
MATLAB	●●●●○

LANGUAGES

TAMIL

English

HOBBIES &





ARAVINDH SWAMY S

hyd.electrical@pantechmail.com

Experience of 3+ years in MATLAB SIMULINK domain and having well knowledge in POWER ELECTRONICS, POWER SYSTEM AND HARDWARE Development of Projects with upgrade Technologies. Having a Good Technical Strength In Implementation of Projects & Conducting Work Shops in Universities....

EDUCATION:

B.Tech, Dr.NGP Institute of Technology, Coimbatore, Anna University.

TECHNICAL SKILLS

Programming Languages: C, C++, M-File

Firmware: MATLAB SIMULINK

WORK EXPERIENCE:

Drive application

- Working on research and development of speed control of bldc by using fuzzy _pid

Renewable energy application

- Worked on research and development of modified sepic converter with mppt(maximum power point tracker)

Power quality

- Working on research and development of Multilevel Inverter Based UPQC for Power Quality Improvement

Micro grid

- Working on research and development of designed DG based Power Quality Conditioner for Micro Grid applications.

Electrical vehicle application

- Working on research and development of Integrating a solar PV (Photovoltaic) with battery storage is presented to a single phase grid for residential and electric vehicle application.

Inverter and converter

- Development of A Permanent Magnet BLDC Motor Drive Powered From A Solar Photovoltaic Array In Luo Converter Is Used For Maximum Power Point Tracking

CURRENT FIELD OF WORK: Hardware of the Loop and State Flow

Seminars / Conference/ Workshops - Conducted / organized:

Recent workshops…

No.	Course	Location
1	Power electronics and renewable energy based on Matlab Simulink	Modern engineering college, pune.
2	Multilevel inverter and drives application using Matlab	CMRCET college ,Hyderabad
3	Renewable energy application by using Matlab	MREC, Hyderabad
4	Power electronics and drives by using Matlab Simulink	Methodist engineering college,hyderabad
5	Power electronics and system by using Matlab Simulink	GNITS college,hyderabad
6	Power converter design and control	Vignan university
7	Power Electronics Applications in Renewable Energy by using MATLAB and IOT	Matrusri Engineering College ,Hyderabad

Overall conducted 25+ workshops, 30+ seminars in various Colleges.



NAVODAYA INSTITUTE OF TECHNOLOGY (NIT)

RAICHUR- 584103, KARNATAKA

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

PANTECH SOLUTIONS
Technology Beyond The Dreams

TWO DAY WORKSHOP ON

" POWER ELECTRONICS APPLICATION IN RENEWABLE ENERGY BY USING MATLAB "

CERTIFICATE
of Participation

THIS IS TO CERTIFY THAT MR. / MS. Ashwinkumar. S, NIT-Raichur HAS ATTENDED TWO DAY WORKSHOP ON " POWER ELECTRONICS APPLICATION IN RENEWABLE ENERGY BY USING MATLAB " CONDUCTED AT NAVODAYA INSTITUTE OF TECHNOLOGY, RAICHUR ON 11-11-2019 AND 12-11-2019, IN ASSOCIATION WITH ISTE AND PANTECH SOLUTIONS, HYDERABAD.

Manager
Pantech Solutions
Hyderabad

Coordinator
ISTE - Student chapter (KA-147)
NIT, Raichur

HOD
NIT, Raichur

Principal
NIT,Raichur

www.pantechsolutions.net

Navodaya Institute of Technology, Raichur
Department: Electrical and Electronics Engineering

Two day workshop on "POWER ELECTRONICS APPLICATION IN RENEWABLE ENERGY BY USING MATLAB " by Shri.Malaiyappan ,Shri. Srinivasan.N&Shri.Sreenivasalu (Pantech Solutions).organized by Department of Electrical and Electronics Engineering, Navodaya Institute of Technology, Raichur campus on 11th & 12th November 2019.









NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

12/11/19

Head of the Department
Department of Electrical and
Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103. Karnataka

NAVODAYA INSTITUTE OF TECHNOLOGY

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Evaluation Form

Workshop/Guest Lecture Title: Power electronics application in RE using MATLAB

Date: 11-11-2019 to 12-11-2019 Time: 9:00 AM to 5:00 PM

Please complete the following evaluation for the Workshop/Guest Lecture you attended. Your feedback will help us evaluate the effectiveness of this program and allow us to make improvements in the future program. **Your feedback is important for planning future programming and services we offer.**

Thank you.

4=Strongly Agree 3=Agree 2=Disagree 1=Strongly Disagree

	Circle your response			
1. The course met my expectations.	(4)	3	2	1
2. The content was helpful.	4	(3)	2	1
3. The level of the course was appropriate.	(4)	3	2	1
5. The speaker had a good understanding of the topics.	(4)	3	2	1
6. The handouts were helpful.	4	(3)	2	1
7. The course was worth my time.	(4)	3	2	1
8. The length of the course was appropriate.	4	(3)	2	1
9. I recommend that the course be repeated for other students.	(4)	3	2	1

What were the most useful aspects of the course?

Sessions was useful, especially the demo of MATLAB.

What changes should be made to enhance/improve this program?

Additional comments: -nil-

Name of the Faculty: B. Madhu Rekha

Signature

Asst prof

EEE Dept

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

NAVODAYA INSTITUTE OF TECHNOLOGY

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Evaluation Form

Workshop/Guest Lecture Title: Power Electronics Application in RE using MATLAB

Date: 11.11.2019 to 12.11.2019 Time: 9.00 AM to 5.00 PM

Please complete the following evaluation for the Workshop/Guest Lecture you attended. Your feedback will help us evaluate the effectiveness of this program and allow us to make improvements in the future program. **Your feedback is important for planning future programming and services we offer.**

Thank you.

4=Strongly Agree 3=Agree 2=Disagree 1=Strongly Disagree

	Circle your response			
1. The course met my expectations.	(4)	3	2	1
2. The content was helpful.	4	(3)	2	1
3. The level of the course was appropriate.	(4)	3	2	1
5. The speaker had a good understanding of the topics.	4	(3)	2	1
6. The handouts were helpful.	(4)	3	2	1
7. The course was worth my time.	4	(3)	2	1
8. The length of the course was appropriate.	(4)	3	2	1
9. I recommend that the course be repeated for other students.	4	(3)	2	1

What were the most useful aspects of the course?

* Sessions was useful, especially the design of MLI

What changes should be made to enhance/improve this program?

—

Additional comments: NIL

Name of the Faculty: VELU.A, Asst Prof/EEE

12/11/2019

Signature

INSTITUTE OF TECHNOLOGY RAICHUR NAVODAYA

NAVODAYA INSTITUTE OF TECHNOLOGY

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Evaluation Form

Workshop/Guest Lecture Title: Power Electronics Application in RE using MATLAB

Date: 11/11/19 to 12/11/19 Time: 09 AM to 5 PM

Please complete the following evaluation for the Workshop/Guest Lecture you attended. Your feedback will help us evaluate the effectiveness of this program and allow us to make improvements in the future program. **Your feedback is important for planning future programming and services we offer.**

Thank you.

4=Strongly Agree 3=Agree 2=Disagree 1=Strongly Disagree

	Circle your response			
1. The course met my expectations.	(4)	3	2	1
2. The content was helpful.	(4)	3	2	1
3. The level of the course was appropriate.	4	(3)	2	1
5. The speaker had a good understanding of the topics.	4	(3)	2	1
6. The handouts were helpful.	(4)	3	2	1
7. The course was worth my time.	(4)	3	2	1
8. The length of the course was appropriate.	4	3	(2)	1
9. I recommend that the course be repeated for other students.	(4)	3	2	1

What were the most useful aspects of the course?

It was good session

What changes should be made to enhance/improve this program? — NO —

Additional comments: — Nil —

Name of the Faculty: Geeta. K.M

Signature 12/11/19

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

NET GROUP OF EDUCATIONAL INSTITUTIONS: RAICHUR
NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

GUEST LECTURE/SEMINAR/ADD ON COURSE/WORKSHOP/CONFERENCE/SHORT TERM TRAINING PROGRAMME/ ANY OTHER

(ECE Dept. and EEE Dept.)

Date:25.01.2018

1	ACADEMIC YEAR	2017-18
2	TYPE OF THE PROGRAM PROPOSED	Two Day Workshop on Matlab and Simulink with Real Time Applications.
	TITLE OF THE PROGRAM:	Two Day Handson Workshop on Matlab and Simulink with Real Time Applications.
	BROAD OBJECTIVE OF THE PROGRAM	To get expertise in coding and simulation in Matlab and Simulink and implement for Real time Applications.
	NAME OF THE INSTITUTION	Navodaya Institute of Technology, Raichur
	NAME OF THE DEPARTMENT(S)	Electronics and Communication Engineering.& Electrical and Electronics Engineering
	TARGETED GROUP OF STUDENTS	B.E-VIII SEM ECE Students (22 Students) and VIII SEM EEE Students (30 students)
8	DATE/DURATION OF SUCH PROGRAMMES CONDUCTED IF ANY IN THIS ACADEMIC YEAR	Yes. September to November of 2017 System design using verilog.
	ELIGIBILITY CRITERIA SET:	VIII SEM ECEStudents, VIII SEM EEEStudents, VISEM ECE Students, Faculties and participants from Other Colleges.
	NUMBER OF DAYS REQUIRED TO ORGANIZE THE PROGRAM	2Days
	PROPOSED DURATION OF THE PROGRAM	2-days by external resources16-02-2018 to 17-02-2018
	TOTAL NO.OF PERIODS REQUIRED FOR THE PROGRAMME	16
	NO.OF PERIODS REQUIRED PER DAY	8

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Scanned by CamScanner

	LOCATION DETAILS:	Navodaya Institute of technology, Raichur, DSP Lab
15	MINIMUM NO OF PARTICIPANTS:	70 Students
16	DETAILS OF SPONSORERS:	NET, Raichur
17	DETAILS OF RESOURCE PERSONS:	External 1.Dr.L.P Siva Kumar, (Assoc.Prof.),Raja Lkshmi Engineering College. Chennai, Tamilnadu. 2.Dr.K.Prem Kumar,(Assoc.Prof.),Raja Lkshmi Engineering College. Chennai, Tamilnadu.
18	REGISTRATION FEE WITH CERTIFICATION:	Yes **External Participants** UG/PG External Students : Rs.200/- Academicians / Research Scholars : Rs.500/- Industry Persons : Rs.1000/- **Internal participants** No fee for NIT students
19	REGISTRATION FEE WITHOUT CERTIFICATION	Nil
20	EVENT ORGANISERS	1. Mr. Laxmikant Reddi, Asst Prof /ECE 2. Mr.A.Velu, Asst Prof/EEE 3. Mrs.Reshma Arasikere, Asst Prof/ECE 4. Mr. Sudhakar Asst Prof/EEE
21	FACULTY COORDINATOR	Mr. Laxmikant Reddi, Asst Prof /ECE
22	PROGRAMME PROPOSED BY:	Mr. Laxmikant Reddi, Asst Prof /ECE
23	ANY OTHER RELEVANT MATTER:	Two Days Hands on Training in Real Time Applications
24	ESTIMATED BUDGET IN Rs:	31,000/-(Anexure-1) (Thirty One Thousands One Hundred Only)

PRINCIPAL
PRINCIPAL
of Technology (NIT)
Raichur-584 103

VICE-PRINCIPAL

HOD
Department of
Electronics & Communication Engineering
Navodaya Institute of Technology
RAICHUR-584 103.

(Laxmikant Reddi)
EVENT COORDINATOR

Head of the Department
Department of Electrical and
Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103. Karnataka

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Scanned by CamScanner

Annexure-I

Budget proposal of ECE department for Add on course on

Applications of "MATLAB and SIMULINK in Real Time Applications"

(ECE Dept. and EEE Dept.)

2017-18(Even Sem)

Sl.no	Description	Amount in Rs.
1	Remuneration for External resource person for 2-day(2* 10,000/-)	20,000/-
2	Travelling Allowances	3,000/-
3	Banners(2*500/-)	1000/-
4	Bouquet(4*150/-)	600/-
5	Tea and Snacks for participants(100 persons(EEE and ECE Students for 2 days)	4,000/-
6	Certificates(100*20/-)	2000/-
7	Miscellaneous	4000/-
	Sub Total(A)	34,600/-
	Income generated through External participants(B)	3500/-
	GrandTotal(A-B)	31,100/-(Thirty oneThousand One Hundred only)

PRINCIPAL VICE-PRINCIPAL HOD (Lavanikant Reddi) EVENT COORDINATOR

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Electronics ... tion Engineering
Navodaya Institute of Technology
RAICHUR-584 103.

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Department of Electrical and Electronics Engineering
Navodaya Institute of Technology
RAICHUR-584 103, Karnataka

Scanned by CamScanner

Request No.:
Year(current):
Date of Request:
Subject:
Amount:
Priority:
Contact Person:
Domain:
If any other please specify:
Remarks of the Principal:

Signature of the Principal

Remarks of the C.I.O

Signature of the C.I.O

Remarks of the Chairman:

Signature of the Chairman

spected Sir,
department of Electronics and communication engineering (ECE) and Electrical and
ectronics Engineering (EEE) have planned to conduct two days workshop on Matlab
nd Simulink with Real time applications which is proposed from 16-02-18 to
7-02-2018. Hence, Request to sanction Rs 31,100/- for conducting the workshop
mitted to the Hon'ble Chairman Sir, NIT. for approval of Rs 31,100/-.

Approved

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Scanned by CamScanner

NAVODAYA
Institute of Technology
RAICHUR

Sl. No	Name of the Participants	Institute/Collage	Amount Paid Rs. seat	Signature
01	Reksha Patil	PG Center	Rs. 1500	Rspatil
02	Dr. Emmanuel	"	1500	
03	Supriya Purohit	"	500	Supriya
04	Shruti	"	200	Shruti
05	Pratiba Patil	"	200	Pratibapatil
06	Sushma	"	200	Sushma
07	Shanath Kumar	"	200	
08	Sangeeta	"	200	Sangeeta
09	Shilpa	"	200	Shilpa
10	M. Sai Tejaswini	"	200	M. Tejaswini
11	Tirumalesh Nayak	"	200	Tirumalesh
12	Md. Pasha. x	"	200	Pasha
13	Sanjeev Gouda	"	200	Sanjeev
14	Nagalenkata prasad.	"	200	
15	Sapna	"	1500	Sapna
16	Bhagyajyoti	"	1500	Bhagyajyoti
17	Manjunath .	"	200	Manjunath
18	H. Harish Kumar	"	200	Harish

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Scanned by CamScanner

Dr. L Shrinivas Sudhindra

19	Dr. L. Shrinivas Sudhindra	"	500	
20	Parvathi - C.S	"	200	
21	Shruthi Kaircatagi	"	200	
22	Manjappa G. Kalmani	"	200	
23	Thirumalesh Nayak P	"	200	
24	Narasa Sai Tejaswi	"	200	
25	Immanuel	"	200	
26	P Bhaskar	"	200	
27	Jyothi R	"	100	
		Total -	6800/-	

INSTITUTE OF TECHNOLOGY RAICHUR

Scanned by CamScanner



A Workshop on "Matlab/ Simulink With Real Time Applications" was organized by Department of Electronics & Communication Engineering & Department of Electrical & Electronics Engineering on 16th & 17th February – 2018 at Navodaya Institute of technology, Raichur. A total of 100 students and faculties have registered for the workshop.

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR



NARAYANA INSTITUTE OF TECHNOLOGY
RAICHUR

NET's

NAVODAYA INSTITUTE OF TECHNOLOGY

Navodaya Nagar, Bijengera Road, Raichur-584103.

Certificate

This is to certify that ~~Mr~~ / Ms / ~~Dr.~~ Muneeb Afzal C

of Navodaya Institute Of Technology, Raichur has participated in Two days workshop on "MATLAB / SIMULINK with Real Time Applications" held on 16th & 17th February 2018 organized by Department of ECE and EEE, NAVODAYA INSTITUTE OF TECHNOLOGY, Raichur.

Coordinator | HOD,ECE | HOD,EEE | Principal

Scanned with CamScanner

NET's

NAVODAYA INSTITUTE OF TECHNOLOGY, RAICHUR

~~Guest Lecture/Seminar/Add on Course/Faculty Development Programme/Workshop/Conference/Short~~ Term Training/Any Other Proposal

Date: 25-02-2020

1	**ACADEMIC YEAR**	2019-20
2	**TYPE OF THE PROGRAMME PROPOSED**	Faculty Development Programme
3	**TITLE OF THE PROGRAMME:**	**Alternative Building Materials**
4	**BROAD OBJECTIVE OF THE PROGRAMME:**	Stabilized mud blocks, Steam cured blocks, Fine concrete blocks, Rammed earth blocks, Mud concrete blocks, Lime–Pozzolana cements, Soil-lime plaster, Composite mortars for masonry, Composite beam and panel roofs, Reinforced brickwork/tile-work roof, Ferro cement and ferroconcrete roofing systems, Unreinforced masonry vaults and domes, Ribbed slab construction, Filler slab roofs, Rammed earth foundations, Reinforced block-work lintels and precast chejjas, Solar passive cooling techniques and Containment reinforcement for earthquake-
5	**NAME OF THE INSTITUTION**	NIT-Raichur
6	**NAME OF THE DEPARTMENT(S)**	Civil Engineering
7	**BENEFICIARIES**	Faculty Members of Civil Engg Dept
8	**ELIGIBILITY CRITERIA SET:**	----------
9	**NUMBER OF DAYS REQUIRED TO ORGANISE THE PROGRAMME**	5 Days (02-03-2020 To 06-03-2020)
10	**PROPOSED DURATION OF THE PROGRAMME**	5 Days
11	**TOTAL NO. OF PERIODS REQUIRED FOR THE PROGRAMME**	35Hours
12	**NO. OF PERIODS REQUIRED PER DAY**	7

13	**LOCATION DETAILS:**	Auditorium
14	**DETAILS OF SPONSORERS:**	NIT Management
15	**DETAILS OF RESOURCE PERSONS:**	1) Mr. Ravi Kumar H R&D In charge CASHUTEC Shakthinagar 2) Mr.Suresh Patil B Smart Build construction Raichur 584101
16	**REGISTRATION FEE WITH CERTIFICATION:**	, 1000/- (Academician/ Research Scholar), 1500/- (Industry person/ Consultant)
17	**REGISTRATION FEE WITHOUT CERTIFICATION:**	----------
18	**EVENT ORGANISERS (STUDENTS):**	1. Sangeetha 2. Vaibhav V 3. Asma Begum 4. Rohit Karamushe
19	**FACULTY COORDINATOR:**	Mr.Maheshwar B AP/Civil/NITR
20	**PROGRAMME PROPOSED BY:**	Faculty Members of Civil Engg Dept
21	**ANY OTHER RELEVANT MATTER:**	------
22	**ESTIMATED BUDGET IN RS:**	25,000/-

This is to the Honorable Chairman of NIT-R for kind approval:

EVENT COORDINATOR

HOD 25.02.2020

PRINCIPAL

PRINCIPAL
Navodaya Institute of Technology (I.T.)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY
NIT RAICHUR

NAVODAYA INSTITUTE OF TECHNOLOGY

Navodaya Nagar,Mantralayam Road, Raichur-584103
Affiliated to VTU, Belagavi, Karnataka, Approved by AICTE, New Delhi

Date: 25-02-2020

To,
The Principal
NIT- Raichur.

Subject: Requisition for permission to conduct Faculty Development Programme.

Sir,

With reference to above cited subject, it is requested to kindly permit to conduct Faculty Development Programme In our department. The resource person Mr. Ravi Kumar R&D In charge CASHLTEC Shakthinagar & Mr.Suresh Patil B Smart Build construction Raichur Has been consented to be available on 02-03-2020 to 06-03-2020.

Thanking you.

HOD 25.02.2020

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY

Navodaya Nagar,Mantralayam Road, Raichur-584103
Affiliated to VTU, Belagavi, Karnataka, Approved by AICTE, New Delhi

Cir.No-2019-20/FDP/05 Date: 29-02-2020

Circular

A faculty development programme on **Alternative Building Materials** will be delivered by Mr. Ravi Kumar R&D In charge CASHUTEC Shakthinagar & Mr.Suresh Patil B Smart Build construction Raichur. All faculties of civil engineering must attend the faculty development programme without fail.

Venue: NIT Auditorium
Date: 02-03-2020 to 06-03-2020.
Time: 10:00am to 5:00pm

HOD

Copy to:

1. Principal for Kind Information
2. All HoD's and Staff of Civil Engg.
3. Dept. Notice Board

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Faculty Development Program
On
ALTERNATIVE BUILDING MATERIALS
March 2nd to 6th 2020

REGISTRATION FORM:

Name :
Branch :
Designation :
College/Institute :
Address :
Contact no :
Email-id :
Registration Details:
DD no :
Date :
Bank :
Accommodation : Yes ☐ No ☐

Declaration:

This is to certify that the above member is bonafied student / Staff of our organization.

Signature of Candidate

Signature of HOD/ Principal (with seal)

Registration Fee:

Students (UG/PG) outside	: Rs.500/-
Academician / Research Scholar	: Rs.1000/-
Industry Person/Consultant	: Rs.1500/-

The Demand Draft to be drawn in favor of "Principal, NIT Raichur" payable at NIT Campus Raichur.

Chief Patron
Shri. S. R. Reddy,
Hon'ble Chairman
Navodaya Education Trust, Raichur.

Patron:
Dr. M.V.Mallikarjuna
Principal, NIT Raichur.

Convener:
Dr. R Saravanan
Professor & Head
Civil Department - NITR
Contact no: 9865656449

Coordinator:
Mr. Maheshwar S M, AP/Civil / NITR
Contact no: 7411204223
Mail: maheshwar.maregoudru@navodaya.edu.in

Organizing Committee:
Mr. Md. Adil, AP/Civil / NITR
Mr. Ashish P Desai, AP/Civil / NITR
Ms. Shwetha Patil, AP/Civil / NITR
Mr. Rajmohan B, AP/Civil / NITR
Mr. Vijaykumar G, AP/Civil / NITR
Mr. Ramesh B, AP/Civil / NITR
Mr. Nagesh S, AP/Civil / NITR
Ms. Priyanka Patil, AP/Civil / NITR
Mr. Bharath Koner, AP/Civil / NITR
Mr. Santhosh Kumar, AP/Civil / NITR
Mr. Shivalingayya H, AP/Civil / NITR
Mr. Panduranga B, AP/Civil / NITR
Ms. Asmiya K, AP/Civil / NITR
Mr. Dineshkumar, AP/Civil / NITR
Ms. Nafiya, AP/Civil / NITR

Accommodation:
Limited accommodation for the participants will be provided in the institute hostels on prior request (Chargeable basis)

NAVODAYA INSTITUTE OF TECHNOLOGY



NET'S
NAVODAYA INSTITUTE OF TECHNOLOGY
Bijanagera Road, Raichur – 584103

Faculty Development Program
on

ALTERNATIVE BUILDING MATERIALS

March 2nd to 6th 2020

In Association with

Venue: NITR Auditorium

Organized by
Department of Civil Engineering
Navodaya Institute of Technology
Mantralayam Road, Raichur – 584103 Karnataka, India

NAVODAYA INSTITUTE OF TECHNOLOGY

Navodaya Nagar,Mantralayam Road, Raichur-584103
Affiliated to VTU, Belagavi, Karnataka, Approved by AICTE, New Delhi

Five days FDP
On
"ALTERNATIVE BUILDING MATERIALS"
March 2nd to 6th 2020
Organized by
Department of Civil Engineering
SCHEDULE

Date	Time	Topics	Resource Person
02-03-2020	8.30 to 09.15	Registration	
	9.15 to 10.00	Inaugural Function	
	10.00 to 11.00	Introduction to building materials	Mr. Ravi Kumar H
	11.00 to 11.15	Tea Break	
	11.15 to 1.00	Introduction to alternative building materials	Mr. Ravi Kumar H
	1.00 to 2.00	Lunch Break	
	2.00 to 3.15	Stabilized mud blocks, Steam cured blocks,	Mr.Suresh Patil B
	3.15 to 3.30	Tea Break	
	3.30 to 5.00	Fine concrete blocks,	Mr.Suresh Patil B
03-03-2020	9.30 to 11.00	Mud concrete blocks,	Mr. Ravi Kumar H
	11.00 to 11.15	Tea Break	
	11.15 to 1.00	Soil-lime plaster,	Mr. Ravi Kumar H
	1.00 to 2.00	Lunch	
	2.00 to 3.30	Composite beam and panel roofs, Reinforced brickwork/tile-work roof,	Mr.Suresh Patil B
	3.30 to 4.00	Tea Break	
	4.00 to 5.00	Ferro cement and ferroconcrete roofing systems,	Mr.Suresh Patil B
04-03-2020	9.30 to 11.00	Unreinforced masonry vaults and domes	Mr. Ravi Kumar H
	11.00 to 11.15	Tea Break	
	11.15 to 1.00	Ribbed slab construction	Mr. Ravi Kumar H
	1.00 to 2.00	Lunch	

	2.00 to 3.30	Rammed earth foundations,	Mr.Suresh Patil B
	3.30 to 4.00	Tea Break	
	4.00 to 5.00	Reinforced block-work lintels	Mr.Suresh Patil B
05-03-2020	9.30 to 11.00	Solar passive cooling techniques	Mr. Ravi Kumar H
	11.00 to 11.15	Tea Break	
	11.15 to 1.00	precast chejjas,	Mr. Ravi Kumar H
	1.00 to 2.00	Lunch	
	2.00 to 3.30	Containment reinforcement for earthquake-resistant masonry,	Mr.Suresh Patil B
	3.30 to 4.00	Tea Break	
	4.00 to 5.00	Filler slab roofs	Mr.Suresh Patil B
06-03-2020	9.30 to 11.00	Composite mortars for masonry	Mr. Ravi Kumar H
	11.00 to 11.15	Tea Break	
	11.15 to 1.00	Rammed earth blocks	Mr. Ravi Kumar H
	1.00 to 2.00	Lunch	
	2.00 to 3.30	Lime–Pozzolana cements	Mr.Suresh Patil B
	3.30 to 4.00	Tea Break	
	4.00 to 5.00	Valedictory Function	

02.03.2020

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY * RAICHUR

NAVODAYA INSTITUTE OF TECHNOLOGY

Navodaya Nagar,Mantralayam Road, Raichur-584103
Affiliated to VTU, Belagavi, Karnataka, Approved by AICTE, New Delhi

Report

Faculty Development Programme on on "Alternative Building Materials"

Date	02-03-2020 to 06-03-2020.
Organized by	Department of Civil Engineering
Resource person (s)	Mr. Ravi Kumar R&D In charge CASHUTEC Shakthinagar & Mr.Suresh Patil B Smart Build construction Raichur.
Convener	DR.M.V.Mallikarjuna
Coordinators	Mr. Mahaeshwar AP/Civil/NITR
Number of participants	19

Program description

Faculty Development Programme on **Alternative Building Materials** was organized by Department of Civil Engineering on 02-03-2020 to 06-03-2020. Mr. Ravi Kumar & Mr.Suresh Patil B given knowledge on Stabilized mud blocks, Steam cured blocks, Fine concrete blocks, Rammed earth blocks, Mud concrete blocks, Lime–Pozzolana cements, Soil-lime plaster, Composite mortars for masonry, Composite beam and panel roofs, Reinforced brickwork/tile-work roof, Ferro cement and ferroconcrete roofing systems, Unreinforced masonry vaults and domes, Ribbed slab construction, Filler slab roofs, Rammed earth foundations, Reinforced block-work lintels and precast chejjas, Solar passive cooling techniques and Containment reinforcement for earthquake-resistant masonry.

CO-ORDINATOR

06.03.2020

HOD

Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY

Navodaya Nagar,Mantralayam Road, Raichur-584103
Affiliated to VTU, Belagavi, Karnataka, Approved by AICTE, New Delhi

Date: 06-03-2020

To,

The Principal,

NIT – Raichur

Sub: Faculty Development Program expenditure details - Reg

Dear Sir,

With reference to the above citied subject, Faculty Development Program was held in our department during March 2nd to 6th 2020 and the details of expenditure are as given below.

Sl. No	Particular	Qty	Rate	Amount
01	Honorarium to Resource Person	2	-	8000.00
02	Flex/Banner	02	350.00	700.00
03	Folders, Pad & Pens	25	60.00	1500.00
04	Brochure & Certificate Printing	25	25.00	625.00
05	Memento for Resource Persons	02	650.00	1300.00
06	Bouquets to Resource Persons	6	50.00	300.00
07	Water Bottles	15	10.00	150.00
08	Refreshments for Participants	5 days	500	2500.00
09	Lunch for Participants	5 days	600	3000.00
	Miscellaneous			500.00
	Total Amount			18575.00

Amount in words: Eighteen Thousand Five Hundred & Seventy Five Only

Thanking You,

Yours Faithfully

06.03.2020

HOD – CE
NIT – R

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY

Navodaya Nagar,Mantralayam Road, Raichur-584103
Affiliated to VTU, Belagavi, Karnataka, Approved by AICTE, New Delhi

Department of Civil Engineering

CERTIFICATE

This is to certify Mr. Ms. Mrs. ASMIYA KAUSAR *of* NIT - RAICHUR *has participated successfully in the Five days Faculty Development Programme on* "**Alternative Building Materials**" *organized by Department of Civil Engineering, NIT-Raichur held on March 2nd to 6th 2020.*

Co-ordinator ***HOD*** ***Principal***

NET's
NAVODAYA INSTITUTE OF TECHNOLOGY
Department of Civil Engineering

FACULTY FEEDBACK FORM

As part of a continuing improvement process, our college appreciates suggestions and inputs regarding the institution. We request you to sincerely answer these questions under assurance of complete confidentiality. Your interest in making our institution better is greatly appreciated.

Name of Speaker: Ravi kumar H

Program: FDP on Advanced building material

Date: 02.07.20 to 06.07.20

Students are required to rate the speaker on the following attributes using the 6 - point scale shown.

Parameter	Excellent	Very Good	Good	Satisfactory
Your perception of his/her knowledge	✓			
Communication skills		✓		
Sincerity/commitment		✓		
Ability to make the subject relevant to profession and life	✓			
The material presented in an organized manner		✓		
Overall rating		✓		

Signature

06.07.2020
Head of Department
Civil Engineering
Navodaya Institute of Technology (NIT
RAICHUR-584 103

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

NAVODAYA INSTITUTE OF TECHNOLOGY

Navodaya Nagar,Mantralayam Road, Raichur-584103
Affiliated to VTU, Belagavi, Karnataka, Approved by AICTE, New Delhi

List of Participants In faculty Development Programme on "Alternative Building Materials"

SL.NO	NAME	SIGN 02-03-2020	SIGN 03-03-2020	SIGN 04-03-2020	SIGN 05-03-2020	SIGN 06-03-2020
1	Mr. Md Adil					
2	Mr. Ashish P Desai				-	-
3	Mr. Rajmohan B					
4	Ms. Shwetha Patil					
5	Mr. Vijaykumar G					
6	Mr.Nagesh Sugandhi					
7	Ms. Priyanaka Patil	-	-	-	-	-
8	Mr. Bharath Koner	-				
9	Mr. Santhosh Kumar					
10	Mr. Ramesh Bargani	-				-
11	Mr. Maheshwar M	-	-	-	-	-
12	Mr. Panduranga B					
15	Ms.Nisarga					
16	Ms. Asmiya Kausar					
17	Ms. Nafiya	-	-	-	-	-
18	Mr. Dineshkumar K G					

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

NET's

NAVODAYA INSTITUTE OF TECHNOLOGY, RAICHUR

~~*Guest Lecture/Seminar/Add on Course/Faculty Development Programme/Workshop/Conference/Short Term Training/Any Other Proposal*~~

Date: 14-03-2018

1	**ACADEMIC YEAR**	2017-2018
2	**TYPE OF THE PROGRAMME PROPOSED**	Faculty Development Programme
3	**TITLE OF THE PROGRAMME:**	**"Sustainable Practice In Geotechnical And Structural Engineering"**
4	**BROAD OBJECTIVE OF THE PROGRAMME:**	The main objective is to enhance the knowledge of students and faculties on analysis, design and quality checking of materials of Civil Engineering structures such as buildings, foundation, bridges, and offshore structures. 1) To understand the different aspects related to seismology and terms related to it. 2) Design and analysis of foundation guidelines based on seismic zones. 3) To understand innovative and energy efficiency ground improvement techniques, stability of slopes in foundation analysis. 4) To provide the participants a strong foundations
5	**NAME OF THE INSTITUTION**	NIT-Raichur
6	**NAME OF THE DEPARTMENT(S)**	Civil Engineering
7	**ELIGIBILITY CRITERIA SET:**	----------
8	**NUMBER OF DAYS REQUIRED TO ORGANISE THE PROGRAMME**	5 Days (19-03-2018 To 23-03-2018)
9	**PROPOSED DURATION OF THE PROGRAMME**	5 Days
10	**TOTAL NO. OF PERIODS REQUIRED FOR THE PROGRAMME**	35 Hours
11	**NO. OF PERIODS REQUIRED PER DAY**	7
12	**LOCATION DETAILS:**	Auditorium

13	DETAILS OF SPONSORERS:	NIT Management
14	DETAILS OF RESOURCE PERSONS:	Dr. Saravanan R Assistant Professor at Sona College of Technology, Salem Mr Suresh Patil B.E., M.Tech Structural Engineer Smart Build Developers Raichur 584101
15	REGISTRATION FEE WITH CERTIFICATION:	1000/- (Academician/ Research Scholar), 1500/- (Industry person/ Consultant)
16	REGISTRATION FEE WITHOUT CERTIFICATION:	------
17	EVENT ORGANISERS (STUDENTS):	Mr.Shivasharanappa Mr.Md. Abdul Ayaz Ms.Pallavi Aishwarya Hadimani Mr.A.Dinesh Ms.Laxmi Jannkar
18	FACULTY COORDINATOR:	Mr. Rajmohan B AP/Civil/NITR
19	PROGRAMME PROPOSED BY:	Civil Department Faculties
20	ANY OTHER RELEVANT MATTER:	------
21	ESTIMATED BUDGET IN RS:	25,000/-

This is to the Honorable Chairman of NIT-R for kind approval.

EVENT COORDINATOR

PRINCIPAL 14/3/18

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA

Navodaya Nagar,Mantralayam Road, Raichur-584103
Affiliated to VTU, Belagavi, Karnataka, Approved by AICTE, New Delhi

Department of Civil Engineering

Date: 14-03-2018

To,
The Principal
NIT- Raichur.

Subject: Requisition for permission to conduct Faculty Development Programme.

Sir,

With reference to above cited subject, it is requested to kindly permit to conduct Faculty Development Programme. in our department. The resource person Dr.Saravanan R & Mr.Suresh Patil has been consented to be available from 19-03-2018 to 23-03-2018

Thanking you.

HOD

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Contact No.: +91-7892343745 | Email Id: sureshpatil5588@gmail.com

Seeking challenging assignments in spearheading Civil Projects Design and Execution Management

Location Preference: Bangalore

PROFILE SUMMARY

- Hardworking professional with **6 years** of experience in making models, structure and designing of residential building and apartments.
- Presently working with ***Altitudez Design and Construction, Bangalore as Structural Engineer.***
- Expert in designing and executing construction projects with a flair for adopting modern construction methodologies, complying with quality standards.
- Experience in seismic design of structures using computer analysis and proof checking.
- Knowledge of survey, investigation, project execution, quality control, quality assurance, material testing, inspection, construction supervision construction procedure, financial control and institutional strengthening.
- Deft in having close coordination with contractors, consultants and other external agencies while ensuring on time deliverables within deadlines.
- Effective communicator with decision making and interpersonal skills with ability to network with project members, consultants, contractors, with consummate ease.

CORE COMPETENCIES

Project Design and Execution	**Site Engineering**	**Civil Engineering**
Construction Management	**Quality Assurance**	**Contract / Tender Management**
Liaison & Negotiation	**Documentation**	**Team Coordination**

CAREER SCAN

Since Mar 2015 **Altitudez Design and Construction, Bangalore** **Structural Engineer**

Key Deliverables:

- ✓ Conducting project meetings with customers in relation to the project design, scheduling, progress, quality and debottlenecking of execution constraints.
- ✓ Framing architectural decisions and vision in areas of product design & development.
- ✓ Designing check of residential building, apartments using E-tabs and SAFE and creating models.
- ✓ Estimating material, construction, and labour costs while working on detailed drawings for construction using RCDC software.
- ✓ Anchoring on-site construction activities to ensure completion of project within the defined parameters and effective resource utilization to maximize the output.
- ✓ Executing contracting works and approving sub contractors bills, claims, etc.; involving in vendor analysis and selection based on technical and financial competencies.
- ✓ Maintaining perfect coordination with architects, consultants, clients, subcontractors, and site engineers, etc. and providing clearance of any technical / commercial ambiguities to catalyze project implementation.
- ✓ Coordinating manpower planning for competency building for implementation of new projects.

Feb 2014-Feb 2015 **E-CONSTRUCT Design and Build Pvt. Ltd., Bangalore** **Structural Engineer**

Key Deliverables:

- ✓ Developed models and designing of residential building, apartments using E-tabs and SAFE as per IS 456 code.
- ✓ Managed proof checking work consisted of five towers of G+10 building with a strong experience of seismic design of structures using computer analysis, FEM and non-linear dynamic analysis.
- ✓ Presenting powerpoint to managers and clients after working on its design.
- ✓ Performed structural inspection and made appropriate recommendations.
- ✓ Ensured machinery/ equipment/ material procurement is in accordance with schedule, quantity and quality.

EDUCATION

2013	**Masters in Technology- Structural Engineering** from Vishveshwaraya Technological University, Belgaum, MVJ College of Engineering, Bangalore
2011	**Bachelor of Engineering- Civil Engineering** from Vishveshwaraya Technological University, Belgaum, BEC, Bagalkot
2006	XII from Department of Pre-university Board, Vidya bharati, P U College, Raichur
2004	X from State Board, Vidya Bharati, P U College, Raichur

IT Skills:

Interested subjects	Analysis and design of RCC structures and high rise buildings
Software's known	ETABS, SAFE, SAP, PROKON, AUTOCAD, RCDC

PERSONAL DETAILS

Date of Birth	05 May, 1988
Languages Known	English and Hindi
Address	1-38/48 Basaveswara Colony, Lingasugur Road, Raichur
References	Available on request

NAVODAYA

Navodaya Nagar,Mantralayam Road, Raichur-584103
Affiliated to VTU, Belagavi, Karnataka, Approved by AICTE, New Delhi

Cir.No- 2017-18/ CDP /03

Date: 14-03-2018

Circular

A faculty development programme on sustainable practice in geotechnical and structural engineering will be delivered by Dr. Saravanan R & Mr.Suresh Patil. All the Faculties of civil engineering department must attend the faculty development programme without fail.

Venue: NIT Auditorium
Date: 19-03-2018 to 23-03-2018
Time: 10:00am to 5:00pm

HOD

Head of Department
Civil Engineering
Navodaya Institute of Technology
RAICHUR-584 103

Copy to:

1. Principal for Kind Information
2. All HoD's and Staff of Civil Engg.
3. Dept. Notice Board

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY • RAICHUR

Navodaya Nagar,Mantralayam Road, Raichur-584103
Affiliated to VTU, Belagavi, Karnataka, Approved by AICTE, New Delhi

Five days FDP
On

'Sustainable Practice in Geotechnical and Structural Engineering

19-03-2018 to 23-03-2018
Organized by
Department of Civil Engineering

SCHEDULE

Date	Time	Topics	Resource Person
19-03-2018	8.30 to 09.15	Registration	
	9.15 to 10.00	Inaugural Function	
	10.00 to 11.00	Role of SSI in seismic response analysis of massive masonry structures	Dr. Saravanan R
	11.00 to 11.15	Tea Break	
	11.15 to 1.00	Role of SSI in seismic response analysis of massive masonry structures	Dr. Saravanan R
	1.00 to 2.00	Lunch Break	
	2.00 to 3.15	Design of Foundations for Earthquake Loading	Mr.Suresh Patil
	3.15 to 3.30	Tea Break	
	3.30 to 5.00	Design of Foundations for Earthquake Loading	Mr.Suresh Patil
20-03-2018	9.30 to 11.00	Sustainable Practices in Concrete	Dr. Saravanan R
	11.00 to 11.15	Tea Break	
	11.15 to 1.00	Sustainable Practices in Concrete	Dr. Saravanan R
	1.00 to 2.00	Lunch	
	2.00 to 3.30	Sustainable Practices in Concrete	Mr.Suresh Patil
	3.30 to 4.00	Tea Break	
	4.00 to 5.00	Sustainable Practices in Concrete	
21-03-2018	9.30 to 11.00	Aspects related to seismology and terms related to it.	Dr. Saravanan R
	11.00 to 11.15	Tea Break	
	11.15 to 1.00	Aspects related to seismology and terms	Dr. Saravanan R

		related to it.	
	1.00 to 2.00	Lunch	
	2.00 to 3.30	Design and analysis of foundation guidelines based on seismic zones.	Dr. Saravanan R
	3.30 to 4.00	Tea Break	
	4.00 to 5.00	Design and analysis of foundation guidelines based on seismic zones.	Mr.Suresh Patil
	9.30 to 11.00	Innovative and energy efficiency ground improvement techniques, stability of slopes in foundation analysis.	Dr. Saravanan R
	11.00 to 11.15	Tea Break	
	11.15 to 1.00	Innovative and energy efficiency ground improvement techniques, stability of slopes in foundation analysis.	Dr. Saravanan R
22-03-2018	1.00 to 2.00	Lunch	
	2.00 to 3.30	Innovative and energy efficiency ground improvement techniques, stability of slopes in foundation analysis.	Mr.Suresh Patil
	3.30 to 4.00	Tea Break	
	4.00 to 5.00	Innovative and energy efficiency ground improvement techniques, stability of slopes in foundation analysis.	Mr.Suresh Patil
	9.30 to 11.00	Participants a strong foundations to the testing techniques through interactive sessions and demonstrations.	Dr. Saravanan R
	11.00 to 11.15	Tea Break	
	11.15 to 1.00	Participants strong foundations to the testing techniques through interactive sessions and demonstrations.	Dr. Saravanan R
23-03-2018	1.00 to 2.00	Lunch	
	2.00 to 3.30	Participants a strong foundations to the testing techniques through interactive sessions and demonstrations.	Mr.Suresh Patil
	3.30 to 4.00	Tea Break	
	4.00 to 5.00	Valedictory Function	

CO-ORDINATOR

HOD

Dept. of Civil Engineering
Navodaya Institute of Technology
Raichur-584 103

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY • RAICHUR •

Navodaya Nagar, Mantralayam Road, Raichur-584103
Affiliated to VTU, Belagavi, Karnataka, Approved by AICTE, New Delhi

List of Participants In faculty Development Programme on "Sustainable Practice in Geotechnical and Structural Engineering"

SL.NO	NAME	Sign 19-03-2018	Sign 20-03-2018	Sign 21-03-2018	Sign 22-03-2018	Sign 23-03-2018
1	Dr. M S Shiva Reddy					
2	Mr. Kristaiah Shetty					
3	Mr. Veerupaxappa					
4	Ms. Suma K S					
5	Mr. Md Adil					
6	Mr. Ashish P Desai					
7	Mr. Rajmohan B					
8	Ms. Shwetha Patil					
9	Mr. Vijaykumar G					
10	Mrs.Priyanka Desai					
11	Mr.Nagesh Sugandhi					
12	Mr. Suryanarayana					
13	Ms. Shruthi Shivaraj					
14	Ms. Priyanaka Patil					
15	Mr. Bharath Koner					
16	Mr. Santosh Kumar					
17	Mr.Salauddin					
18	Ms. Channabassamma N					

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Navodaya Nagar,Mantralayam Road, Raichur-584103
Affiliated to VTU, Belagavi, Karnataka, Approved by AICTE, New Delhi

Department of Civil Engineering

Report

Faculty Development Programme on "Sustainable Practice in Geotechnical and Structural Engineering"

Date	19-03-2018 to 23-03-2018
Organized by	Department of Civil Engineering
Resource person (s)	Dr. Saravanan R Assistant Professor at Sona College of Technology, Salem Mr.Suresh Patil B.E., M.Tech Structural Engineer Smart Build Developers Raichur 584101
Convener	DR.M.V.Mallikarjuna
Coordinators	Mr. Rajmohan B AP/Civil/NITR
Number of participants	18

Program description

Faculty Development Programme on **SUSTAINABLE PRACTICE IN GEOTECHNICAL & STRUCTURAL ENGINEERING** was organized by Department of Civil Engineering from 19-03-2018 to 23-03-2018.DR. Saravanan R & Mr.Suresh Patil given knowledge of the primary objective of this course is to teach students the essentials of strong foundations to the testing techniques through interactive sessions and demonstrations.

CO-ORDINATOR

HOD

PRINCIPAL
Navodaya Institute of Technology (NIT,
RAICHUR-584 103

NAVODAYA

Navodaya Nagar,Mantralayam Road, Raichur-584103
Affiliated to VTU, Belagavi, Karnataka, Approved by AICTE, New Delhi

Department of Civil Engineering

FACULTY FEEDBACK FORM

As part of a continuing improvement process, our college appreciates suggestions and inputs regarding the institution. We request you to sincerely answer these questions under assurance of complete confidentiality. Your interest in making our institution better is greatly appreciated.

Name of Speaker: Dr. Ramanagouda

Program: FDP on Pavement Materials Characterization and Design

Date: 19.03.2018 to 23.03.2018.

Students are required to rate the speaker on the following attributes using the 6 - point scale shown

Parameter	Excellent	Very Good	Good	Satisfactory
Your perception of his/her knowledge	✓			
Communication skills	✓			
Sincerity/commitment	✓			
Ability to make the subject relevant to profession and life		✓		
The material presented in an organized manner	✓			
Overall rating	✓			

Signature

Head of Department
Civil Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

PRINCIPAL
Navodaya Institute of Technology (NIT,
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

NAVODAYA

Navodaya Nagar, Mantralayam Road, Raichur 584103
Affiliated to VTU, Belagavi, Karnataka, Approved by AICTE, New Delhi

Date: 31.03.2018

To,

The Principal,
NIT Raichur

Sub: Faculty Development Program expenditure details - Reg

Dear Sir,

With reference to the above cited subject, Faculty Development Program was held in our department during March 19th to 21st 2018 and the details of expenditure are as given below.

Sl. No	Particular	Qty	Rate	Amount
01	Honorarium to Resource Person			6000
02	Flex/Banner	2	350	700
03	Folders, Pad & Pens	20	60	1200
04	Brochure & Certificate Printing	20	60	1200
05	Memento for Resource Persons	2	700	1400
06	Bouquets to Resource Persons	6	50	300
07	Water Bottles	30	10	300
08	Refreshments for Participants	5 days	500	2500
09	Lunch for Participants	5 days	600	3000
	Miscellaneous			500
	Total Amount			17100

Amount in words: seventeen Thousand one Hundred Only

Thanking You,

Yours Faithfully

HOD - CSE
NIT Raichur

PRINCIPAL
Navodaya Institute of Technology (NIT),
RAICHUR 584 103

Navodaya Nagar, Mantralayam Road, Raichur-584103
Affiliated to VTU, Belagavi, Karnataka, Approved by AICTE, New Delhi

Department of Civil Engineering

Certificate

This is to certify Mr/ Ms/ Mrs MD. ADIL *of* NIT-RAICHUR *has participated successfully in the Five days Faculty Development Programme on* "**SUSTAINABLE PRACTICE IN GEOTECHNICAL & STRUCTURAL ENGINEERING**" *organized by Department of Civil Engineering, NIT-R in association with CASHUTEC – Raichur held on 19th to 23rd March 2018.*

Co-ordinator *HOD* *Principal*

SUSTAINABLE PRACTICES IN GEOTECHNICAL AND STRUCTURAL ENGINEERING

FIVE DAYS FACULTY DEVELOPMENT

PROGRAMME REGISTRATION FORM

Name :

Branch :

Designation :

College/Institute :

Address :

Contact no :

Email-id :

Registration Details:

DDno :

Date :

Bank :

Accommodation :Yes ☐ No ☐

Declaration:

This is to certify that the above member is bonafied student / Staff of our organization.

Signature of Candidate

Signature of HOD/ Principal (with seal)

RegistrationFee:

Academician / Research Scholar :Rs.1000/-
Industry Person/Consultant :Rs.1500/-

The Demand Draft to be drawn in favor of "Principal, NIT Raichur" payable at NIT Campus Raichur.

Chief Patron
Shri. S. R. Reddy,
Hon'ble Chairman
Navodaya Education Trust, Raichur.

Patron:
Dr. M.V.Mallikarjuna
Principal, NIT Raichur.

Convener:
Dr. M.V.Mallikarjuna
Principal, NIT Raichur.

Coordinator:
Mr. Rajmohan B, AP/Civil / NITR
Contact no: 9964661205
Mail: rajmohan.rcr@gmail.com

Students Organizing Committee:

Mr.Shivasharanappa
Mr.Md. Abdul Ayaz
Ms.Pallavi
Aishwarya Hadimani
Mr.A Dinesh
Ms.Laxmi Janakar

Accommodation:
Limited accommodation for the participants will be provided in the institute hostels on prior request (Chargeable basis)

www.navodayaeducationtrust.com
navodayaeducationtrust

NAVODAYA INSTITUTE OF TECHNOLOGY



NET'S
NAVODAYA INSTITUTE OF TECHNOLOGY
Bijanagera Road, Raichur –584103

FIVE DAYS FACULTY DEVELOPMENT PROGRAMME
On

SUSTAINABLE PRACTICES IN GEOTECHNICAL AND STRUCTURAL ENGINEERING,

MARCH 19th to 23rd of 2018

Venue: NITR Auditorium

Organized by
Department of Civil Engineering
Navodaya Institute of Technology
Mantralayam Road, Raichur – 584103.Karnataka. India

www.navodayaeducationtrust.com
navodayaeducationtrust



ABOUT THE DEPARTMENT

Civil Engineering Department at Navodaya Institute of Technology, Raichur was established in the year of 2010 with an intake of 60. The Civil Engineering department aims to produce graduate engineers excellent for careers in the public and private sectors encompassing in all areas of Civil Engineering.

The faculty is well trained in all major investigations in civil engineering and well equipped state-of-art laboratories and computing facilities are the resources of the department. Apart from the curriculum, students are encouraged to take up mini projects on various disciplines. The department also offers consultancy services for soil testing and concrete testing to construction sites in and around Raichur. The faculty members of the department are dedicated to provide solutions to the people in need in the areas of structures, soils, surveying, concrete, materials.

The Department also encourages its students to engage in extra-curricular and co-curricular activities. Technical Festivals and National & International Conferences and Annual Alumni Meets thus ensuring the overall development, nurturing of team spirit and organizational skills.

VISION

To become a centre of excellence in Civil Engineering producing innovative and creative Civil Engineers to meet the global challenges.

MISSION

To provide a platform to the students towards attaining quality education in Civil Engineering.

To educate students about professional & ethical responsibilities and train them to build leadership and entrepreneurship qualities for their career development.

To create opportunities and guide students in acquiring career oriented jobs in the field of Civil Engineering.

ABOUT THE WORKSHOP

The main objective is to enhance the knowledge of students and faculties on analysis, design and quality checking of materials of Civil Engineering structures such as buildings, foundation, bridges, and offshore structures.

PRINCIPAL
Navodaya Institute of Technology (Rtr.)
RAICHUR-584 103

OBJECTIVES

To understand the different aspects related to knowledge and issues related to it.

Design and analysis of foundation guidelines based on various issues.

To understand innovative and energy efficiency ground improvement techniques, stability of slopes in foundation analysis.

To provide the participants a strong foundations to the testing techniques through interactive sessions and demonstrations.

RESOURCE PERSONS:

Dr. Saravanan R

Assistant Professor
Sona College of Technology,
Salem, Tamil Nadu

Mr.Suresh Patil B.E, M.Tech
Structural Engineer
Smart Build Developers Raichur 584101

TARGET AUDIENCE

This Five-day Faculty Development Programme is designed for the professionals working as

- PGStudents
- ResearchProfessionals
- Academic Faculty Members
- Consultants
- Industrialists

IMPORTANT DATES

Last date for registration: 17-03-2018

FDP DATE – MARCH 19th to 23rd of 2018

Confirmation e-mail to be sent on 20-03-2018

Spot registration will be there on the day of event

No TA/DA will be provided.

Date:02/05/2017

From,

Head of Department,
Department of Civil Engg,
Navodaya Institute of Technology,
Raichur

To,

The Dean & Principal,
Navodaya Institute of Technology,
Raichur

Respected Sir,

Sub: Requisition for funding to conduct the Faculty Development Program - Reg.

I bring to your kind notice that we are conducting the FDP on '**Surveying using Total Station**' in our department on 5th to 7th May 2017. The estimated budget of FDP is Rs. 27,700/- (Rupees Twenty Seven Thousand and Seven Hundred only). Details of budget for FDP are enclosed herewith. I request you to provide the amount. Same will be settled immediately after the completion of FDP.

Thanking you,

Sl. No.	Description	Amount (Rs.)
1	Remuneration including TA & DA for the resource person 1. Dr. Prabhu G Rakaraddi, Professor, Department of Civil Engineering, Basaveshwar Engineering College, Bagalokot 2. Mr. Vivek R Patil, Asst. Professor Dept. of Civil Engg., KLECET, Chikodi Belagavi 3. Mr. Sampath Kumar, AIMIL Limited, Bengaluru 4. Mr. Bhagyesh, Lawrence & Mayo (India) Pvt. Ltd., Bengaluru	20000.00
2	Flex (Rs. 350 × 2 nos)	700.00
3	Refreshments for Participants (Tea & Snacks) (Approx. 25 × 10 × 4)	1000.00
4	Food for Participants (25 × 60 × 2)	3000.00
5	Brochure & Certificate Printing (40 × 25)	1000.00
6	Conference Pad, Pen & Folders for Participants	2000.00
	TOTAL (Rupees Twenty Seven Thousand and Seven Hundred only)	**27700.00**

Your's faithfully

Dr. Shiva Reddy M S
HOD_CE

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Head of Department
Civil Engineering
Navodaya Institute of Technology
RAICHUR-584 103

NET's

NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

Faculty development program

Date: 02/05/2017

1	ACADEMIC YEAR	2016-17
2	TYPE OF THE PROGRAMME PROPOSED	**Faculty development program**
3	TITLE OF THE PROGRAMME:	**'Surveying using Total Station**
4	BROAD OBJECTIVE OF THE PROGRAMME:	Total Station in Civil Engineering projects for various purposes such as column marking, centre line marking for roads and canals, area computations, earthwork calculations etc. They also provided enough theoretical aspects and working knowledge
5	NAME OF THE INSTITUTION	Navodaya Institute of Technology -Raichur
6	NAME OF THE DEPARTMENT(S)	Civil Engineering
7	BENEFICIARY	'Faculty Members of Civil Engg Dept
8	ELIGIBILITY CRITERIA SET:	Civil Engineering
9	NUMBER OF DAYS REQUIRED TO ORGANISE THE PROGRAMME	03 DAY(5th to 7th May 2017)
10	PROPOSED DURATION OF THE PROGRAMME	03 DAY
11	TOTAL NO. OF PERIODS REQUIRED FOR THE PROGRAMME	18 Hours
12	NO. OF PERIODS REQUIRED PER DAY	06
13	LOCATION DETAILS:	Auditorium

14	**DETAILS OF SPONSORERS:**	NIT Management
15	**DETAILS OF RESOURCE PERSONS:**	1. Dr. Prabhu G Rakaraddi, Professor, Department of Civil Engineering, Basaveshwar Engineering College, Bagalokot 2. Mr. Vivek R Patil, Asst. Professor Dept. of Civil Engg., KLECET, Chikodi Belagavi 3. Mr. Sampath Kumar, AIMIL Limited, Bengaluru 4. Mr. Bhagyesh, Lawrence & Mayo (India) Pvt. Ltd., Bengaluru
16	**REGISTRATION FEE**	- Academicians: Rs.500 Industry Professionals: Rs.1000
17	**EVENT ORGANISERS (STUDENTS):**	Ms.Shruthi Ms.Sneha Mr.Adarsh S Mr.Amberesh
18	**FACULTY COORDINATOR:**	Mr.Rajmohan B
19	**PROGRAMME PROPOSED BY:**	Faculty Members of Civil Engg Dept
20	**ANY OTHER RELEVANT MATTER:**	-
21	**ESTIMATED BUDGET IN RS:**	Total Budget – 27700/-

This is to the Honorable Chairman of NIT for kind approval:

PRINCIPAL

HOD

Head of Department
Civil Engineering
Navodaya Institute of Technology (IoT)
RAICHUR-584 103

EVENT COORDINATOR

PRINCIPAL
Navodaya Institute of Technology (IoT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR AUTONOMY

Navodaya Nagar Mantralayam Road, Raichur-584103
Affiliated to VTU, Belagavi, Karnataka, Approved by AICTE, New Delhi

Cir.No: 2016-17/FDP/02 Date: 04-05-2017

Circular

Faculty Development Program on **surveying using Total Station**' will be delivered Dr. Prabhu G Rakaraddi, Professor, Department of Civil Engineering, Basaveshwar Engineering College, Bagalokot & Mr. Vivek R Patil, Asst. Professor, KLECET, Chikodi Belagavi. All the faculties of civil engineering department must attend the lecture without fail.

Venue: NIT Auditorium
Date: 05-05-2017
Time: 10:30am to 5:00pm

HOD

Copy to:

1. Principal for Kind Information
2. All HoD's and Staff of Civil Engg
3. Dept. Notice Board

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

INVITATION

Department of Civil Engineering

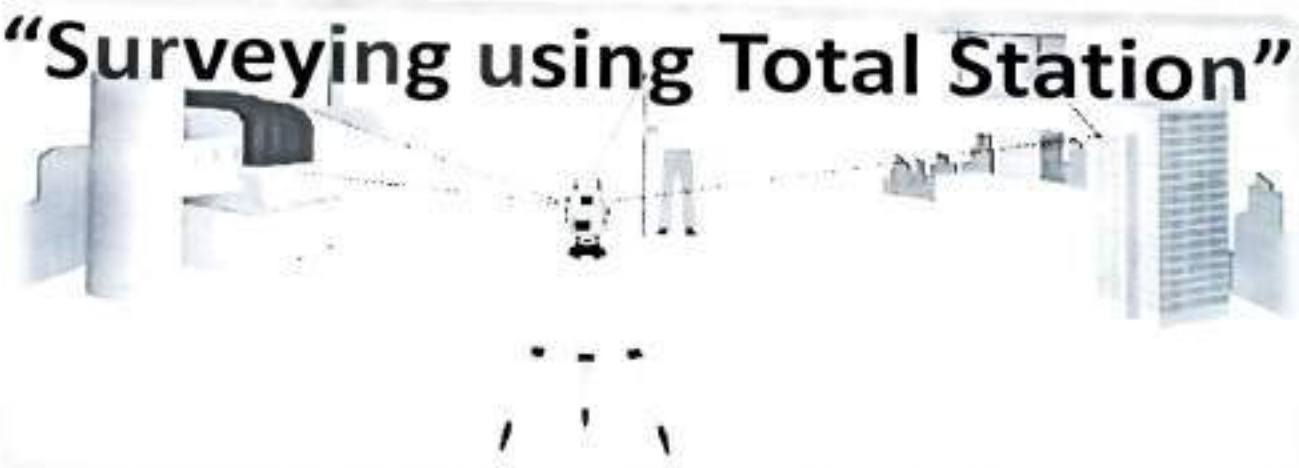
Welcomes all the HODs & Faculty members

To

Faculty Development Program

on

"Surveying using Total Station"



Venue: Auditorium, NIT-R.

Date: 5th to 7th May - 2017

Navodaya Nagar,Mantralayam Road, Raichur-584103
Affiliated to VTU, Belagavi, Karnataka, Approved by AICTE, New Delhi

Department of Civil Engineering

Report

Faculty development program on "Surveying using total station"

Date	5th, 6th & 7th May 2017
Organized by	Department of Civil Engineering
Resource person (s)	Dr.Prabhu G. Rakaraddi, Professor, Dept. of Civil Engg.., BEC Bagalkot, Mr.Vivek R Patil Assistant Professor, Civil Engineering Department, KLE College of Engineering & Technology Chikodi, Mr. Natarajan & Mr.Sampath AIMIL Limited Bangalore and Mr. Bhagyesh Lawrence & Mayo(India).Pvt Ltd, Bagalore
Convener	Dr. Shivpraksh C K
Coordinators	Rajmohan B
Number of participants	21

Program description

The Department of Civil Engineering has organized the faculty development program on "Surveying using total station" on 5th, 6th & 7th May 2017 in Navodaya Institute of Technology, Seminar Hall. The Resource Persons were Dr.Prabhu G. Rakaraddi, Professor, Dept. of Civil

Engg., BLDE Bagalkot, Mr.Vivek R Patil Assistant Professor, Civil Engineering Department, KLE College of Engineering & Technology Chikodi, Mr. Natarajan & Mr.Sampath AIMIL Limited Bangalore and Mr. Bhagyesh Lawrence & Mayo(India) Pvt Ltd, Bagalore

The event started with a Welcome Address. The program was preceded by a short talk about the Introduction of Resource Person followed by a Lecture. They emphasized on a wide use of Total Station in Civil Engineering projects for various purposes such as column marking, centre line marking for roads and canals, area computations, earthwork calculations etc. They also provided enough theoretical aspects and working knowledge on surveying using Total Station. It has helped participants to learn technical skill through hands on learning activities and technical concepts involved in civil engineering.

CO-ORDINATOR

HOD

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Date: 27-05-2017

To,

The Principal,

NIT Raichur

Sub: Faculty Development Program expenditure details - Reg

Dear Sir,

With reference to the above citied subject, Faculty Development Program was held in our department during 5th to 7th May 2017 and the details of expenditure are as given below.

Sl. No	Particular	Qty	Rate	Amount
01	Flex/Banner	02	250.00	500.00
02	Folders, Pad & Pens	25	56.00	1400.00
03	Brochure & Certificate Printing	55	20.00	1100.00
04	Memento for Resource Persons	04	650.00	2600.00
05	Accommodation for Resource Persons	2 days	-	2110.00
06	Bouquets to Resource Persons	6	50.00	300.00
07	Water Bottles	15	10.00	150.00
08	Refreshments for Participants	2 days	-	3200.00
09	Lunch for Participants	2 days	-	1190.00
10	Conveyance Charges for Resource Persons	2	-	4500.00
11	Honorarium for Resource Persons	2		8500.00
12	Miscellaneous			200.00
	Total Amount Spent (A)			25750.00
	Amount Collected from Participants (B)	4	500.00	2000.00
	Net Amount Spent (C = A-B)			23750.00
	Advance Amount Received through Cheque (D)			17240.00
	Amount to be reimbursed (E = C-D)			6510.00

Amount in words: Six thousand five hundred and ten rupees only.

Thanking You,

Yours Faithfully

HOD CE
NIT R

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Navodaya Nagar, Mantralayam Road, Raichur 584103
Affiliated to VTU, Belagavi, Karnataka, Approved by AICTE, New Delhi

Department of Civil Engineering

FACULTY DEVELOPMENT PROGRAM
On
"SURVEYING USING TOTAL STATION"
5th, 6th & 7th May 2017

Organized by
Department of Civil Engineering

SCHEDULE

Date	Time	Session	Topics	Resource Person
05/05/2017	9.00 to 09.30		Registration	
	9.30 to 10.00		Inaugural Function	
	10.00 to 11.00	I	Introduction to Total Station	Mr. Bhagyesh
	11.00 to 11.15		Tea Break	
	11.15 to 1.00	II	Basic Operations of Total Station	Mr. Bhagyesh
	1.00 to 2.00		Lunch Break	
	2.00 to 3.15	III	Station Setup - Theory & Practical	Mr. Vivek R Patil
	3.15 to 3.30		Tea Break	
	3.30 to 5.00	IV	Resection - Theory & Practical	Mr. Sampath Kumar
06/05/2017	9.30 to 11.00	I	Stake out - Theory & Practical	Mr. Vivek R Patil
	11.00 to 11.15		Tea Break	
	11.15 to 1.00	II	Measure Topo - Theory & Practical	Mr. Vivek R Patil
	1.00 to 2.00		Lunch	
	2.00 to 3.15	III	Surveying Techniques for Major Civil Engineering Projects	Dr. P G Rakaraddi
	3.15 to 3.30		Tea Break	
	3.30 to 4.30	IV	Data Interpretation	Mr. Vivek R Patil
	4.30 to 5.00		Valedictory Function	
07/05/2017	10.00 to 1.00	I	Hands on Experience/Practice session	Mr. Vivek R Patil

Navodaya Institute of Technology
RAICHUR-584 103

DEPARTMENT OF CIVIL ENGINEERING

Acknowledgement

Received Rs. 5,000 - (Five Thousand Only) as honorarium and Rs.2,000 - (Two Thousand Only) towards conveyance for having attended the FDP on "Surveying using Total Station", as a resource person at Navodaya Institute of Technology, Raichur

Name : Dr. Prabhu G Rakaraddi

Signature:

Designation : Professor

Date : 06-05-2017
Place : Raichur

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

DEPARTMENT OF CIVIL ENGINEERING

Acknowledgement

Received Rs. 3,500/- (Three Thousand Five Hundred Only) as honorarium and Rs.2,500/- (Two Thousand Five Hundred Only) towards conveyance for having attended the FDP on "Surveying using Total Station", as a resource person at Navodaya Institute of Technology, Raichur.

Name : Mr. Vivek Patil

Signature:

Designation : Assistant Professor

Date : 06-05-2017
Place : Raichur

NAVODAYA
Institute of Technology
RAICHUR

Navodaya Nagar,Mantralayam Road, Raichur-584103
Affiliated to VTU, Belagavi, Karnataka, Approved by AICTE, New Delhi

Department of Civil Engineering

List of Participants in FDP "Surveying Using Total Station"

S.NO	NAME	Sign 05-05-2017	Sign 06-05-2017	Sign 07-05-2017
1	Dr M S Shiva Reddy		-	-
2	Dr. Shivprakash CK	-	-	-
3	Mr. Kristaiah Shetty			
4	Mr. Virupaxappa			
5	Ms. Shambhavi Tadkal	-	-	-
6	Ms. Suma K S	-	-	-
7	Mr. Md Adil			
8	Mr. Ashish P Desai			
9	Mr. Rajmohan B			
10	Ms. Shwetha Patil	-		
11	Mr. Vijaykumar G			
12	Mrs.Priyanka Desai			
13	Mr.Nagesh Sugandhi			
14	Mr. Suryanarayana			
15	Ms. Channabassamma N			
16	Ms. Nandini			

17	Sachin Kulkarni			
18	Ravi Rivankar			
19	Shanti Kumar			

CO-ORDINATOR

HOD

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

NET's
NAVODAYA
Institute of Technology
RAICHUR
Navodaya Nagar, Bijanagera Road
RAICHUR 584 103, Karnataka INDIA
Ph: +91-8532 240334/ 240115
Fax: 240054/ 240061/ 223070
E-mail: nit.princi@gmail.com
web: www.navodaya.edu.in

Date:27/04/2017

To,
Mr. Vivek R Patil
Assistant Professor,
Department of Civil Engineering,
KLE College of Engineering & Technology,
Chikodi- 591201

Dear Sir,

Greetings from Navodaya Institute of Technology - Raichur

We are conducting a Faculty Development Program in our institution on"Surveying Using Total Station during 5th, 6th& 7th May 2017. In this regard, we would like to extend our warm invitation, to welcome you as a resource person for theabove said program.

Thanking You,

Your's faithfully

HOD
Department of Civil Engineering
NIT-Raichur

NET's

NAVODAYA INSTITUTE OF TECHNOLOGY, RAICHUR

~~*Guest Lecture/Seminar/Add on Course/Faculty Development Programme/Workshop/Conference/Short Term Training/Any Other Proposal*~~

Date: 06-04-2016

1	**ACADEMIC YEAR**	2015-16
2	**TYPE OF THE PROGRAMME PROPOSED**	Faculty Development Programme
3	**TITLE OF THE PROGRAMME:**	Training on Building modeling by using Rivet software
4	**BROAD OBJECTIVE OF THE PROGRAMME:**	The primary objective of this course is to teach students the essentials of working in Rivet using features and tools. This course teaches new users the basics of creating variety of techniques.
5	**NAME OF THE INSTITUTION**	NIT-Raichur
6	**NAME OF THE DEPARTMENT(S)**	Civil Engineering
7	**BENEFICIARIES**	Faculty Members
8	**ELIGIBILITY CRITERIA SET:**	Faculty From Civil Engineering Department
9	**NUMBER OF DAYS REQUIRED TO ORGANISE THE PROGRAMME**	5 Days (13-04-2016 – 17-04-2016)
10	**PROPOSED DURATION OF THE PROGRAMME**	5 Days
11	**TOTAL NO. OF PERIODS REQUIRED FOR THE PROGRAMME**	35 Hours
12	**NO. OF PERIODS REQUIRED PER DAY**	7
13	**LOCATION DETAILS:**	Auditorium
14	**DETAILS OF SPONSORERS:**	NIT Management

15	**DETAILS OF RESOURCE PERSONS:**	Dr. Rajesh kumar Assistant Professor PSNA College Of Engineering & Technology Dindigul Tamil Nadu Mr.Ravi Revankar Consultant Vidwat infrastructure Pvt.Ltd Vijaypur
16	**REGISTRATION FEE WITH CERTIFICATION:**	Academicians: Rs.700 Industry Professionals: Rs.1000
17	**REGISTRATION FEE WITHOUT CERTIFICATION:**	----------
18	**EVENT ORGANISERS (STUDENTS):**	Avinash S Ram Reddy Sumaiya Megha Yadav
19	**FACULTY COORDINATOR:**	Mr.Mohammed Adil Asst prof Civil Department NIT Raichur
20	**PROGRAMME PROPOSED BY**	Faculties of Civil Engineering Department
21	**ANY OTHER RELEVANT MATTER:**	------
22	**ESTIMATED BUDGET IN RS:**	25,000/-

This is to the Honorable Chairman of NIT-R for kind approval:

06.10.2014

EVENT COORDINATOR

HOD

PRINCIPAL

PRINCIPAL
Navodaya Institute of Technology (V)
RAICHUR-584 103

RAICHUR

NAVODAYA

Navodaya Nagar,Mantralayam Road, Raichur-584103
Affiliated to VTU, Belagavi, Karnataka, Approved by AICTE, New Delhi

Department of Civil Engineering

Date: 06-04-2016

To,
The Principal
NIT- Raichur.

Subject: Requisition for permission to conduct Faculty Development Programme.

Sir,

With reference to above cited subject, it is requested to kindly permit to conduct Faculty Development Programme. in our department. The resource person Dr. Rajesh kumar Assistant Professor PSNA College Of Engineering & Technology, Dindigul. Tamil Nadu and Ravi Revankar Consultant Vidwat infrastructure Pvt.Ltd Vijaypur has been consented to be available from 13-04-2016 to 17-04-2016.

Thanking you,

permitted

HOD

PRINCIPAL
Navodaya Institute of Technology
RAICHUR-584 103

NAVODAYA
Institute of Technology
RAICHUR

Navodaya Nagar,Mantralayam Road, Raichur-584103
Affiliated to VTU, Belagavi, Karnataka, Approved by AICTE, New Delhi

Department of Civil Engineering

Cir.No- 2015-16 / FDP / 01 Date: 06-04-2016

Circular

A Faculty Development Programme. on Training on Building modeling by using Rivet software will be delivered by Dr. Rajesh kumar and Mr.Ravi Revankar .All the faculties of civil engineering department must attend the Faculty Development Programme Without fail.

Venue: NIT Auditorium
Date: 13-04-2016 to 17-04-2016
Time: 10am to 5:00pm

HOD 06-04-2016
Head of Department
Civil Engineering
Navodaya Institute of Technology (NIT)
Raichur-584 103

Copy to:

1. Principal for Kind Information
2. All HoD's and Staff of Civil Engg.
3. Dept. Notice Board

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA

Navodaya Nagar,Mantralayam Road, Raichur-584103
Affiliated to VTU, Belagavi, Karnataka, Approved by AICTE, New Delhi

FACULTY DEVELOPMENT PROGRAM

ON

"TRAINING ON BUILDING MODELING BY USING RIVET SOFTWARE"

13th to 17th April 2016

Organized by

Department of Civil Engineering

SCHEDULE

Date	Time	Topics	Resource Person
13-04-2016	9.00 to 09.30	Registration	
	9.30 to 10.00	Inaugural Function	
	10.00 to 11.00	Introduction to the Autodesk Revit software	Dr. Rajesh kumar
	11.00 to 11.15	Tea Break	
	11.15 to 1.00	Basic drawing and editing tools	Dr. Rajesh kumar
	1.00 to 2.00	Lunch Break	
	2.00 to 3.15	Station Setup - Theory & Practical	Ravi Revankar
	3.15 to 3.30	Tea Break	
	3.30 to 5.00	Setting up levels and grids	Ravi Revankar
14-04-2016	9.30 to 11.00	Working with views	Dr. Rajesh kumar
	11.00 to 11.15	Tea Break	
	11.15 to 1.00	Starting a structural project based on a linked architectural model	Dr. Rajesh kumar
	1.00 to 2.00	Lunch	
	2.00 to 3.15	Starting a structural project based on a linked architectural model	Ravi Revankar
	3.15 to 3.30	Tea Break	
	3.30 to 5.00	Adding structural columns and walls	Ravi Revankar

15-04-2016	10.00 to 1.00	Hands on Experience/Practice session	Dr. Rajesh kumar
	11.00 to 11.15	Tea Break	
	11.15 to 1.00	Adding foundations and structural slabs	Dr. Rajesh kumar
	1.00 to 2.00	Lunch	
	2.00 to 3.15	Structural reinforcement	Ravi Revankar
	3.15 to 3.30	Tea Break	
	3.30 to 5.00	Beams, trusses, and framing systems	Ravi Revankar
16-04-2016	10.00 to 1.00	Hands on Experience/Practice session	Dr. Rajesh kumar
	11.00 to 11.15	Tea Break	
	11.15 to 1.00	Adding foundations and structural slabs	Dr. Rajesh kumar
	1.00 to 2.00	Lunch	
	2.00 to 3.15	Structural reinforcement	Ravi Revankar
	3.15 to 3.30	Tea Break	
	3.30 to 5.00	Beams, trusses, and framing systems	Ravi Revankar
17-04-2016	10.00 to 1.00	Analytical models and placing loads	Dr. Rajesh kumar
	11.00 to 11.15	Tea Break	
	11.15 to 1.00	Project practices to reinforce learning	Dr. Rajesh kumar
	1.00 to 2.00	Lunch	
	2.00 to 3.15	Construction documents, Detailing & Scheduling	Ravi Revankar
	3.15 to 3.30	Tea Break	
	3.30 to 5.00	Valedictory Function	

06.04.2016

CO-ORDINATOR

HOD

NAVODAYA INSTITUTE OF TECHNOLOGY • RAICHUR

6-4-16

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA
Institute of Technology
RAICHUR

Navodaya Nagar,Mantralayam Road, Raichur-584103
Affiliated to VTU, Belagavi, Karnataka, Approved by AICTE, New Delhi

Department of Civil Engineering

List of Participants in FDP on Training on Building modeling by using Rivet software

S.NO	NAME	Sign 13-04-2016	Sign 14-04-2016	Sign 15-04-2016	Sign 16-04-2016	Sign 17-04-2016
1	Dr. M S Shiva Reddy					
2	Dr. Shivprakash CK					
3	Mr. Kristaiah Shetty					
4	Mr. Veerupaxappa					
5	Ms. Shambhavi Tadkal					
6	Ms. Suma K S					
7	Ms. Nandini					
8	Mr. Md Adil					
9	Mr. Ashish P Desai					
10	Mr. Rajmohan B					
11	Ms. Shwetha Patil					
12	Mr. Vijaykumar G					
13	Ms. Channabassamma N					

17.04.2016
CO-ORDINATOR

HOD

PRINCIPAL
Navodaya Institute of Technology (Int)
RAICHUR-584 103

INSTITUTE OF TECHNOLOGY
NAVODAYA
RAICHUR

NAVODAYA

Navodaya Nagar,Mantralayam Road, Raichur-584103
Affiliated to VTU, Belagavi, Karnataka, Approved by AICTE, New Delhi

Department of Civil Engineering

Report

Faculty Development Programme on Building Modeling By Using Rivet Software

Date	13-04-2016 to 17-04-2016
Organized by	Department of Civil Engineering
Resource person (s)	Dr. Rajesh kumar Assistant Professor PSNA College Of Engineering & Technology, Dindigul, Tamil Nadu. Ravi Revankar Consultant Vidwat infrastructure Pvt.Ltd Vijaypur
Convener	Dr. Shivareddy M S
Coordinators	Mr.Mohammed Adil Asst prof Civil Department NIT Raichur
Number of participants	13

Program description

Workshop on **"Training on Building modeling by using Rviet software"** was organized by Department of Civil Engineering on 13-04-2016 to 17-04-2016. DR. Rajesh kumar given knowledge of the primary objective of this course is to teach students the essentials of working in Rivet using features and tools.

13.04.2016

CO-ORDINATOR

NAVODAYA INSTITUTE OF TECHNOLOGY

HOD

Navodaya Institute of Technology
RAICHUR-584 103

NAVODAYA
Institute of Technology
RAICHUR

Navodaya Nagar, Mantralayam Road, Raichur-584103
Affiliated to VTU, Belagavi, Karnataka, Approved by AICTE, New Delhi

Date: 17-04-2016

To,

The Principal,
NIT – Raichur

Sub: Faculty Development Program expenditure details - Reg

Dear Sir,

With reference to the above citied subject, Faculty Development Program was held in our department during 13th to 16th of April and the details of expenditure are as given below.

Sl. No	Particular	Qty	Rate	Amount
01	Honorarium to Resource Person	2	-	8000.00
02	Flex/Banner	02	250.00	500.00
03	Folders, Pad & Pens	20	50.00	1000.00
04	Brochure & Certificate Printing	20	20.00	400.00
05	Memento for Resource Persons	02	650.00	1300.00
06	Accommodation for Resource Persons	5 days	700	3500.00
07	Bouquets to Resource Persons	6	50.00	300.00
08	Water Bottles	15	10.00	150.00
09	Refreshments for Participants	5 days	500	2500.00
10	Lunch for Participants	5 days	500	2500.00
11	Conveyance Charges for Resource Persons	2	-	4500.00
	Miscellaneous			200.00
	Total Amount			24850.00

Amount in words: twenty four thousand eight hundred fifty only.

Thanking You,

Yours Faithfully

HOD – CE
NIT – R

19.04.2016

PRINCIPAL
Navodaya Institute of Technology
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

NAVODAYA

Navodaya Nagar,Mantralayam Road, Raichur-584103
Affiliated to VTU, Belagavi, Karnatak, Approved by AICTE, New Delhi

Department of Civil Engineering

Certificate

This is to certify Mr. Ms. Mrs. RAJMOHAN B *of* NIT - RAICHUR *has participated successfully in the two days Faculty development programme on* "**Training on Building modeling by using Rivet software**" *organized by Department of Civil Engineering, NIT– Raichur held on 13th to 17th April, 2016*

Co-ordinator

HOD

Principal

Workshop programme on

Training on Building modeling by using Rivet software

13th to 17th April 2016

REGISTRATION FORM

Name: .....

Designation: .....

Academic Qualification: .....

Institution & Address for Communication:

.....

.....

Phone: .....

Email: .....

Demand Draft Details:

D.D. No. : .....Date: .....

Amount: .....

Date: .....Signature: .....

Sponsoring Authority :

This is to certify that Mr/Ms.....

.....

is sponsored to participate in Faculty development programme on Training on Building Modelling Using Rivet software at Navodaya Institute of Technology, Raichur..

Date: Signature

(Photocopy of this form may also be used)

Chief Patron
Sri S R Reddy, Hon'ble Chairman
Navodaya Education Trust (NET®), Raichur

Patron
Sri N.Bharath Reddy, Hon'ble Trustee
Navodaya Education Trust (NET®), Raichur

Chief
Dr Shivaprakash C K
Dean & Principal, NIT-Raichur

Convener
Dr Shivareddy M S
HOD, Dept Of Civil Engineering, NIT-Raichur

Student Organizing Committee
Mr.Avinash S
Mr.Ram Reddy
Ms.Sumaiya
Ms.Megha Yadav

Registration Fee
Academicians: Rs.700
Industry Professionals: Rs.1000
Students : Rs.300
The fee includes the registration kit, tea & lunch during the program
Registration starts from 9.00am.

NAVODAYA

FACULTY DEVELOPEMENT PROGRAMME ON

Training on Building modeling by using Rivet software

13th to 17th April 2016

ORGANIZED BY

Department of Civil Engineering



Navodaya Education Trust®
NAVODAYA INSTITUTE OF TECHNOLOGY
Navodaya Nagar, Mantralayam Road,
Raichur-584103, Karnataka, India.

Venue:
Auditorium
NIT–Raichur - 584103

About the Institute

Navodaya Institute of Technology, Raichur (NIT-R) is established in the year of 2008 with a committment & responsibilty to meet the needs of modern education. NIT-R is established by the visionary management with a mission to create sensitive and responsible generation of entrepreneurs, inventors and technocrats. NIT-R is all inclusive campus with independent Academic Blocks for each department, Administrative Block, Library Block& other amenities in a vast campus of more than 30 acres of land surrounded by hillocks & lakes.

The institute contains latest equipment & resources and is purpose built for teaching & research. NIT-R focuses on promoting quality technical education in north Karnataka region and Hyderabad-Karnataka region in particular.

About the Department

Department of Civil Engineering was established in the year 2010. The department is well equipped with laboratories, computing facilities, independent library and other infrastructure. Thus, the department also takes up consultancy work pertaining to Planning, structural design, testing of materials and soil investigation for civil engineering projects as a part of the departmental activities. The Department is well known for qualified and experienced teaching faculties. The faculty members of the department continue to strive loftier by exploring new frontiers of knowledge, imparting the latest technical knowledge to the students. The department has been imparting quality education to meet the technological advancement and industrial requirements.The department has established its credibility by being committed to the teaching, learning, research and developmental activities.

Resource Persons

Dr. Rajesh kumar
Assistant Professor
PSNA College Of Engineering & Technology,
Dindigul,
Tamil Nadu

Ravi Revankar
Consultant
Vidwat infrastructure Pvt Ltd
Vijaypur

Who can attend the program

- Academicians
- Practicing Civil Engineers
- Industry Professionals

Coordinator
Prof. Md.Adil
Assistant Professor,
Dept. of Civil Engineering, NIT-Raichur
Email: adil.nitr@gmail.com
Contact no. : +91- 99 64 661 205

Co-Coordinator
Prof. Virupaxappa G
Assistant Professor,
Dept. of Civil Engineering, NIT-Raichur
Email: adil.nitr@gmail.com
Contact no. : +91- 9900247386

About the Program

This workshop provides enough theoretical aspects and working knowledge on Buliding modeling using Revit software. It also helps participants to learn technical skill through hands on learning activities and technical concepts involved in civil engineering.

The primary objective of this course is to teach students the essentials of working. This course teaches new users the basics.





PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY

Navodaya Nagar,Mantralayam Road, Raichur-584103
Affiliated to VTU, Belagavi, Karnataka, Approved by AICTE, New Delhi

Date: 02.08.2018

From
Dr.Rajesh Kumar
Professor and Head,
Navodaya Institute of Technology,
Raichur-584103.

To
The Principal,
Navodaya Institute of Technology,
Raichur-584103

Sir

Subject: Requisition to conduct Training Programs for Non-Teaching Faculty- Reg

With reference to above cited subject, it is requested to kindly permit to conduct Training Programs on **"Analysis and design using STAAD Pro"** for Non-Teaching Faculty In our department from 05.08.2018 to 07.08.2018.

Thanking You

Yours faithfully

HOD-CE

PRINCIPAL
Navodaya Institute of Technology (RIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY

Navodaya Nagar,Mantralayam Road, Raichur-584103
Affiliated to VTU, Belagavi, Karnataka, Approved by AICTE, New Delhi

2.8.18

CIRCULAR

Respected Sir / Madam,

Greetings from Navodaya Institute of Technology, Raichur...

The department of Civil Engineering is organizing a **"Analysis and design using STAAD Pro"** for Non-Teaching Faculties to upgrade the support staff's skills.

The main objective of this workshop is used for analysing & designing structures like – buildings, towers, bridges, industrial, transportation and utility structures. Designs may include any building structures like tunnels, culverts, bridges, piles, petrochemical plants; and building materials like timber, concrete, steel, cold-formed steel, and aluminium, We are pleased to inform that the department of Civil Engineering, Navodaya Institute of Technology is organizing a workshop for Non-Teaching Faculty during **05.08.2018 to 07.08.2018.**

In this context, we look forward your support and request you to encourage participation of support staff's by circulating the attached invitation.

Venue: Department of Civil Engineering
Navodaya Institute of Technology, Raichur.
Timing: 9.00AM to 5.00PM

Thanking you,
Yours sincerely,

HOD-CE
Head of Department
Civil Engineering
Navodaya Institute of Technology
RAICHUR-584 103

PRINCIPAL
Navodaya Institute of Technology (NIT),
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR
ACCREDITED

NAVODAYA INSTITUTE OF TECHNOLOGY

Navodaya Nagar,Mantralayam Road, Raichur-584103
Affiliated to VTU, Belagavi, Karnataka, Approved by AICTE, New Delhi

NON TEACHING STAFF SKILLS UP GRADUATION

DEPARTMENT: Civil Engineering

ACADEMIC YEAR: 2017-18

DATE: 05-08-2018

S.No	Name of the Staff	Date	Details
1	Hari Prasad	05-08-18	
2.	Shivashankar	"	
3	Chandrashekar	"	
4	Sharanabasava	"	

HOD-CE
Head of Department
Civil Engineering
Navodaya Institute of Technology (121)
RAICHUR-584 103

PRINCIPAL
Navodaya Institute of Technology (121)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

A Report

On

Analysis and design using STAAD Pro

Coordinators:

Dr. Rajesh Kumar, *Head and Professor*
Mr.Ramesh Baragani, *Assistant Professor*
Mr Bharath Koner, *Assistant Professor*
Department of Civil Engineering

Organized by:

Department of Civil Engineering
Navodaya Institute of technology-Raichur

Date: 05.08.2018 to 07.08.2018

NAVODAYA INSTITUTE OF TECHNOLOGY

Navodaya Nagar,Mantralayam Road, Raichur-584103
Affiliated to VTU, Belagavi, Karnataka, Approved by AICTE, New Delhi

Report

Training programs on "**Analysis and design using STAAD Pro**"

Date	05.08.2018 to 07.08.2018
Organized by	Department of Civil Engineering
Resource person (s)	Dr. Rajesh Kumar, Head and Professor Mr.Ramesh Baragani, Assistant Professor Mr.Bharath Koner, Assistant Professor
Coordinators	Mr.Ramesh Baragani, Assistant Professor

Program description

The department of Civil Engineering is organizing a **"Analysis and design using STAAD Pro"** for Non-Teaching Faculties to upgrade the support staff's skills.

The main objective of this workshop is used for analyzing & designing structures like – buildings, towers, bridges, industrial, transportation and utility structures. Designs may include any building structures like tunnels, culverts, bridges, piles, petrochemical plants; and building materials like timber, concrete, steel, cold-formed steel, and aluminum.

07/8/2018
CO-ORDINATOR

07/8/2018
HOD
Head of Department
Civil Engineering
Navodaya Institute of Technology (R)
RAICHUR-584 103

PRINCIPAL
Navodaya Institute of Technology (R),
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Analysis and design using STAAD Pro

Objective

STAAD is the abbreviation for Structural Analysis and Design. STAAD Pro is one of the popular software that is used for analysing & designing structures like - buildings, towers, bridges, industrial, transportation and utility structures. Designs may include any building structures like tunnels, culverts, bridges, piles, petrochemical plants, and building materials like timber, concrete, steel, cold-formed steel, and aluminium.

STAAD or STAAD Pro was developed by Research Engineers International at Yorba Linda, CA in 1997.

To get rid of the boring & time-consuming manual procedures Structural Engineers started using automated software STAAD Pro.

Course Overview:

STAAD Pro® is one of the most widely-used software for developing and analyzing the designs of various structures, such as petrochemical plants, tunnels, bridges etc. STAAD Pro® v8i, the latest version, allows civil engineering individuals to analyze structural designs in terms of factors like force, load, displacements etc. Multisoft Virtual Academy STAAD Pro® v8i online training builds expertise in using the software at a professional level in domains, including construction companies, government agencies, architecture firms etc.

STAAD.Pro Software Capabilities:

- Analyse for time dependent effects
- Check designs for cold-formed sections
- Comply with seismic requirements
- Create finite element meshes
- Design & analyse with finite element meshes, structural models
- Design beams, columns, walls and resisting frames
- Design to international design standards

- Loads and load combinations
- Integrate slab and foundation designs
- Model reinforced concrete, steel
- Structural design documentation
- Share structural models

Why to learn?

STAAD.Pro provides flexible modeling environment, fluent data collaboration, and advanced features. It best structural analysis & design software that supports Indian as well as all international codes.

STAAD.Pro permits structural engineers to design & analyze any type of structure virtually. Structural consultants, structural engineering firms, departments in construction companies, government agencies, owner/operators, offshore platform designers, many more is extensively using this software.

Learning Objectives:

The course will cover all the steps involved in structural analysis & designing of concrete & steel.

This course will introduce one to STAAD Pro's state of the art user interface, prevailing analysis and design engines with a sophisticated finite element (FEM), visualization tools, and dynamic analysis capabilities.

Some of the features that we focus in training include:

Model Generation: Generation of an interactive menu-driven model with concurrent 3D display 2D & 3D graphics generation using rectangular or polar coordinate systems Segments of repetitive geometry used to generate complex structural models.

- **Model Verification:** 2D/3D drawings on screen and printer/plotter full 3D shapes for frames, elements Isometric or any rotations for full 3D viewing.
- **Static Analysis:** 2D/3D analysis on the basis of state-of-the-art Matrix method to handle extremely large work. Linear, non-linear, p-delta analysis with automatic load & stiffness correction.
- **Dynamic/Seismic Analysis:** Mass modelling, frequency, and mode shape Response spectrum extraction, analysis of time history Modal damping ratio for individual models.
- **Secondary Analysis:** Finite element capabilities, concrete design, steel design, and timber design. Forces & displacements at sections between nodes. Maximum & minimum force envelopes.

Content Structure:

Chapter	Topic	Duration	Faculty
Chapter 1	Introduction to STAAD.Pro® V8i	3 hours	Dr. Rajesh Kumar,
Chapter 2	Model Generation and Editing	3 hours	
Chapter 3	Introduction to Loading	4 hours	Mr.Ramesh Baragani
Chapter 4	Automatic Load Generation	4 hours	
Chapter 5	Concrete Design	6 hours	
Chapter 6	Seismology	5 hours	
Chapter 7	FEM / FEA	4 hours	Mr.Bharath Koner
Chapter 8	Steel Design	4 hours	
Chapter 9	Report Generation	4 hours	
Chapter 10	Foundation Design	3 hours	

Head of Department
Civil Engineering
Navodaya Institute of Technology (I
RAICHUR-584 103

PRINCIPAL
Navodaya Institute of Technology (IIIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

NAVODAYA INSTITUTE OF TECHNOLOGY

Navodaya Nagar,Mantralayam Road, Raichur-584103
Affiliated to VTU, Belagavi, Karnataka, Approved by AICTE, New Delhi

Date: 03.01.2025

From
Dr.Saravanan
Professor and Head,
Navodaya Institute of Technology,
Raichur-584103.

To
The Principal,
Navodaya Institute of Technology,
Raichur-584103

Sir

Subject: Requisition to conduct Training Programs for Non-Teaching Faculty- Reg

With reference to above cited subject, it is requested to kindly permit to conduct Training Programs on **"Hands on Workshop in AutoCAD"** for Non-Teaching Faculty In our department from 06.01.2025 to 08.01.2020.

Thanking You

Yours faithfully

PRINCIPAL
Navodaya Institute of Technology (NIT),
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

HOD/CE

NAVODAYA INSTITUTE OF TECHNOLOGY

Navodaya Nagar,Mantralayam Road, Raichur 584103
Affiliated to VTU, Belagavi, Karnataka, Approved by AICTE, New Delhi

CIRCULAR

Respected Sir / Madam,

Greetings from Navodaya Institute of Technology, Raichur

The department of Civil Engineering is organizing a **"Hands on Workshop in Autocad"** for Non-Teaching Faculties to upgrade the support staff's skills.

The main objective of this workshop is to

A. Increase ability to communicate with people

B. Learn to sketch and take field dimensions.

C. Learn to take data and transform it into graphic drawings.

D. Learn basic Auto Cad skills.

E. Learn basic engineering drawing formats

F. Prepare the student for future Engineering positions

We are pleased to inform that the department of Civil Engineering, Navodaya Institute of Technology is organizing a workshop for Non-Teaching Faculty during **06.01.2020 to 08.01.2020.**

In this context, we look forward your support and request you to encourage participation of support staff's by circulating the attached invitation.

Venue: Department of Civil Engineering
Navodaya Institute of Technology, Raichur.
Timing: 9.00AM to 5.30PM

Thanking you,
Yours sincerely,

HOD CE

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY

Navodaya Nagar, Mantralayam Road, Raichur-584103
Affiliated to VTU, Belagavi, Karnataka, Approved by AICTE, New Delhi

NON TEACHING STAFF SKILLS UP GRADUATION

DEPARTMENT: (illegible)

ACADEMIC YEAR: 2019-20 DATE: 01-01-2020

S.No	Name of the Staff	Date	Details
1	(illegible)	01-01-20	

HOD-CE

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

A Report
On
Hands on Workshop in AutoCAD

Coordinators:

Dr. Sravanan R, *Head and Professor*
Mr.Panduranga, *Assistant Professor*
Mr.Bharath Koner, *Assistant Professor*
Department of Civil Engineering

Organized by:

Department of Civil Engineering
Navodaya Institute of technology-Raichur

Date: 6th January to 8th January, 2020

NAVODAYA INSTITUTE OF TECHNOLOGY

Navodaya Nagar,Mantralayam Road, Raichur 584103
Affiliated to VTU, Belagavi, Karnataka, Approved by AICTE, New Delhi

Report

Training programs on "Hands on Workshop in AutoCAD"

Date	06-01-2020 to 08-01-2020
Organized by	Department of Civil Engineering
Resource person (s)	Dr. Sravanan R, Head and Professor Mr.Panduranga, Assistant Professor Mr.Bharath Koner, Assistant Professor
Coordinators	Mr.Bharath Koner, Assistant Professor

Program description

The department of Civil Engineering is organizing a **"Hands on Workshop in AutoCAD"** for Non-Teaching Faculties to upgrade the support staff's skills.

The main objective of this workshop is to

A. Increase ability to communicate with people

B. Learn to sketch and take field dimensions.

C. Learn to take data and transform it into graphic drawings.

D. Learn basic Auto Cad skills.

E. Learn basic engineering drawing formats

F. Prepare the student for future Engineering positions

08/01/20

CO-ORDINATOR

HOD

Dept. of Engineering
Civil Engineering
Navodaya Institute of Technology

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

Basic knowledge of AutoCAD:

Welcome to the world of CAD. In this first tutorial you will be learning the very basics of AutoCAD. This course was designed so that the commands and instructions should work on almost any version of AutoCAD, although this version was designed specifically for AutoCAD 2016 and will work with older versions. By the end of this level, you will have the skills to develop basic 2D drawings and print them out to scale.

Basics Commands which we have covered in this workshop and many more

AUTOCAD COMMAND	DISCRIPTION	FURTHER INFORMATION
LINE	Create a line	Basic AutoCAD commands: Mastering the LINE command in AutoCAD
TRIM	Trim object to meet the edges of other objects	Basic AutoCAD commands: Trim and Extend in AutoCAD
CIRCLE	Create a circle	Basic AutoCAD commands: Circles in AutoCAD
EXTEND	Extend object to meet the edges of other objects	Basic AutoCAD commands: Trim and Extend in AutoCAD
ARRAYRECT	Create a rectangular pattern of object	Basic AutoCAD commands: Arrays in AutoCAD
ARRAYPOLAR	Create a circular pattern of object	Basic AutoCAD commands: Arrays in AutoCAD
COPY	Create a copy of an object	Basic AutoCAD commands: Move and Copy objects in AutoCAD
MOVE	Move an object	Basic AutoCAD commands: Move and Copy objects in AutoCAD
ROTATE	Rotate an object	Basic AutoCAD commands: Mirror and Rotate in AutoCAD
MIRROR	Create a mirrored copy of an object	Basic AutoCAD commands: Mirror and Rotate in AutoCAD
ERASE	Delete an object	Basic AutoCAD commands: Explode and Erase in AutoCAD
EXPLODE	Breaks an object into its components object	Basic AutoCAD commands: Explode and Erase in AutoCAD
LAYERS	Open the layer properties windows	Basic AutoCAD commands: Working with Layers in AutoCAD
CHAMFER	Bevels the edges of an object	Basic AutoCAD commands: Chamfer and Fillet
FILLET	Rounds the edges of objects	Basic AutoCAD commands: Chamfer and Fillet

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NET's

NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

GUEST LECTURE/SEMINAR/ADD-ON COURSE/WORKSHOP/CONFERENCE/SHORT TERM TRAINING PROGRAMME/ANY OTHER PROPOSAL

Date: 26-06-2018

1	ACADEMIC YEAR	2018-19
2	TYPE OF THE PROGRAMME PROPOSED	Six day Faculty Development Programme
3	TITLE OF THE PROGRAMME:	**"Instructional Design And Delivery System (IDDS)"**- Outcome Based Education
4	BROAD OBJECTIVE OF THE PROGRAMME:	Analyzing the paradigm shift needed for learner centric; designing & developing problem centered lesson plans; Constructivist & Cognitive approaches of delivering lectures; Designing students evaluation techniques; Developing curriculum mapping procedures & understanding the recent origins of performance appraisal systems of OBE.
5	NAME OF THE DEPARTMENT(S)	At Institutional Level
6	TARGETED GROUP	Faculty of NIT
7	DATE/DURATION OF SUCH PROGRAMMES CONDUCTED IF ANY IN THIS ACADEMIC YEAR:	Nil
8	ELIGIBILITY CRITERIA SET:	Faculty having less than 8 years of experience
9	NUMBER OF DAYS REQUIRED TO ORGANISE THE PROGRAMME	6 Days
10	PROPOSED DATE & DURATION OF THE PROGRAMME	16.07.2018 to 21.07.2018
11	TOTAL NO. OF PERIODS REQUIRED FOR THE PROGRAMME	36 Hours
12	NO. OF PERIODS REQUIRED PER DAY	06 Hours
13	LOCATION DETAILS:	Auditorium - NIT, Raichur
14	NO. OF PARTICIPANTS:	40-50 permitted

15	**DETAILS OF SPONSORERS:**	NET Trust
16	**DETAILS OF RESOURCE PERSONS:**	**1. Dr. T.G. Sambanthan,** Professor, Dept. of CSE, NITTTR- Chennai 2. **Dr. P. Shivashankar,** Asst. Professor, Dept. of EEE/ ECE, NITTTR-Chennai
17	**REGISTRATION FEE WITH CERTIFICATION:**	NIL
21	**FACULTY COORDINATOR:**	Dr. K. Rajesh Kumar, Professor & HOD (CE)
22	**PROGRAMME PROPOSED BY:**	Dr. Suman Kumar B. Professor & Vice Principal
23	**ANY OTHER RELEVANT MATTER:**	The resource persons are from NITTTR (National Institute of Technical Teachers Training and Research)-Chennai. It is a Central Govt. Institute providing training to teachers in India
24	**ESTIMATED BUDGET IN RS:**	Rs: **1,15,000/- (Rupees One Lakh Fifteen Thousands Only)** (Detailed Budget Enclosed)

This is to the Honorable CEO of NIT for kind approval:

PRINCIPAL

VICE-PRINCIPAL

EVENT COORDINATOR

APPROVED/ NOTAPPROVED

C.E.O OF NIT

NET's
NAVODAYA
Institute of Technology
RAICHUR
(Affiliated to Visvesvaraya Technological University (VTU) Belagavi
Affiliated to AICTE, New Delhi and Recognized by the Govt. of Karnataka)

Navodaya Nagar, Bijanagera Road
RAICHUR - 584 103, Karnataka, INDIA
Ph : +91 8532 240334
Fax : 240054 / 240061
E-mail : principal.nit@navodaya.edu.in
Web : www.navodayaengg.in

Date: 19-07-2018

To

The Director,
NITTTR, Taramani,
Chennai.

Subject: FDP on IDDS – Course Fee-reg.:

Respected Sir,

As per the email conversation and personal interaction of Dr. M. V. Mallikarjun, Principal with you about the conduction of FDP on IDDS at our institute by Dr. Sambanthan and the team from 27th July to 1st August 2018, we thank you for confirming our request.

In this regard, I here with attached a Demand Draft (DD No. 295316) of Rs. 70,000/- dated 18.07.2018 in favour of The Director, NITTTR, Chennai towards fee for conducting the FDP. This is for your kind perusal.

Yours truly,

Dr. K. Rajesh Kumar M.E., Ph. D,
Professor & Head,
Department of Civil Engineering,
Navodaya Institute of Technology,
Raichur, Karnataka, INDIA
Email: hodcivil.nit@navodaya.edu.in Contact no: 0 94432 24506

Enclosure: Demand Draft.



Sambanthan T Gnana

Researcher, Outcome Based Education

India · 7 connections

Join to Connect

NITTTR, Chennai

Indian Institute of Technology, Madras

About

Since 1995, till Sept. 2018, Dr T. Gnana Sambanthan was a Professor & head of the CSE Dept., of NITTTR, a centrally funded Institution of the Ministry of Human Resource Development, Govt. of India, in Chennai. He has done his B.E., M.E., and Ph.Ds in Civil engineering, in full time at a stretch, without a gap. He holds an M.E with distinction from Indian Institute of Science, Bangalore and a Ph.D from IIT Madras. He has obtained a blend of Industrial, Research and Teaching/Training experiences of several years from India and abroad. He also had a two years stint with the state council for Science & Technology coordinating Industry-Institute-Interaction cell. As a Software professional, he had developed over 1,50,000 lines of coding on UNIX platform in India and abroad. In 1998, he was sponsored under World Bank to the Huddersfield University, UK, on "Curriculum Development in Computer Science Discipline". In 2000, he was nominated to Manila, Philippines on "Computer Applications in Technical Education & Training" under Colombo Plan. In 2017 he was nominated to Dhaka, Bangladesh on TVET, CPSC programme on OBE. He has worked in Riyadh, Saudi Arabia for 7 years as a s/w engineer, apart from visiting a few other countries. He has published and presented over 100 papers in National & International Journals, Conferences & Seminars in India and in abroad and also authored two editions of a book on computer graphics. He has visited over 1000 Technical Institutions, on various Committees such as AICTE etc., in over 18 states of India. He has been rendering popular lectures to students on 'UNIX/LINUX OS', based on his Industrial experience and to teachers on 'Instructional System Design for OBE through CDIO' based on his training and research experiences, in over 100 Higher educational and Management Institutions in India. He was the chairman in implementing IT curriculum in Portblair, Andamans. He has won several awards and distinctions from various organizations, including the Jawaharlal Nehru Memorial Award from the University of Madras. He has already produced 13 Ph.Ds under the Madras University and currently guiding 6 Ph.Ds, in Engineering Education, apart from associating himself in Doctoral committees and adjudications of dozen Universities for over 100 candidates in the past 10 years. His areas of interest are concept extractions, Learning Grids and Computer Science Education, Instructional Designs, OBE-CDIO and e_Learning.

NAVODAYA INSTITUTE OF TECHNOLOGY

NAVODAYA EDUCATION TRUST'S

NAVODAYA INSTITUTE OF TECHNOLOGY

RAICHUR

Cordially invite you to attend the inaugural function of

One Week Faculty Development Programme

"INSTRUCTIONAL DESIGN AND DELIVERY SYSTEM (IDDS)"

Chief Guest

Dr. T. G. Sambanthan

Prof. & Head
Dept. of CSE, NITTTR, Chennai
(Resource Person)

President

Shri. S. R. Reddy

Hon'ble Chairman
Navodaya Education Trust®, Raichur

Guests of Honour

Dr. A. Mahendrakar
Medical Superintendent
NMCH & RC, Raichur

Dr. P. Vijaya Kumar
Director (IP & SA)
Navodaya Education Trust®, Raichur

Date: **27.07.2018 Friday** Time: **10:00 AM**

Venue

NIT-Auditorium
NIT Campus, Raichur

Shri. N. Bharath Reddy
CEO, NIT, Raichur

Dr. M. V. Mallikarjuna
Principal, NIT

NAVODAYA INSTITUTE OF TECHNOLOGY

RAICHUR, KARNATAKA

Date: 26-07-2018

CIRCULAR

One week faculty development programme is being conducted from 27th July to 1st August'2018 at our institute. The resource persons are from **National Institute of Technical Teachers Training & Research**, Chennai.

It is hereby informed to all the teaching staff, the list enclosed, that they should attend this programme as it will add value to their professional growth. The details of the programme along with the invitation card is enclosed for your kind information. Sunday will be full working day due to the schedule of this FDP. Looking forward for interactive learning.

Coordinator

Principal

PRINCIPAL
Navodaya Institute of Technology (N)
RAICHUR-584 103

Copy to

- CEO for kind information
- Vice Principal & All HODs
- Mr. Ravi, Manager

To
Mr. Ashok
26/7/18

Registration Details for one week Faculty Development Program

Sl.No	Name of the Faculty	Department	Designation
1	Ms. Priyanka Desai	Civil	Asst. Prof
2	Ms. Priyanka Patil	Civil	Asst. Prof
3	Ms. Shruti Shivraj	Civil	Asst. Prof
4	Mr. Ashish P Desai	Civil	Asst. Prof
5	Mr. Bharat Komer	Civil	Asst. Prof
6	Mr. Md. Adil	Civil	Asst. Prof
7	Mr. Nagesh Sugandhi	Civil	Asst. Prof
8	Mr. Santhosh Kumar	Civil	Asst. Prof
9	Mr. Rajmohan B	Civil	Asst. Prof
10	Mr. Vijay Kumar G	Civil	Asst. Prof
11	Mr. Virupaxappa	Civil	Asst. Prof
12	Mr. Ramesh B	Civil	Asst. Prof
13	Mr. Basavaraj Akki	Civil	Asst. Prof
14	Mr. Praveengouda	Civil	Asst. Prof
15	Mr. Imran Basha A	Mechanical	Asst. Prof
16	Mr. Rajashekarappa	Mechanical	Asst. Prof
17	Mr. Ravi Kulkarni	Mechanical	Asst. Prof
18	Mr. Srikar G Kulkarni	Mechanical	Asst. Prof
19	Mr. Vinayak Manur	Mechanical	Asst. Prof
20	Ms. Geeta V	Mechanical	Asst. Prof
21	Ms. Geeta K M	EEE	Asst. Prof
22	Ms. Sana Anjum	EEE	Asst. Prof
23	Mr. B K Mazumdar	EEE	Asst. Prof
24	Mr. B Madhu babu	EEE	Asst. Prof
25	Mr. Kiran Kumar T H	EEE	Asst. Prof
26	Mr. Satish Kumar K S	ECE	Asst. Prof
27	Mr. Sudhakar Kotturu	EEE	Asst. Prof
28	Ms. Priyadarshini	ECE	Asst. Prof
29	Mr. Amarappa Pagi	ECE	Asst. Prof
30	Mr. Sachin Kumar	ECE	Asst. Prof
31	Ms. Channaveeramma E	ECE	Asst. Prof
32	Ms. Madhuri Devi CH	ECE	Asst. Prof
33	Ms. Reshma Arasikere	ECE	Asst. Prof
34	Ms. Veenashree H	ECE	Asst. Prof
35	Ms. Preeti Sagar	ECE	Asst. Prof
36	Mr. Chetan Gudi	CSE	Asst. Prof
37	Mr. B Shiva Kumar Reddy	CSE	Asst. Prof
38	Ms. Shanti	CSE	Asst. Prof
39	Ms. Supriya G Purohit	CSE	Asst. Prof
40	Ms. Bhagavati	CSE	Asst. Prof

DATE: 01/8/18

Course No. & Title : Instructional Model for OBE

Duration : 27.07.2018 to 01.08.2018

Coordinator : Dr.T.G.Sambanthan & Dr.P.Sivasankar

LIST OF PARTICIPANTS

1. **Mr. VIRUPAXAPPA G**
Assistant Professor in CIVIL ENGINEERING,
Navodaya Institute of Technology,
Raichur, Karnataka
Mobile No: 9902473826
E-mail: vvgudur@gmail.com

2. **Mr. NAGESH SUGANDHI**
Assistant Professor in CIVIL ENGINEERING,
Navodaya Institute of Technology,
Raichur, Karnataka
Mobile No: 7795326184
E-mail: nagesh.s.lad@gmail.com

3. **Mr. RAMESH BARAGANI**
Assistant Professor in CIVIL ENGINEERING,
Navodaya Institute of Technology,
Raichur, Karnataka
Mobile No: 7204313650
E-mail: rameshbaragani@gmail.com

4. **Mr. VINAYAK S**
Assistant Professor in CIVIL ENGINEERING,
Navodaya Institute of Technology,
Raichur, Karnataka
Mobile No: 9986185828
E-mail: CHETANSWAMY07@gmail.com

5. **Dr.K.RAJESH KUMAR**
Prof. & Head in Civil Engineering
Navodaya Institute Of Technology,
Raichur, Karnataka
Mobile No: 9443224506
E-mail: krk.prof@gmail.com

6. **Mr. VIJAY KUMAR GOMARSI**
Assistant Professor in CIVIL ENGINEERING,
Navodaya Institute of Technology,
Raichur, Karnataka
Mobile No: 7795115208
E-mail: vijaygomarsi@gmail.com

7. **Mr. SANTOSH KUMAR**
Assistant Professor in CIVIL ENGINEERING,
Navodaya Institute of Technology,
Raichur, Karnataka
Mobile No: 9663589411
E-mail: santunmath2112@gmail.com

8. **Mrs. PRIYANKA PATIL**
Assistant Professor in CIVIL ENGINEERING,
Navodaya Institute of Technology,
Raichur, Karnataka
Mobile No: 9008291466
E-mail: priyanka.patil9880@gmail.com

9. **Ms. SHRUTI SHIVARAJ**
Assistant Professor in CIVIL ENGINEERING,
Navodaya Institute of Technology,
Raichur, Karnataka
Mobile No: 9986761551
E-mail: shrutiswamy4611@gmail.com

10. **Ms. PRIYANKA DESAI**
Assistant Professor in CIVIL ENGINEERING,
Navodaya Institute of Technology,
Raichur, Karnataka
Mobile No: 8494892347
E-mail: priyankasdesai2@gmail.com

MA.

11. BASAVARAJ AKKI
Assistant Professor in CIVIL ENGG.
Navodaya Institute of Technology,
Raichur, Karnataka
Mobile No: 9035124002
E-mail: basuakki006@gmail.com
basuakki.cv006@gmail.com

12. Mr. PRAVEENGOWDA
Assistant Professor in CIVIL ENGINEERING,
Navodaya Institute of Technology,
Raichur, Karnataka
Mobile No: 9483766433
E-mail: bpraveenpatil@gmail.com

13. Mr. MOHAMMED ADIL
Assistant Professor in CIVIL ENGINEERING,
Navodaya Institute of Technology,
Raichur, Karnataka
Mobile No: 9739170189
E-mail: adil.nitr@gmail.com

14. Mr. ASHISH P DESAI
Assistant Professor in CIVIL ENGINEERING,
Navodaya Institute of Technology,
Raichur, Karnataka
Mobile No: 9741502149
E-mail: ashishdesai009@gmail.com

15. Mr. RAJMOHAN B
Assistant Professor in CIVIL ENGINEERING,
Navodaya Institute of Technology,
Raichur, Karnataka
Mobile No: 9964661205
E-mail: ~~RAJMOHAN.NCR~~@gmail.com
Rajmohan.nca@gmail.com

16. Mr. SRIKAR G KULKARNI
Assistant Professor in MECH. ENGINEERING
Navodaya Institute of Technology,
Raichur, Karnataka
Mobile No: 9844929917
E-mail: srikargkulkarni@gmail.com

17. Mr. RAJA SHAKARAPPA
Assistant Professor in MECH. ENGINEERING
Navodaya Institute of Technology,
Raichur, Karnataka
Mobile No: 9731694575
E-mail: appa.raja@gmail.com

18. Mr. IMRAN BASHA A
Assistant Professor in MECH. ENGINEERING,
Navodaya Institute of Technology,
Raichur, Karnataka
Mobile No: 9916467047
E-mail: imranbasha@gmail.com

19. Mr. PRIYANKAR DHOLE
Assistant Professor in MECH. ENGINEERING,
Navodaya Institute of Technology,
Raichur, Karnataka
Mobile No: 9972083592
E-mail: Priyankardhole@gmail.com

Ms.

20. Ms. VEMAREDDY GEETA
Assistant Professor in MECH. ENGINEERING
Navodaya Institute of Technology,
Raichur, Karnataka
Mobile No: 9490196250
E-mail: ~~vemareddygeeta~~@gmail.com
vemareddygeeta@gmail.com

21. VINAYAK MANUR
Assistant Professor in MECH ENGG,
Navodaya Institute of Technology, Raichur
Raichur Dist, Karnataka
Mobile No: 8904567258
E-mail: vinayakrm91@gmail.com

22. Ms. GEETA K M
Assistant Professor in EEE,
Navodaya Institute of Technology,
Raichur, Karnataka
Mobile No: 9980350975
E-mail: geeta.engg7@gmail.com

23.Ms. SANA ANJUM
Assistant Professor in EEE,
Navodaya Institute of Technology,
Raichur, Karnataka
Mobile No: 7676277510
E-mail: sana92.anjum@gmail.com

24.Mr. SUDHAKAR KOTHURU
Assistant Professor in EEE,
Navodaya Institute of Technology,
Raichur, Karnataka
Mobile No: 9590058209
E-mail: ~~sana92.anjum@gmail.com~~ sudk.kothuru@gmail.com

25. Mr. B K MAZUMDAR
Assistant Professor in EEE,
Navodaya Institute of Technology,
Raichur, Karnataka
Mobile No: 9845229996
E-mail: mazumdarbk@gmail.com

26. Mr. KIRAN KUMAR T H
Assistant Professor in EEE,
Navodaya Institute of Technology,
Raichur, Karnataka
Mobile No: 9738120986
E-mail: kirankumarth4231@gmail.com

27. Mr. SATISH KUMAR K S SATHISH KUMAR. KS
Assistant Professor in EEE,
Navodaya Institute of Technology,
Raichur, Karnataka
Mobile No: 9489139501
E-mail: sunilsatish@gmail.com sunilsathish@gmail.com

28. Mr. B MADHU BABU
Assistant Professor in EEE,
Navodaya Institute of Technology,
Raichur, Karnataka
Mobile No: 9700002442
E-mail: madhu.7263@gmail.com

29. MADHURI DEVI CHODEY
Assistant Professor in ECE,
Navodaya Institute of Technology,
Raichur Dist, Karnataka
Mobile No: 9666713819
E-mail: madhurichodey@gmail.com

30.N PRIYADARSHINI
Assistant Professor in ECE,
Navodaya Institute of Technology,
Raichur, Karnataka
Mobile No: 9160166788
E-mail: espriya07@gmail.com

31. AMARAPPA PAGI
Assistant Professor in ECE,
Navodaya Institute of Technology,
Raichur, Karnataka
Mobile No: 9663338639
E-mail: amarappa.pagi@gmail.com

32. E CHANNAVEERAMMA
Assistant Professor in ECE,
Navodaya Institute of Technology,
Raichur, Karnataka
Mobile No: 8884977757
E-mail: echennu.putta@gmail.com

33. Ms. VEENASHREE HIREMATH
Assistant Professor in ECE,
Navodaya Institute of Technology,
Raichur, Karnataka
Mobile No: 9945467178
E-mail: veenahiremath80@gmail.com

34. Mrs. RESHMA ARASIKERE
Assistant Professor in ECE,
Navodaya Institute of Technology,
Raichur, Karnataka
Mobile No: 9902242190
E-mail: reshma.a.198@gmail.com

35. PREETI SAGAR PATTANKUDE
Assistant Professor in ECE,
Navodaya Institute of Technology,
Raichur, Karnataka
Mobile No: 9663546650
E-mail: preeti.p73@gmail.com

36. Mr. CHETAN GUDI
Assistant Professor in CSE,
Navodaya Institute of Technology,
Raichur, Karnataka
Mobile No: 9663426789
E-mail:
chetangudi@navodaya.edu.in@gmail.com

37. Mr. B SIVA KUMAR REDDY
Assistant Professor in CSE,
Navodaya Institute of Technology,
Raichur, Karnataka
Mobile No: 9686281370
E-mail: bskreddy64@gmail.com

38. Mrs. USHA
Assistant Professor in CSE,
Navodaya Institute of Technology,
Raichur, Karnataka
Mobile No: 7406095773
E-mail: katoriusha@gmail.com

39. Ms. BHAGAVATI
Assistant Professor in CSE,
Navodaya Institute of Technology,
Raichur, Karnataka
Mobile No: 8867334413
E-mail: bhagavati.a@gmail.com

40. Mrs. SHAZIA ANJUM
Assistant Professor in CSE,
Navodaya Institute of Technology,
Raichur, Karnataka
Mobile No: 9741252320
E-mail: anjum.shazia@gmail.com
anjum.shazia@gmail.com

41. Mrs. E SHANTHI
Assistant Professor in CSE,
Navodaya Institute of Technology,
Raichur, Karnataka
Mobile No: 9886582540
E-mail: shanthiejarla@gmail.com

42. Mrs. SUPRIYA G PUROHIT
Assistant Professor in CSE,
Navodaya Institute of Technology,
Raichur, Karnataka
Mobile No: 9611167184
email: supipurohit@gmail.com

43. Mr. GURURAJ
Assistant Professor in H & S,
Navodaya Institute of Technology,
Raichur, Karnataka
Mobile No: 8147693655
E-mail: gururaj.kurumanna@gmail.com

44. Mr. SRIKANTH
Assistant Professor in H & S,
Navodaya Institute of Technology,
Raichur, Karnataka
Mobile No: 9035672018
E-mail: srikanth.kjk@gmail.com

45. Ms. SHALINI K
Assistant Professor in H&S,
Navodaya Institute of Technology,
Raichur, Karnataka
Mobile No: 8495054381
E-mail: kshalini874@gmail.com

46) RANI KULKARNI H
Assistant Professor in Mechanical Eng
Navodaya Institute of Technology,
Raichur, Karnataka
Mobile No:- 9611064259
Email: rani.k.087@gmail.com

(P.T.O)

NAVODAYA

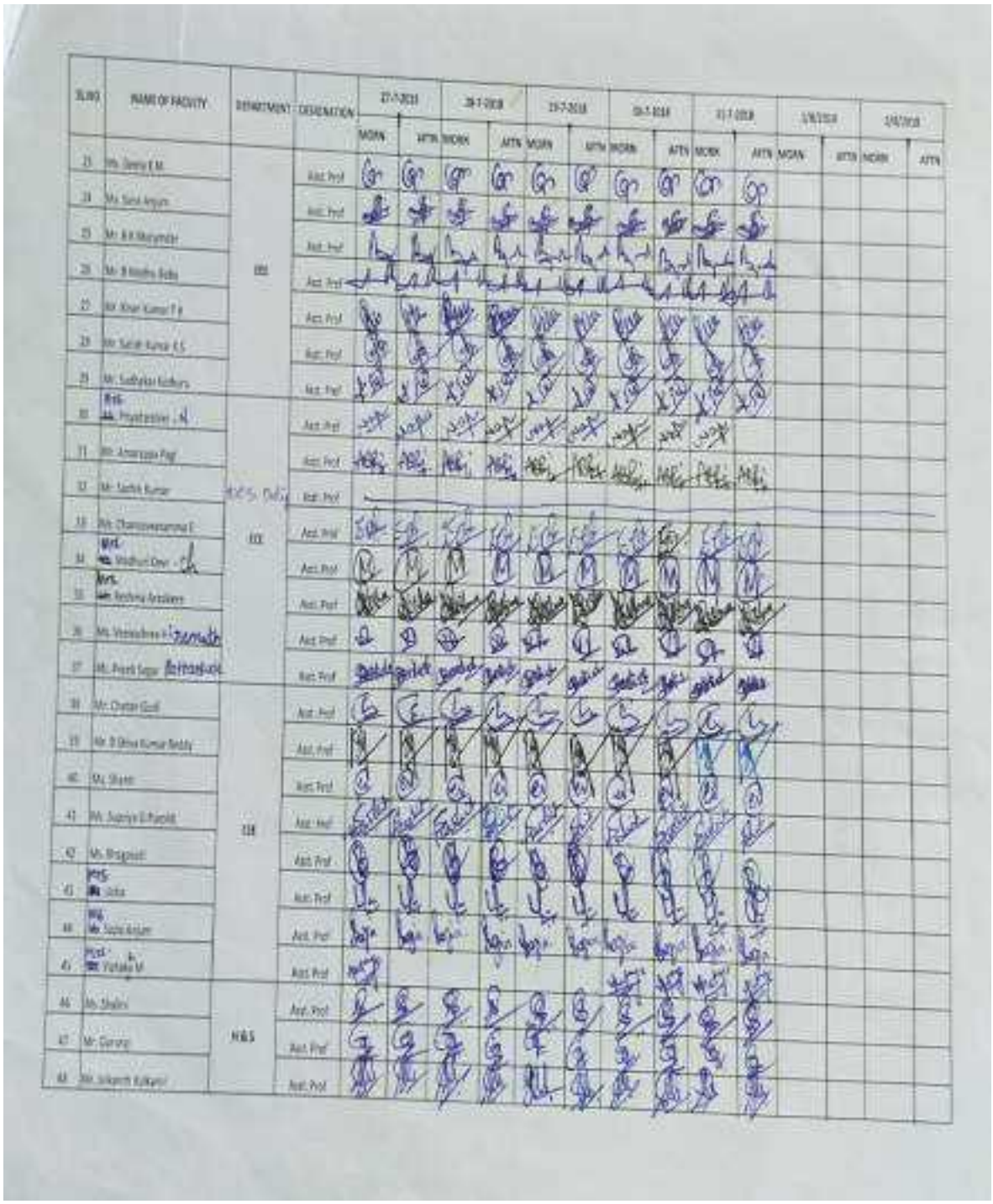
NET'S

NAVODAYA INSTITUTE OF TECHNOLOGY, RAICHUR

FACULTY DEVELOPMENT PROGRAM

ATTENDENCE SHEET

SL NO	NAME OF FACULTY	DEPARTMENT	DESIGNATION	27-7-2018		28-7-2018		29-7-2018		30-7-2018		31-7-2018		1/8/2018		2/8/2018	
				MORN	AFTN	MORN	AFTN	MORN	AFTN	MORN	AFTN	MORN	AFTN	MORN	AFTN	MORN	AFTN
1	Ms. Priyanka Desai	Civil	Asst. Prof														
2	Ms. Priyanka Patil		Asst. Prof														
3	Ms. Shruthi Shirsagi		Asst. Prof														
4	Mr. Ashish P Desai		Asst. Prof														
5	Mr. Bharat Kumar	DCS Jadhav	Asst. Prof														
6	Mr.Md. Atif		Asst. Prof														
7	Mr. Nagesh Sugnahi		Asst. Prof														
8	Mr. Santhosh Kumar		Asst. Prof														
9	Mr. Rajmohan B		Asst. Prof														
10	Mr. Vijay Kumar C		Asst. Prof														
11	Mr. Mogalappa		Asst. Prof														
12	Mr. Ramesh B		Asst. Prof														
13	Mr. Basavaraj Mali		Asst. Prof														
14	Mr. Praveengouda		Asst. Prof														
15	Mr. Manjunath S		Asst. Prof														
16	Mr. Imran Basha A	Mechanical	Asst. Prof														
17	Mr. Rajashekarappa		Asst. Prof														
18	Mr. Ravi Kulkarni		Asst. Prof														
19	Mr. Srihari G Kulkarni		Asst. Prof														
20	Mr. Vinayak Mimar		Asst. Prof														
21	Mr. Sneha V		Asst. Prof														
22	Mr. Prashantkar Dhole		Asst. Prof														



NET's
NAVODAYA INSTITUTE OF TECHNOLOGY, RAICHUR
Department of Civil Engineering

To,
The Principal,
NIT- Raichur.

Date: 25-07-2018

Subject: Permission to use Auditorium for Guest Lecture – Regarding

Sir,

With reference to above cited subject, Civil Engineering Department is organizing a Faculty Development Programme from 27-07-18 to 01-08-18. We request you to kindly permit us to use Auditorium.

Thanking You

Yours' Sincerely

HOD-CE

NET's
NAVODAYA INSTITUTE OF TECHNOLOGY, RAICHUR
Department of Civil Engineering

Report of Faculty Development Programme "Instructional Design and Delivery System (IDDS)"

Resource Person:
Dr. T. G. Sambanthan
Prof.& Head, Dept. CSE, NITTTR, Chennai
Dr. Sivasankar
Assistant Professor, Dept. of ECE, NITTTR, Chennai

A six days Faculty development Programme (FDP-2018) was successfully conducted in NET's Navodaya Institute of Technology, Raichur (NITR), on 27th July – 1st August 2018. The FDP programme received an overwhelming response with about 50 participants from NITR.

The programme was inaugurated in the morning at auditorium on 27th July 2018 by the hands of Dr. T. G. Sambanthan (Resource Person), Dr. Mahendrakar (Medical Superintendent, Navodaya Medical College Hospital & Research Centre, Raichur), Dr. P. Vijaya Kumar (Director (IP&SA) Navodaya Education Trust, Raichur), Shri. S. R. Reddy (Chairman), Dr. M. V Mallikarjuna, (Principal, NITR) & Dr. K. Rajesh Kumar (Coordinator, FDP).

HOD





Faculty Development Programme "Instructional Design and Delivery System (IDDS)"

NET's
NAVODAYA INSTITUTE OF TECHNOLOGY
Department of Civil Engineering

STUDENT FEEDBACK FORM

As part of a continuing improvement process, our college appreciates suggestions and inputs regarding the institution. We request you to sincerely answer these questions under assurance of complete confidentiality. Your interest in making our institution better is greatly appreciated.

Name of Speaker
Program:
Date:

Students are required to rate the speaker on the following attributes using the 6 -point scale shown.

Parameter	Excellent	Very Good	Good	Satisfactory
Your perception of his/her knowledge				
Communication skills				
Sincerity/commitment				
Ability to make the subject relevant to profession and life				
The material presented in an organized manner				
Overall rating				

Signature

NAVODAYA
Institute of Technology
RAICHUR
(Affiliated to Visvesvaraya Technological University (VTU), Belagavi
approved by AICTE, New Delhi and Recognized by the Govt. of Karnataka)

Navodaya Nagar, Bijanagera Road
RAICHUR - 584 103, Karnataka, INDIA
Ph : +91 8532 240334
Fax : 240054 / 240061
E-mail : principal.nit@navodaya.edu.in
Web : www.navodayaengg.in

24.07.2018

To,
The Manager,
Nrupatunga Hotel,
Near Ambedkar Circle,
Raichur

Sub – Providing accommodation for our Guests – Reg

Sir,

With respect to the above cited subject, we would like to book 02 double rooms with A/C for our guests from 26.07.2018 to 01.08.2018 in your hotel. The details of the guests are given below for your booking procedure.

Name of Guest 1 : Dr. T G Sambanthan, Prof. & Head, Dept. of CSE, NITTTR, Chennai

Duration of Stay : 26.07.2018 Evening to 01.08.2018 Morning

Name of Guest 2 : Dr. Sivasankar, Asst. Professor, Dept. of ECE, NITTTR, Chennai

Duration of Stay : 27.07.2018 Mid Night to 01.08.2018 Morning

Looking forward for your kind cooperation.

Thanks & Regards,

Principal
NIT-R

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Navodaya Education Trust's

NAVODAYA INSTITUTE OF TECHNOLOGY

Navodaya Nagar, Bijanagera Road, RAICHUR – 584103,
KARNATAKA

NAVODAYA

DEPARTMENT OF CIVIL ENGINEERING

SL No.	Expenditure Details	Bill Sl.No.	Bill No/ Voucher	Amount Rs.
1.	Course fee with DD charges	01	15187	70,350.00
2.	To and Fro Flight Tickets for the resource persons from Chennai (Boarding Passes Attached)	02-05	Confirmation Ticket attached	12,000.00
3.	Hotel stay charges at Nrupatunga	06,07	29348 29349	32,135.40
4.	To & Fro Travel Allowances from Hyderabad to Raichur	08	158	12,135.00
5.	Miscellaneous expenditure for Resourse Person	09	Voucher	1,900.00
6.	To & Fro Travel Allowances from Chennai Airport to resource persons' residence	10,11	Voucher	3,000.00
7.	Food Expenses for Resource Persons on the way to Raichur	12	Voucher	3,500.00
8.	Miscellaneous Expenses	13	Voucher	1,000.00
9.	Banner (6 No.s) (Yusuf Arts) Fixing Charges	14	Delivery Receipt	5,568.00 100.00
10.	Food Expenses of Resource Persons 1)Atithi Fine Dining 2)Hassan Iyengar's Bakery 3)Hotel Udupi Priya	15-17	0006367, 6368, 6501, 6496, 6498, 388, 390	2,705.00
11.	Food Expenses for Resource Persons on the way back to Chennai	18	Voucher	2,000.00
12.	Invitation Cards (Sriram Printers)	19	105	1,250.00
13.	Spiral Binding (Ganesh Enterprises)	20	4163	1,800.00
14.	Reception items 1) Lamp- 200.00 2) Water Bottle -100.0 3) Flower Bouquet- 1240.00 4) Pooja Oil, Cotton Wicks , Camphor, etc.-70.00	21	Voucher	1610.00
15.	Tea with Snacks for the Participants	22	15702	3,000.00
16.	Microphone	23	46	6,595.00
17.	Photographs (Shree Amba Digital Studio)	24,25	1863, 1864	1,900.00
18.	Petrol Allowance to staff	26,27	18505 & 18416	800.00
19.	Triangle Name Board 5 numbers Reflect Radium	28		750.00
20.	Battery for Micro Phone Sequin Gift Paradise	29		160.00
	Grand Total			1,64,258.40

PRINCIPAL

Co-ordinator
Professor & Head
Department of Civil Engineering
Navodaya Institute of Technology

National Institute of Technical Teachers Training and Research

Taramani, Chennai - 600 113.

(Govt. of India, MHRD, Dept. of Secondary and Higher Education)

RECEIPT

No. 15187 Date 25-07-18

Name .....

Address Navodaya Institute of Technology
Raichur, Karnataka - 584103

Sl.No	Details	Amount Rs.	P.
1	S/c on "Instructional Model ~~Grant-in-Aid~~ for Outcome Based Education	—	
2	Course Fee for FDP - IDDS.	70000	-
3	~~Rent~~ From 27-07-2018 to 01-08-2018		
4	Sale of Books / CD's		
5	Refund of Tour Advance		
6	Recoveries		
	SBI, NET Campus		
	DDNO. 295316		
	dt. 18-07-18		
	Total Rs. ...	70000	-

Received Rs. 70000/- (Rupees Seventy thousand only)

R Vijayalakshmi
CASHIER

Course # 1

Title: "**Instructional Design and Delivery System (IDDS)**"

Course Content:

Principles of Teaching and Learning; Student Characteristics; Student Motivation; Instructional Objectives; Instructional Methods; Instructional Media; Instructional Planning; Student Evaluation; OBE Overview; Microteaching.

Please note that each one of the above areas would be handled by different teacher (trainer) which provides information about that topic in general perspective, and in isolation. Course content has been designed by the 'Department of Engineering Education' of NITTTR.

Course fee: Rs 70,000/=, payable by DD drawn in the name of "The Director", NITTTR, at Chennai. There will be no other remuneration payable to the resource faculties; However to-and-from Chennai by Air/road must be borne by the Institute in addition to local hospitality (stay, food and local transport).



NET's

NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

~~GUEST LECTURE/SEMINR/ADD ON COURSE~~/ Faculty Development Programme ~~WORKSHOP / CONFERENCE/SHORT TERM TRAINING PROGRAMME/ANY OTHER~~ PROPOSAL

Date: 01.06.2015

1	**ACADEMIC YEAR**	2015-16
2	**TYPE OF THE PROGRAMME PROPOSED**	*Faculty Development Programme*
3	**TITLE OF THE PROGRAMME:**	**"Android Application Development"**
4	**BROAD OBJECTIVE OF THE PROGRAMME:**	Android is an open source and Linux-based operating system for mobile devices such as smartphones and tablet computers. Android was developed by the Open Handset Alliance, led by Google, and other companies. This FDP will teach you basic Android programming and will also take you through some advance concepts related to Android application development.
5	**NAME OF THE INSTITUTION**	NET's, NAVODAYA INSTITUTE OF TECHNOLOGY
6	**NAME OF THE DEPARTMENT(S)**	COMPUTER SCIENCE & ENGINEERING
7	**BENEFICIARY**	Faculty Members
8	**PROPOSED DATE OF THE PROGRAMME**	07.07.2015 to 11.07.2015 (05 days)
9	**ELIGIBILITY CRITERIA SET:**	Faculty Members of CSE, ECE, EEE
10	**NUMBER OF DAYS REQUIRED TO ORGANISE THE PROGRAMME**	Five Days
11	**PROPOSED DURATION OF THE PROGRAMME**	30 Hours
12	**TOTAL NO. OF PERIODS REQUIRED FOR THE PROGRAMME**	30 Periods
13	**NO. OF PERIODS REQUIRED PER DAY**	06 Periods
14	**LOCATION DETAILS:**	Department of CSE, SSW Lab, NIT, Raichur

15	**MINIMUM NO OF PARTICIPANTS:**	10
16	**DETAILS OF SPONSORERS:**	---
17	**DETAILS OF RESOURCE PERSONS:**	**1. Mr. Shrivatsa Koulgi** **Asst. Professor, Dept. of ISE** **SDMCET, Dharwad**
18	**REGISTRATION FEE WITH CERTIFICATION:**	---
19	**REGISTRATION FEE WITHOUT CERTIFICATION:**	---
20	**FACULTY COORDINATOR:**	1. Mr. Chetan Gudi 2. Mr. Sivakumar Reddy
21	**EVENT ORGANISERS (STUDENTS):**	---
22	**PROGRAMME PROPOSED BY:**	Dept. of CSE
23	**ANY OTHER RELEVANT MATTER:**	Nil
24	**ESTIMATED BUDGET IN RS:**	Rs. 52000

PRINCIPAL 1.6.15

PRINCIPAL
Rayadaya Institute of Technology (NIT)
RAICHUR-584 103

HOD

Head of Department
Computer Science & Engineering
Rayadaya Institute of Technology
RAICHUR-584 103

01/06/15
EVENT COORDINATOR

RAYADAYA INSTITUTE OF TECHNOLOGY RAICHUR

NAVODAYA
Institute of Technology

Department of Computer Science & Engineering

Five days' FDP on
"Android Application Development"
on
7th to 11th July 2015

REGISTRATION FORM

Name : _____

Branch : _____

Designation : _____

College Name : _____
& Address

_____

Phone/ Mobile: _____

E-mail : _____

Registration Fee Details

DD No. : _____ Date: _____

Bank : _____

Accommodation: YES ☐ NO ☐

DECLARATION: This is to certify that the above member is bonafide Staff of this institution/ Organization.

Signature of the Participant

Sign. Of HOD/Principal/
Head of Organization (with seal)

DD to be drawn in favor of "PRINCIPAL, NIT Raichur" payable at SBI, NET Campus, Raichur.

PRINCIPAL 6.7.15
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Chief Patron:
Sri S. R. Reddy,
Hon'ble Chairman, NET® Raichur

Patron:
Sri N. Bharath Reddy,
Trustee, NET® Raichur

Chief Convener:
Dr. Shivareddy M S
Principal,
N I T Raichur

Convener
Mr. Lohitha H N
HOD, CSE, NIT Raichur

Coordinator:
Prof. Sivakumar Reddy, CSE, NIT
E-Mail: bskreddy64@gmail.com
Contact no. : +91 – 9686281370

Co-Coordinator
Prof. Chetan Gudi, CSE, NIT
E-Mail: chetangudi@navodaya.edu.in
Contact no. : +91 - 9663426789

Organizing Committee:

Prof. Hanumesh D	Asst. Professor
Prof.Gayatri Anand	Asst. Professor
Prof.Rajashekhar R	Asst. Professor
Prof. Madhushree N	Asst. Professor
Prof.Vijay Kumar	Asst. Professor
Prof.Shanthi E	Asst. Professor
Prof. Jayashree	Asst. Professor

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Five days' FDP
On
"Android Application Development"
On
7th to 11th July 2015

NAVODAYA
Institute of Technology



Organized by

Department of Computer Science & Engineering
Navodaya Nagar, Raichur-584 103, Karnataka.
Tel:08532-240115,240115
Mobile No: +91-9686281370
: +91-9663426789
Fax : 08532-240054
e-mail :- hodcse.nit@navodaya.edu.in
web :- ww.navodaya.edu.in/engineering

06/07/15
Head of Department
Computer Science & Engineering
Navodaya Institute of Technology
RAICHUR-584 103

ABOUT THE INSTITUTE

Navodaya Institute of Technology (NIT), Raichur, established in 2008, is affiliated to Visvesvaraya Technological University (VTU), Belagavi and is approved by All India Council for Technical Education (AICTE), New Delhi. The growing need and the driving force for all the progress and achievements of the metropolis is our most respected Chairman **Sri. S. R. Reddy** of Navodaya Education Trust, Raichur. NIT, has scope of providing quality technical education in North Karnataka region in general and Hyderabad Karnataka Region in particular. The educational environment empowers its students' development. The Institute focuses on Digital Learning & Technical training with value based education to enable overall development of aspirants.

NIT is in an exclusive campus. Administrative Block, Library Block, Separate and Independent Academic Blocks for each Department, Power back up, Hostels for Boys and Girls, Canteen, Bank, ATM, Campus Transport, Internet, Playground, Auditorium in a safe campus of about 30 acres and surrounded by hillocks and lake.

ABOUT THE DEPARTMENT

The department of Computer Science & Engineering is established in the year 2010. The department offers BE program in Computer Science & Engineering with annual intake 60. The Department has excellent lab facilities with state of the art equipments and has exclusive Computer Centers equipped with 60 computer systems, 30 core 2 dual personal computers in the centre & one NT server including number of Computer Science & Engineering related software packages. The department is provided with highly qualified experienced and committed faculty members and faculty are from reputed institutes. The Department gives importance not only to academic education but also to extra and co-curricular activities to students for practicing a good technical events on regular basis. The department strives hard to impart quality education and to keep the students in the competitive edge of the research activities, encouraging towards University level and the students are also involved in placement of the students through campus interviews.

VISION

To be acknowledged as a center of excellence in Computer Science & Engineering through teaching, research and extension programs to the context of societal needs globally and foster a culture dedicated to learning & discovery and be known as a world-class center of innovation.

MISSION

The mission of the department is to participate, strive for achieving excellence in computing disciplines by being pursued through an spectrum of academic programmes in computing of contemporary standards. The continued efforts is to produce computing graduates with potential to design and develop systems involving the integration of software and hardware devices, innovative approaches to programming and problem solving as well as creative ways to use computers. Large scale software systems and computing infrastructure of an organization.

ABOUT WORKSHOP

Android application development is the process by which applications are created for devices running the Android operating system. Google states that "Android apps can be written using Kotlin, Java, and C++ languages" using the Android software development kit (SDK), while using other languages is also possible. Android is an open source and Linux-based operating system for mobile devices such as smartphones and tablet computers. Android was developed by the Open Handset Alliance, led by Google, and other companies. This FDP will teach you basic Android programming and will also take you through some advance concepts related to Android application development.

This course is open to students, Research Scholars, Faculties and Industrial Employees. **Number of participants is limited to 50.**

RESOURCE PERSON

Mr. Shrivatsa Kesalge
Asst. Professor
Dept. of ISE
SDMCET, Dharwad

IMPORTANT DATES

Last date for Registration	03/07/2015
Participants list confirmation via E-mail	04/07/2015

REGISTRATION FEES

Research Scholar	Rs. 500
Faculties	Rs. 500
Industrial Employees	Rs. 700

The Demand Draft drawn in favour of "**Principal, NIT Raichur**" payable at **NIT Campus Raichur**

ACCOMODATION

For the period of the workshop, the participants from the AICTE approved institutions will be provided accommodation in the student's hostel of Navodaya Institute of Technology, Raichur. Hostel Accommodation will be arranged based on their request.

How to reach Raichur

Raichur is well connected by Road and Railway to all nearby and important cities like Bangalore, Hyderabad, Chennai,



NEU's
NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR
Tel : +91 8532-200115, 240334, Fax : +91-8532-240054
E-Mail :hodcse.nit@navodaya.edu.in

CIRCULAR

Cir: 2015 16/FDP/02

22.06.2015

Respected Sir / Madam,

Greetings from Navodaya Institute of Technology, Raichur...

The department of Computer Science & Engineering is organizing a 5 day FDP on **"Android Application Development"** The attached brochure may kindly be circulated among the faculty members / colleagues.

Android is an open source and Linux-based operating system for mobile devices such as smartphones and tablet computers. Android was developed by the Open Handset Alliance, led by Google, and other companies. This FDP will teach you basic Android programming and will also take you through some advance concepts related to Android application development.

We are pleased to inform that the department of Computer Science & Engineering, Navodaya Institute of Technology is organizing a Faculty Development Programme during 7th July - 11th July 2015.

In this context, we look forward your support and request you to encourage participation of professionals by circulating the attached brochure.

Thanking you,
Yours sincerely,

Mr. Lohitha H N
Convener

Head of Department
Computer Science & Engineering
Navodaya Institute of Technology
RAICHUR-584 103

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Chief Patron
Shri. S. R. Reddy, Hon'ble Chairman,
Navodaya Education Trust (NET®),
Raichur.

Patron
Shri. N. Bharath Reddy., Hon'ble Trustee,
Navodaya Education Trust (NET®),
Raichur.

Chief Convener
Dr.Shivareddy M S,
Principal, NIT, Raichur

Convener
Mr. Lohitha H N
(I/C) HOD, CSE, NIT. Raichur

Coordinator
Prof. Chetan Gudi, CSE, NIT
E-Mail: chetangudi@navodaya.edu.in
Contact no. : +91 - 9663426789

Co-Coordinator
Prof.Sivakumar Reddy, CSE, NIT
E-Mail: bskreddy64@gmail.com
Contact no. : +91 - 9686281370

Organizing Committee
1. Prof. Hanumesh D
2. Prof.Gayatri Anand
3. Prof.Rajashekhar Reddy
4. Prof. Madhushree N
5. Prof.Vijay Kumar
6. Prof.Shanthi E
7. Prof. Jayashree

NAVODAYA INSTITUTE OF TECHNOLOGY

BELLARY ROAD, RAICHUR - 584103

INVITATION

Department of Computer Science & Engineering

HODs & Faculties are invited to attend Five days

Faculty Development Program

on

"Android Application Development"

By

Mr. Shrivatsa Koulgi
Asst. Professor, Dept. of ISE
SDMCET, Dharwad

From *7th July To 11th July 2015*

Venue: Department of CSE

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

06/07/15

Computer Science & Engineering
Navodaya Institute of Technology

Inaugural function - Agenda

1. Introduction
2. Invocation song
3. Welcome address by Mr. Lohitha R N -(I/C) HOD/CSE
5. Introduction of Resource person
6. Chief Guest Address
7. Presidential address
8. FDP- Session - 1

Five day FDP on

Android Application Development

Valedictory Function - Agenda

1. Introduction
2. Feedback - Faculty participants
3. Feedback by HOD
5. Feedback by Coordinator
6. Feedback by Resource person/Chief Guest
7. Certificate distribution
8. Presidential address
9. Vote of Thanks

PRINCIPAL
Navodaya Institute of Technology (RIT)
RAICHUR-584 103

NAVODAYA

Faculty development programme on

Android Application Development

Schedule on 07.07.2015 to 11.07.2015

Day	*Session-1 (09.30 am to 11.00am)*			*Session-2 (11.15 am to 12.45pm)*		*Session-2 (2.00 pm to 3.30pm)*		*Session-3 (3.45pm to 5.15pm)*
Tuesday (07.07.2015)	*Registration 09.00 to 10.00*	*Inauguration and Key note address by* Mr. Shrivatsa Koulgi Asst. Professor, Dept. of ISE, SDMCET, Dharwad		*Mr. Shrivatsa Koulgi Asst. Professor, Dept. of ISE SDMCET, Dharwad* (Introduction to Mobile Apps)		*Mr. Shrivatsa Koulgi Asst. Professor, Dept. of ISE SDMCET, Dharwad* (Introduction to Mobile Apps)		*Mr. Shrivatsa Koulgi Asst. Professor, Dept. of ISE SDMCET, Dharwad* (Android Architecture)
Wednesday (08.07.2015)	*Mr. Shrivatsa Koulgi Asst. Professor, Dept. of ISE SDMCET, Dharwad* (Android Architecture)		*Tea Break (11.00am To 11.15 am)*	*Mr. Shrivatsa Koulgi Asst. Professor, Dept. of ISE SDMCET, Dharwad* (Activities, Intents)	*Lunch break (12.45pm to 02.00pm)*	*Mr. Shrivatsa Koulgi Asst. Professor, Dept. of ISE SDMCET, Dharwad* (Android UI, Web View)	*Tea Break (03.30pm To 03.45 pm)*	*Mr. Shrivatsa Koulgi Asst. Professor, Dept. of ISE SDMCET, Dharwad* (Android UI, Web View)

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Computer Science & Engineering
Navodaya Institute of Technology
RAICHUR-584103

Day	Session-1 (09.30 am to 11.00am)		Session-2 (11.15 am to 12.45pm)		Session-3 (2.00 pm to 3.30pm)		Session-4 (3.45pm to 5.15pm)
Thursday (09.07.2015)	Mr. Shrivatsa Koulgi Asst. Professor, Dept. of ISE SDMCET, Dharwad (Image, Audio and Video Processing)		Mr. Shrivatsa Koulgi Asst. Professor, Dept. of ISE SDMCET, Dharwad (Image, Audio and Video Processing)		Mr. Shrivatsa Koulgi Asst. Professor, Dept. of ISE SDMCET, Dharwad (Data Storage, SQLite)		Mr. Shrivatsa Koulgi Asst. Professor, Dept. of ISE SDMCET, Dharwad (Data Storage, SQLite)
Friday (10.07.2015)	Mr. Shrivatsa Koulgi Asst. Professor, Dept. of ISE SDMCET, Dharwad (SMS, Calling, Email)	Tea Break (11.00am To 11.15 am)	Mr. Shrivatsa Koulgi Asst. Professor, Dept. of ISE SDMCET, Dharwad (Sensors & Location based Services)	Lunch break (12.45pm to 02.00pm)	Mr. Shrivatsa Koulgi Asst. Professor, Dept. of ISE SDMCET, Dharwad (Bluetooth & WiFi)	Tea Break (03.30pm To 03.45 pm)	Mr. Shrivatsa Koulgi Asst. Professor, Dept. of ISE SDMCET, Dharwad (Client-Server Programming)
Saturday (11.07.2015)	Mr. Shrivatsa Koulgi Asst. Professor, Dept. of ISE SDMCET, Dharwad (Web services in Android)		Mr. Shrivatsa Koulgi Asst. Professor, Dept. of ISE SDMCET, Dharwad (Case Studies, Mini Project)		Mr. Shrivatsa Koulgi Asst. Professor, Dept. of ISE SDMCET, Dharwad (Case Studies, Mini Project)		Mr. Shrivatsa Koulgi Asst. Professor, Dept. of ISE SDMCET, Dharwad (Case Studies, Mini Project)

PRINCIPAL
Basaveshwar Engineering College
Raichur-584 103

Date: 02.07.2015

From
Mr. Lohitha H N.
(I/C) Head.
Department of CSE/ISE
Navodaya Institute of Technology.
Raichur-584103.

To
The Principal,
Navodaya Institute of Technology.
Raichur-584103

Sir

Subject: Req. for Auditorium hall to conduct FDP - Reg

We have proposed to conduct the FDP on the topic "**Android** Application Development" from 07.07.2015 to 11.07.2015. For this Purpose, we need auditorium on 01.07.2015 for inauguration function. From 10:00AM-12:00PM. Hands on session will be conducted in SSW Laboratory, Dept. of CSE. Kindly permit us to use the auditorium and other resources needed.

Thanking You

Yours faithfully

[Mr. Lohitha H N]

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

02.07.2015

From

Mr. Lohitha H N,
(I/C) HOD, CSE.,
NIT, Raichur

To

The Principal,
NIT, Raichur.

Sir,

Sub: Requisition for Accommodation and Food -Reg

I bring to your kind notice that we are conducting the Five days FDP on "Android Application Development" in our department on 7th July 2015 to 11th July 2015. For that we want to arrange the accommodation and food for the Resource Person on the above said days. So kindly do the needful.

Resource Person Details,

1. **Mr. Shrivatsa Koulgi**
   Asst. Professor, Dept. of ISE
   SDMCET, Dharwad

Thanking you,

Lohitha 02/07/15.
HOD, CSE

PRINCIPAL 2.7.15
Udayagiri Institute of Technology (NIT)
RAICHUR-584 103

NATIONAL INSTITUTE OF TECHNOLOGY RAICHUR

Head of Department
Computer Science & Engineering
Udayagiri Institute of Technology
RAICHUR-584 103

NAVODAYA
Institute of Technology
RAICHUR

Department of Computer Science & Engineering

Faculty Development Programme (FDP) on

"Android Application Development"

From 7th July to 11th July 2015

Participants Details

S.No.	Name of the Faculty	Designation & Dept.
1.	Prof. Lohitha H N	Asst. Professor(I/C HOD)
2.	Prof. Chetan Gudi	Asst. Professor, CSE
3.	Prof. Jayashree	Asst. Professor, CSE
4.	Prof. Hanumesh D	Asst. Professor, CSE
5.	Prof.Gayatri Anand	Asst. Professor, CSE
6.	Prof.Vijay Kumar	Asst. Professor, CSE
7.	Prof. Madhushree N	Asst. Professor, CSE
8.	Prof. Sivakumar Reddy	Asst. Professor, CSE
9.	Prof.Shanthi E	Asst. Professor, CSE
10.	Prof.Rajashekhar Reddy	Asst. Professor, CSE
11.	Prof. Rammurthy	Asst. Professor, ECE
12.	Prof. Daniel N	Asst. Professor, EEE
13.	Prof. Geetha	Asst. Professor, EEE
14.	Prof. Majumdar	Asst. Professor, EEE
15.	Prof. Sachin	Asst. Professor, ECE
16.	Prof. Chennaveeramma E	Asst. Professor, ECE

11.7.15

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Lohitha 11/07/15

HOD & Convener

Head of Department
Computer Science & Engineering
Navodaya Institute of Technology
RAICHUR-584 103

Report on "Android Application Development"

The Department of computer science and Engineering, NIT Raichur has organized a five Day Faculty Development programme on "Android Application Development" from 07th – 11th July 2015. Mr. Shrivatsa Koulagi, Assistant Professor, Dept of ISE, SDMCET, Dharwad has started a session on need of Android Application Development Technology for faculty members.

The FDP was designed to get exposure in the main facts of Android and web application development including android life cycle and android application structure, creating project, preparing the file for text controls and button controls, and finally Android and provide knowledge to the participants in the area of web application development. The applications of Android and web application development in the current industrial scenario were also discussed in details.



PRINCIPAL 11.7.15
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

11/07/15.

NAVODAYA
Institute of Technology
RAICHUR

RAICHUR- 584103, KARNATAKA

DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

CERTIFICATE

This is to certify that Mr./Ms./Dr. Chetan Gudi of CSE has attended Five day FDP on *"Android Application Development"* conducted at Navodaya Institute of Technology, Raichur on 07-07-2015 to 11-07-2015.

Coordinator 11/07/15

HOD

Principal 11/7/15

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

NAVODAYA
Institute of Technology
RAICHUR

RAICHUR- 584103, KARNATAKA

DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

CERTIFICATE

This is to certify that Mr./Ms./Dr. Sivakumar Reddy of CSE has attended Five day FDP on *"Android Application Development"* conducted at Navodaya Institute of Technology, Raichur on 07-07-2015 to 11-07-2015.

Coordinator

HOD

Principal

NET's
NAVODAYA
Institute of Technology
RAICHUR

RAICHUR- 584103, KARNATAKA

DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

CERTIFICATE

This is to certify that Mr./Ms./Dr. Shanthi E of CSE has attended Five day FDP on

"Android Application Development" conducted at Navodaya Institute of Technology, Raichur

on 07-07-2015 to 11-07-2015.

Coordinator

HOD

Principal

NAVODAYA
Institute of Technology
RAICHUR

RAICHUR- 584103, KARNATAKA

DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

CERTIFICATE

This is to certify that Mr./Ms./Dr. Vijaykumar Yadav of CSE has attended Five day FDP on *"Android Application Development"* conducted at Navodaya Institute of Technology, Raichur on 07-07-2015 to 11-07-2015.

Coordinator

Lohitha
HOD

Principal

NAVODAYA
Institute of Technology
RAICHUR

RAICHUR- 584103, KARNATAKA

DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

CERTIFICATE

This is to certify that Mr./Ms./Dr. Hanumesh D of CSE has attended Five day FDP on *"Android Application Development"* conducted at Navodaya Institute of Technology, Raichur on 07-07-2015 to 11-07-2015.

Coordinator 11/07/15

HOD

Principal

Shrivatsa Koulgi
Asst. Professor
Dept. of ISE
SDM College of Engineering and Technology
Dharwad
e-mail: shrivatsa.koulgi@gmail.com
Mobile: +91-9036908670



Work experience:

Institution	Designation	Experience (in years)	Courses handled
Dept. of ISE, BVBCET, Hubli (Autonomous)	Lecturer	3.5	System software, Web technology, compiler design, Essentials of IT (Industry elective, with Infosys), DS Lab, SS Lab, CN Lab
Dept. of ISE, SDMCET, Dharwad (Autonomous)	Assistant Professor	2	M. Tech Courses (Enterprise Application programming, Android Application development) UG courses (Compiler Design, USP)

Strengths:

- Good programming skills with C/C++, JAVA, Android, Python
- Cleared GATE-2016 with overall ranking of **4124**.
- Worked with autonomous institutions in accordance with Outcome based education (OBE).
- Can contribute towards OBE activities involving Course Outcomes (CO's), Programme outcomes (PO's) etc…
- Worked as a department Project coordinator for the previous academic year 2015-16, Dept. of ISE, SDMCET, Dharwad

Areas interested:

- Text Mining, Machine learning , Compilers, Automata, Application development

M.Tech Project:

- *"Parallelization of multi document summarization using multi-threading"*

Papers Presented:

- "Parallelization of multi document summarization using multi-threading", International Journal of Engineering Research & Technology (IJERT), ISSN: 2278-0181, Vol. 3 Issue 7, July – 2014
- "Recommendation Item Based on Keyword Search using Big Data", International Journal on Recent and Innovation Trends in Computing and Communication (IJRITCC), ISSN: 2321-8169, Volume: 4 Issue: 7, July-2016
- "Textured Based Steganography using LSB", International Journal of Research in Advent Technology (IJRAT), E-ISSN: 2321-9637, Vol.4, Issue 7, July 2016

Technologies:

- C/ C++, Java, Android, Web programming, Hibernate, Swings.
- Operating systems- Windows, Linux (Ubuntu, Open Suse)

Projects Guided:

- "Smart Irrigation system based on IoT"
- Parts of Speech Tagger
- "Optical Character Recognition for Kannada"
- "Criminal Database maintenance system for Police department"
- Android application for "Blood and Platelet information system for Cancer Hospital , Navanagar"

Co-Curricular Activities :

- Secured 3rd place at **national level paper presentation** event '**think-10**' on '**Text Summarization using NLP**' held at SDMCET, Dharwad.
- Organized a National level workshop on **"Massively parallel computing"** in collaboration with NVIDIA.
- Secured **3rd place** for the project '**Text Summarization using NLP**' in **state level project exhibition competition** in the event '**SRISHTI**' held at SSIT, Tumkur.
- Secured **1nd place at state level on C Programming** event '**Brainstorm-10**' held at SDMCET, Dharwad.
- Secured **1st place at State Level On Spot C Programming** event '**Think-09**' held at SDMCET, Dharwad.
- Secured **2nd place at state level on spot C Programming** event '**Brainstorm-09**' held at SDMCET, Dharwad.

Extra-Curricular Activities :

Learning **Tabla** since 14 years. Performed all over India with well-known musicians.

- All India Radio "**B HIGH**" grade artist.
- Secured **1st** prize in National level competition conducted by All India Radio for the year 2010-11
- Awarded with **"Pt. Nikhil Ghosh Memorial Award"** by NCPA, Mumbai
- Won **1st** place in ' **Yuva Dharwad Utsav** '.
- Won **2nd** place in **VTU** cultural festival held in SBMJCE Bangalore.
- Won **2nd** place in **VTU** cultural team selection held at SBMJCE Bangalore.
- Completed '**Vidwat 1st** 'with distinction.



NET's

NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

~~GUEST LECTURE/SEMINR/ADD-ON COURSE~~/ *Faculty Development Programme* ~~WORKSHOP~~/ ~~CONFERENCE/SHORT TERM TRAINING PROGRAMME/ANY OTHER PROPOSAL~~

Date: 04.04.2016

1	ACADEMIC YEAR	2015-16
2	TYPE OF THE PROGRAMME PROPOSED	*Faculty Development Programme*
3	TITLE OF THE PROGRAMME:	**"Latex an open Source Technology and Role of Modelling in Research Methodologies"**
4	BROAD OBJECTIVE OF THE PROGRAMME:	LaTeX is a high-quality typesetting system; it includes features designed for the production of technical and scientific documentation. LaTeX is the de facto standard for the communication and publication of scientific documents. LaTeX is available as free software. Model building is an integral part of the social sciences because models guide both theory development and research design. Model building has been accentuated and accelerated by many forces in contemporary life. Models seem appropriate to the worlds of computers, biotechnology, and automation, and they have conferred new status on the scientist in government, industry, and the military. Models are also very important to social scientists because they provide a framework through which important questions are investigated.
5	NAME OF THE INSTITUTION	NET's NAVODAYA INSTITUTE OF TECHNOLOGY
6	NAME OF THE DEPARTMENT(S)	COMPUTER SCIENCE & ENGINEERING
7	BENEFICIARY	Faculty Members
8	PROPOSED DATE OF THE PROGRAMME	07.05.2016 to 08.05.2016 (02 days)
9	ELIGIBILITY CRITERIA SET:	Faculties from CSE, ECE & EEE
10	NUMBER OF DAYS REQUIRED TO ORGANISE THE PROGRAMME	Two Days
11	PROPOSED DURATION OF THE PROGRAMME	12 Hours

12	**TOTAL NO. OF PERIODS REQUIRED FOR THE PROGRAMME**	12 Periods
13	**NO. OF PERIODS REQUIRED PER DAY**	06 Periods
14	**LOCATION DETAILS:**	NETs NIT Auditorium
15	**MINIMUM NO OF PARTICIPANTS:**	20
16	**DETAILS OF SPONSORERS:**	---
17	**DETAILS OF RESOURCE PERSONS:**	**1. Dr. J. V. Ramana Reddy,** Research Associate, Defense University (DIAT), Pune. **2. Dr. A. V. N. Krishna,** Professor, Department of CSE, NIT, Raichur.
18	**REGISTRATION FEE WITH CERTIFICATION:**	---
19	**REGISTRATION FEE WITHOUT CERTIFICATION:**	---
20	**FACULTY COORDINATOR:**	1.Mr. Sivakumar Reddy 2. Mr. Chetan Gudi
21	**EVENT ORGANISERS (STUDENTS):**	---
22	**PROGRAMME PROPOSED BY:**	Dept. of CSE
23	**ANY OTHER RELEVANT MATTER:**	Nil
24	**ESTIMATED BUDGET IN RS:**	Rs. 26000

PRINCIPAL

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

HOD 11/4/16

Head of Department
Computer Science & Engineering
Navodaya Institute of Technology
RAICHUR-584 103

EVENT COORDINATOR

INSTITUTE OF TECHNOLOGY RAICHUR

ABOUT RAICHUR:



Raichur, popularly known as 'Cotton City' is bounded by Krishna river on the North and Tungabhadra river on the south. A world famous Hatti Gold Mines is 75 kms away. RTPS, contributing major portion of state's power requirement is situated at Shakti Nagar about 15 kms from the city. Famous pilgrimage centres Mantralayam and Panchamukhi are within a radius of about 35 kms.

HOW TO REACH RAICHUR:

Raichur is well connected by Road and Railway to all nearby and important cities like Bengaluru, Hyderabad, Chennai, Mumbai, Tirupathi, Kalaburgi, Hubballi, Ballary and so on.

ABOUT THE INSTITUTION

Navodaya Institute of Technology, Raichur, Karnataka is a self-financing co-educational institution established in the year 2008 with all the necessary infrastructural facilities provided through the vision of Navodaya Education Trust (NET®) that ably governs the institution. The college is approved by AICTE, New Delhi and is affiliated to Visvesvaraya Technological University (Belagavi). The institution has completed Eight years of dedicated and excellent service in the field of technical education.

The guiding spirit and the driving force for all the progress and achievements of the institution is our honorable chairman of Navodaya Education Trust (NET®), Sri S. R. Reddy. NIT focuses on promoting quality technical education in north Karnataka region and Hyderabad Karnataka region in particular.

This institution offers six UG courses in MECH., CIVIL., CSE, ISE, ECE, EEE and Two PG courses in M.Tech. - Thermal Power Engineering and Digital Communication & Networking programmes.

The Vision of this college aims at developing NIT the most respected institution of higher learning and Research for its excellence in Science and Technology.

ABOUT THE DEPARTMENT

The Department was started in the year 2008 with an intake of 60 students with the vision to provide the quality technical education. The department of CSE/ISE has well established computer laboratories with internet connectivity, excellent library, experienced & highly qualified faculty members to impart knowledge in the areas of various programming languages, Data Structures, Networks, Database Systems, and Unix System Programming.

ABOUT THE FDP

At various stages in engineering study, one comes across Report or Thesis preparation for Research irrespective of the engineering discipline. A tool such as LaTeX can be quite handy to solve such problems, provided we can formulate the problems appropriately.

To start with a research, knowledge on modelling is needed. Thus one session in this FDP also focuses on the role of modelling in research methodologies.

This FDP will include a Practical session on Preparing a simple Report by writing code in LaTeX.



A Two Day

Faculty Development program

On "LATEX" an Open Source

Technology & Role of Modelling

in Research Methodologies

ORGANIZED BY

Department of Computer Science / Information Science & Engineering

NET's *

NAVODAYA INSTITUTE OF TECHNOLOGY

Navodaya Nagar, Bijengera Road, Raichur-584103, Karnataka, India.

Website: www.navodaya.edu.in

Registration Form
(Photocopy may be used if required)

Name (in BLOCK letters):

Academic Qualification:

Experience:

Designation &
Department:

College Address:

Name of Affiliated University:

E mail :

Contact Number:

Declaration:
The information furnished above is true to the best of my knowledge.

Date:
Place: Signature of the Applicant

Mr./Ms./Dr. _____
is an employee (faculty)/PG student of our institute. He / She will be permitted to attend the programme if selected.

Date:
Place:

Signature & Seal
H.O.D/Principal

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Chief Patron
Sri. S. R. Reddy, Hon'ble Chairman,
Navodaya Education Trust (NET®),
Raichur.

Patron
Sri. N. Bharath Reddy., Trustee,
Navodaya Education Trust (NET®),
Raichur.

Chief Convener
Dr.Shivaprakash C. K., Dean & Principal,
NIT, Raichur

Convener
Dr. A V N Krishna, HOD-CSE, NIT-R

Address for communication:
The Principal
Navodaya Institute of Technology (NIT),
Navodaya Nagar, Bijengera Road,
RAICHUR- 584 103.

Coordinator
Prof.Chetan Gudi
Dept of CSE, NIT
E-mail : chetangudi@navodaya.edu.in
Contact no. : +91 – 9663426789

Co-Coordinator
Prof. Sivakumar Reddy
Dept of CSE, NIT
E-mail : bskreddy64@gmail.com
Contact no. : +91 - 9686281370

Organizing Committee

1. Prof. Hanumesh D
2. Prof. Gayatri Anand
3. Prof. Rajashekar Reddy
4. Prof. Madhushree N
5. Prof. Vijay Kumar
6. Prof. Shanthi E
7. Prof. Lohitha H N
8. Prof. Jayashree

COURSE CONTENT

Day 01

- Introduction to LaTeX.
- Practical Session on LaTeX

Day 02

- Role of Modelling in Research Methodologies

Resource Persons:

1. Dr. J V Ramana Reddy Research Associate,
   Department of Applied Mathematics,
   Defense University (DIAT),
   Pune.

2. Dr. A V N Krishna
   Professor, Department of CSE,
   Navodaya Institute of Technology
   Raichur

Important dates
FDP date : 07th May & 08th May, 2016
Last date for Registration : Spot Registration

VENUE: NIT Auditorium, NIT, Raichur

NET's
NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR
Tel : +91 8532-200115, 240334, Fax : +91-8532-240054
E-Mail :hodcse.nit@navodaya.edu.in

CIRCULAR

c/o: 2015 16/FDP/01

20.04.2016

Respected Sir / Madam,

Greetings from Navodaya Institute of Technology, Raichur...

The department of Computer Science & Engineering is organizing a 1 week FDP on **"Latex an open Source Technology and Role of Modelling in Research Methodologies"** The attached brochure may kindly be circulated among the faculty members / colleagues.

LaTeX is a high-quality typesetting system; it includes features designed for the production of technical and scientific documentation. LaTeX is the de facto standard for the communication and publication of scientific documents. LaTeX is available as free software.

Model building is an integral part of the social sciences because models guide both theory development and research design. Model building has been accentuated and accelerated by many forces in contemporary life. Models seem appropriate to the worlds of computers, biotechnology, and automation, and they have conferred new status on the scientist in government, industry, and the military. Models are also very important to social scientists because they provide a framework through which important questions are investigated.

We are pleased to inform that the department of Computer Science & Engineering, Navodaya Institute of Technology is organizing a Faculty Development Programme during 7th May - 8th May 2016.

In this context, we look forward your support and request you to encourage participation of professionals by circulating the attached brochure.

Thanking you,
Yours sincerely,

Dr. A V N Krishna
Convener

Head of Department
Computer Science & Engineering
Navodaya Institute of Technology

Chief Patron
Shri. S. R. Reddy, Hon'ble Chairman,
Navodaya Education Trust (NET®),
Raichur.

Patron
Shri. N. Bharath Reddy., Hon'ble Trustee.
Navodaya Education Trust (NET®),
Raichur.

Chief Convener
Dr.ShivaPrakash C K.
Principal and Dean, NIT, Raichur

Convener
Dr.A V N Krishna
HOD, CSE, NIT. Raichur

Coordinator
Prof.Chetan Gudi, CSE, NIT
E-Mail: chetangudi@navodaya.edu.in
Contact no. : +91 - 9663426789

Co-Coordinator
Prof.Sivakumar Reddy, CSE, NIT
E-Mail: bskreddy64@gmail.com
Contact no. : +91 - 9686281370

Organizing Committee
1. Prof. Hanumesh D
2. Prof.Gayatri Anand
3. Prof.Rajashekhar Reddy
4. Prof. Madhushree N
5. Prof.Vijay Kumar
6. Prof.Shanthi E
7. Prof. Lohitha H N
8. Prof. Jayashree

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA
Institute of Technology

Invitation Letter

DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

A Two Day

Faculty Development program

On

"LATEX" an Open Source Technology

BY,

Dr. J V Ramana Reddy,

Research Associate, Defense University (DIAT),

Pune.

And

Role of Modelling in Research Methodologies

BY,

Dr. A V N Krishna

Professor, Dept., of CSE, NIT

Raichur.

On

07th & 8th May 2016

At 10:00 AM

Venue: NIT SEMINAR HALL

INSTITUTE OF TECHNOLOGY RAICHUR NAVODAYA

Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Head of Department
Computer Science & Engineering
Navodaya Institute of Technology
RAICHUR-584 103

Date: 04.05.2016

From
Dr. A. V. N. Krishna,
Professor and Head,
Department of CSE/ISE
Navodaya Institute of Technology,
Raichur-584103.

To
The Principal,
Navodaya Institute of Technology,
Raichur-584103

Sir

Subject: Req. for Auditorium hall to conduct FDP - Reg

We have proposed to conduct the FDP on the topic "**Latex an open Source Technology and Role of Modeling in Research Methodologies**" on 07.05.2016 and 08.05.2016. For this Purpose, we need auditorium on above mentioned dates from 10.00AM-06:00PM on both days. Kindly permit us to use the auditorium.

Thanking You

Yours faithfully

[Dr. A. V. N Krishna]

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

From

26.04.2016

Dr. A V N Krishna,

HoD, CSE.,

NIT, Raichur

To

The Dean Principal,

NIT, Raichur.

Sir,

Sub: Requisition for Accommodation and Food -Reg

I bring to your kind notice that we are conducting the Two days FDP on "Latex an open Source Technology and Role of Modeling in Research Methodologies" in our department on 7th May 2016 to 08th May2016. For that we want to arrange the accommodation and food for the Resource Persons on the above said days. So kindly do the needful.

Resource Person Details,

1. **Dr. J. V. Ramana Reddy,**

   Research Associate,

   Defense University (DIAT),

   Pune.

Thanking you,

PRINCIPAL
National Institute of Technology (NIT)
RAICHUR-584 103

HOD, CSE

Head of Department
Computer Science & Engineering
National Institute of Technology
RAICHUR-584 103

NAVODAYA
Institute of Technology
RAICHUR

Department of Computer Science & Engineering

Faculty Development Programme (FDP) on

"Latex an open Source Technology and Role of Modelling in Research Methodologies"

From 7th May to 8th May 2016

Participants Details

S.No.	Name of the Faculty	Designation & Dept.	Signature
1.	Dr. A V N Krishna	Professor & HOD, CSE	
2.	Prof. Chetan Gudi	Asst. Professor, CSE	
3.	Prof. Sivakumar Reddy	Asst. Professor, CSE	
4.	Prof. Hanumesh D	Asst. Professor, CSE	
5.	Prof.Gayatri Anand	Asst. Professor, CSE	
6.	Prof.Rajashekhar Reddy	Asst. Professor, CSE	
7.	Prof. Madhushree N	Asst. Professor, CSE	
8.	Prof.Vijay Kumar	Asst. Professor, CSE	
9.	Prof.Shanthi E	Asst. Professor, CSE	
10.	Prof. Lohitha H N	Asst. Professor, CSE	
11.	Prof. Jayashree	Asst. Professor, CSE	
12.	Prof. Majumdar	Asst. Professor, EEE	
13.	Prof. Daniel N	Asst. Professor, EEE	
14.	Prof. Geetha	Asst. Professor, EEE	
15.	Prof. Rammurthy	Asst. Professor, ECE	
16.	Prof. Sachin	Asst. Professor, ECE	
17.	Prof. Chennaveeramma	Asst. Professor, ECE	
18.	Prof. Laxmikanth Reddy	Asst. Professor, ECE	

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

HOD & Convener

NAVODAYA INSTITUTE OF TECHNOLOGY, RAICHUR-584 103

Department of Computer Science / Information Science & Engineering

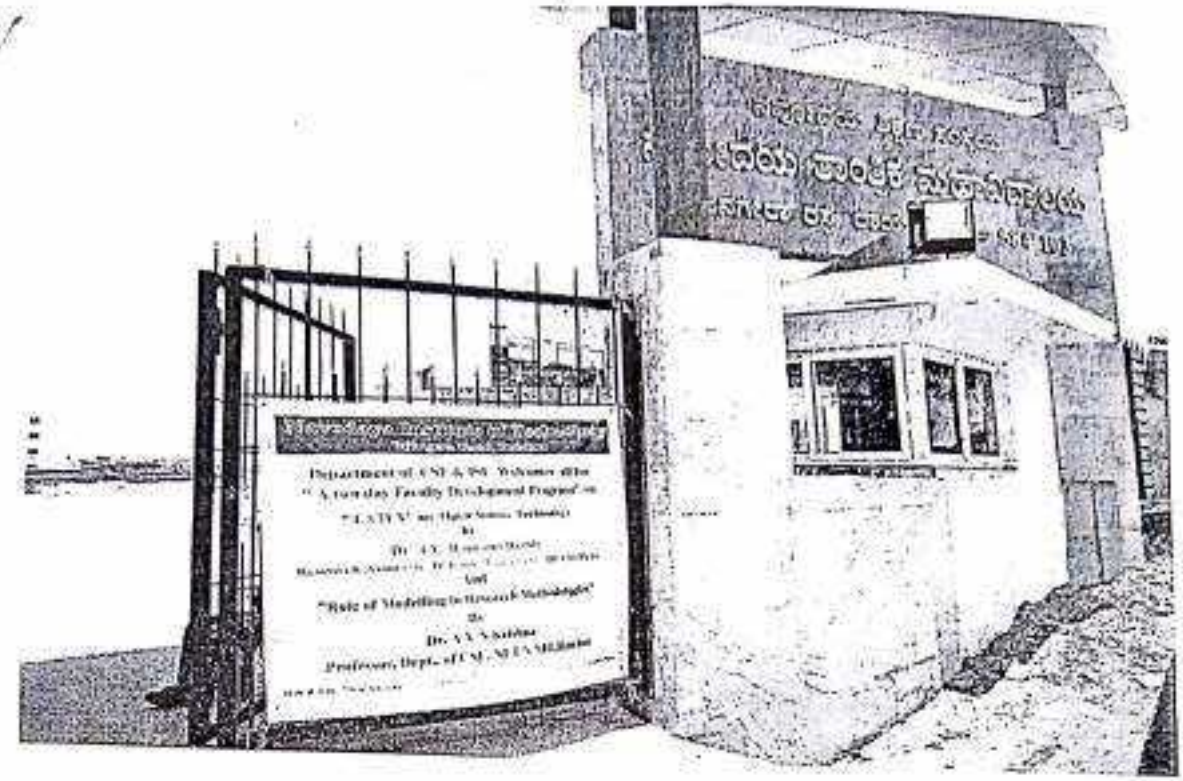
Report on Faculty Development Program Conducted on 7th and 8th of May 2016

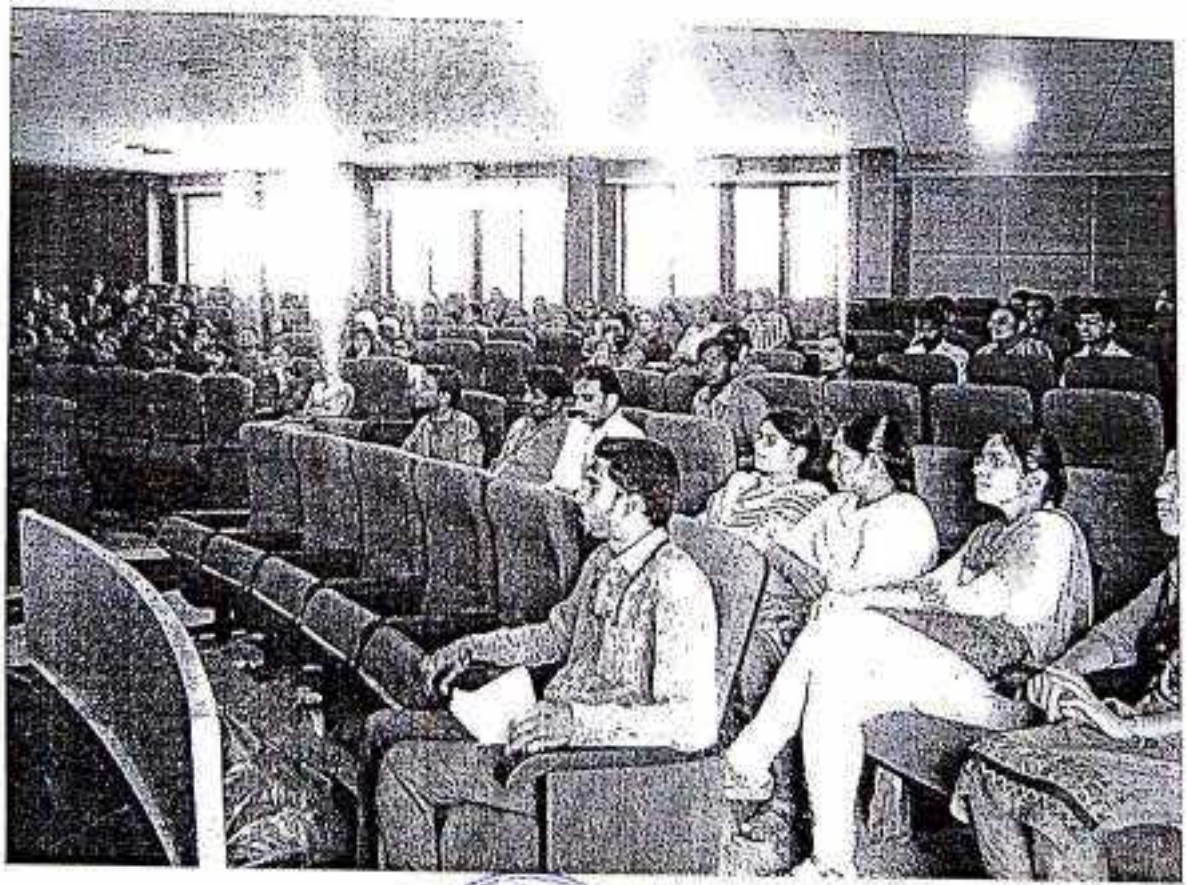
The FDP was comprised of 2 days of which on the 1st day it was on "LaTeX" an open source technology which was presented by Dr. J V Ramana Reddy, Research Associate, Defense University (DIAT), Pune. The Day started with PowerPoint Presentation on LaTeX introduction and its implementation Details. The session went on for Two and half hours with Faculty actively participating in its implementation Part. The Discussion included implementation of Mathematical Formulae, formatting issues and other relevant issues with respect to the documentation of books, publications and so on. Then there was a break for 15 min for Tea Session. The second session was followed with practical training of LaTeX on systems. The Resource person was very cooperative in clearing the doubts of the faculty in its applications. The session was well attended by faculty from CSE, ECE & EEE departments.

The second day of the program i.e., on 8th May was presented by Dr A V N Krishna Professor, Department of CSE, he started with introductory concepts and then followed with the relevance of modelling in the overall project development. He then emphasized on the design deployment and testing of mathematical models. He had considered a numerical mathematical model and clearly explained its relevance in multiple applications like thermal engineering, cryptography and data mining areas, thus the session has imbibed a lot of research interest in the faculty. The session ended at 12 noon by passing thanks to the professor by the faculty.

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Head of Department
Computer Science & Engineering
Navodaya Institute of Technology
RAICHUR-584 103

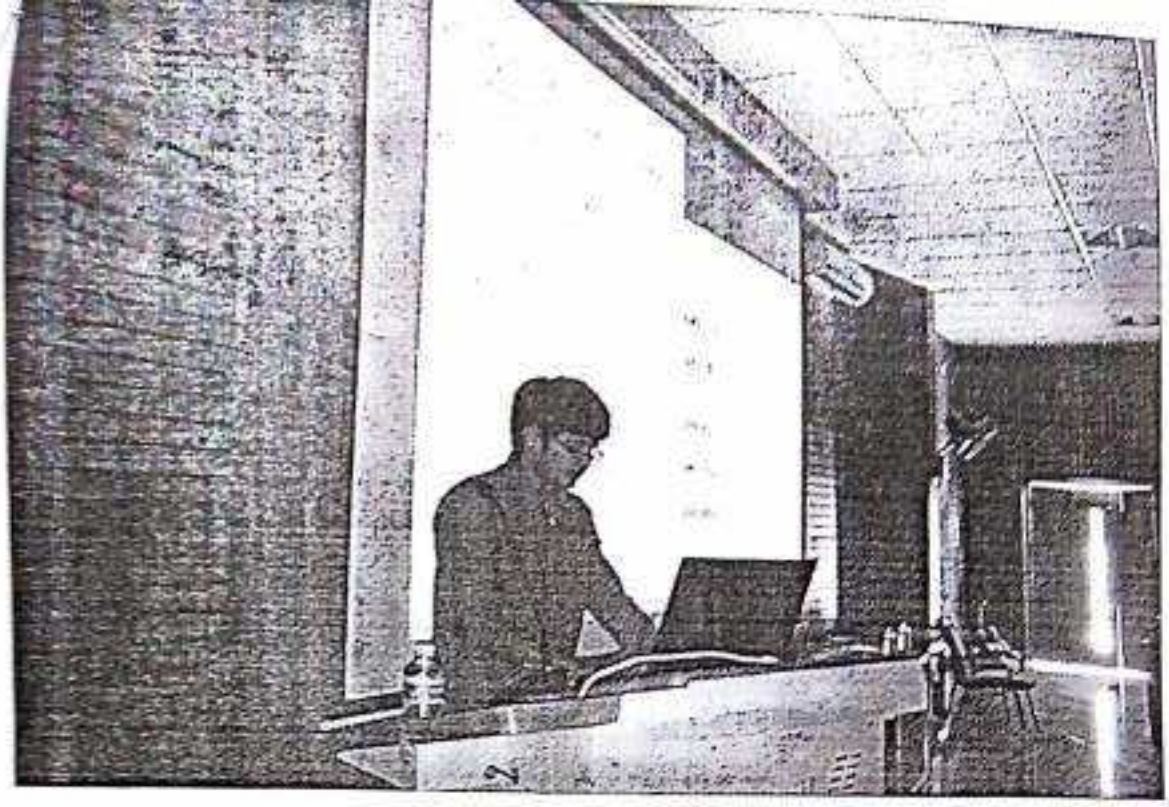




INSTITUTE OF TECHNOLOGY RAICHUR

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Head of Department
Computer Science & Engineering
Navodaya Institute of Technology
RAICHUR-584 103





PRINCIPAL
Rajiv Gandhi Institute of Technology (RIIT)
RAICHUR-584 103

Professor
Computer Science & Engineering
Rajiv Gandhi Institute of Technology
RAICHUR-584 103

NAVODAYA
Institute of Technology
RAICHUR

Navodaya Nagar, Bijanagera Road
RAICHUR 584 103, Karnataka, INDIA
Ph : 91-8532 240334/ 240115
Fax 240054/ 240061/ 223070
E-mail : net.princi@gmail.com
web : www.navodaya.edu.in

CERTIFICATE

This is to certify that Mr. /Ms. Chetan Gudi of

NET's-NIT, Raichur has attended the

'Two day Faculty Development Program'

On

"LATEX" an Open Source Technology

And

"Role of Modelling in Research Methodologies"

Organized by the department of Computer Science & Engineering.

On

07th & 8th May 2016.

HOD	Dean & Principal	Trustee
Dept. of CSE.	NET's- NIT, Raichur	NET's- NIT, Raichur

J V Ramana Reddy

ramana.sigc@gmail.com, mathjvr@iitm.ac.in

Department of Mathematics, Indian Institute of Technology Madras, India - 600036

Mob: +91 7620462979, DoB: 22-04-1989, Male

Education

Defence Institute of Advanced Technology(DU), Department of Applied Mathematics — Pune, India
Ph.D. (Research area: Biofluiddynamics, Mathematical Biology) — Aug 2011 to Dec 2016

University of Hyderabad — Hyderabad, India
Master's in Mathematics — July 2009 to July 2011

Silver Jubilee Government Degree College, Sri Krishnadevaraya University — Kurnool, India
Bachelor's in Sciences — April 2006 - April 2009

Research Experience

Post-Doctoral Fellow
Indian Institute of Technology Madras, Department of Mathematics — Chennai, India
Working with Prof. S Sundar — January 2018 to Present
Title: Analysis of a mathematical formulation between the blood flow turbulence intensity and arterial murmurs for earlier detection of stroke risk.

Research Associate
Indian Institute of Technology Kharagpur, Department of Mathematics — Kharagpur, India
Working with Prof. G P Raja Sekhar and Dr. P Sekhar Ramadi — September 2017 to January 2018
Title: Locomotion of Microorganisms under External Gradients

- Modelled and simulated the locomotion of the organism due to the internal activity and also calculated the power required to migrate. Consolidating the investigations in the form of the article.

Research Associate
Defence Institute of Advanced Technology(DU), DIAT-DRDO Project — Pune, India
Working with Dr. D Srikanth and Prof. Samir Kumar Das — Sep 2016 to Sep 2017
Title: Non-Newtonian nanofluid model for blood flow through Asymmetric tapered stenotic dynamic artery in the presence of catheter

- Modelled the colloidal suspensions of Ag nanoparticles in a modified polar fluid model. Blood with the suspensions of ceramic (nonmetallic) TiO_2 nanoparticles is also modelled by understanding it's Biomedical/Pharmaceutical Applications. The analyzed models can be used for drug delivery in the treatment of stenotic conditions.
- Developed customized mathematical solutions for the drug delivery modelled system. Numerically simulated the modeled highly coupled and nonlinear partial differential equations using the Homotopy Perturbation Method (HPM) and showed the convergence of the HPM method.
- Explored the rate of drug delivery from the temperature sensitive drug coated nanoparticles and also the dependency of the drug dispersion on the temperature at the targeted organs.
- Improving the skills in 3D modelling of the unhealthy artery, turbulent modelling, simulating the modelled problems using the FEM and exploring the FEinCS.

Ph.D. Scholar - JRF (Aug. 2011 to Aug. 2013) and SRF (Aug. 2013 to Aug. 2016)
Defence Institute of Advanced Technology(DU), Dept. of Applied Mathematics — Pune, India
Supervisor - Dr. D Srikanth and Dr. SVSSNVG Krishna Murthy — Aug 2011 to Dec 2016
Title: Mathematical Modelling of Blood Flow through Stenotic Tapered Arteries

- Polar fluid models are improved such that the resultant model is significant in interpreting the theological anomalies associated with blood flow (hemodynamics) in small vessels. The model has been developed by considering the fact that the blood is the suspension of white blood cells, red blood cells, and platelets in the plasma.
- Designed and conceptualized the stenosis and tapered blood vessel segments through mathematical equations.

Raichur-584 103

- Incorporated the viscoelastic property of the blood vessel wall through the flexibility of the arterial wall has been achieved by applying the one sided sinusoidal mathematical function.
- Diagnosis and treating procedure like the catheterization is integrated with the mathematical model. Analyzed the significant changes in blood flow dynamics once the catheter introduced into the lumen of the stenotic tapered artery.
- Developed the solution procedure to get the closed form solutions for some exploratory models. Whereas some other mathematical models having the system of the complexly coupled and highly non-linear partial differential equations. Consequently, a transformation has been developed which convert the irregular domain into a simple rectangular domain, along which the difference methods hold good. On this transformed domain, the solutions of the complex PDE's are obtained.
- Modelled the physiological process of the cardiac cycle by using the Trigonometric - Periodic Functions.
- Numerically evaluated the velocity, volumetric flow rate, impedance and the wall shear stress on the arterial wall. Further, Numerically predicted the impact of catheter radius, the height of the stenosis, tapering angle, Reynolds and Strouhal number on the physiological parameters.
- Attended workshops and conferences in the field of Biofluid mechanics and applied mathematics domains, and participated in oral presentations.

Skills and Techniques

- Expertise in fluid flow simulation using MATLAB and Mathematics
- Developed numerical simulation in FORTRAN and Octave
- Expertise in simulation through Computational Fluid Dynamic tools, Ansys-Fluent
- Analysis of research data, data plotting and Image processing in professional softwares like, Origin, Tecplot
- Expertise in drafting software tools like CSS, LaTeX, HTML and Office
- Experienced in Image processing (PCA, SHIFT, etc), Optimization programming (Pure pursuit algorithm with obstacles, etc) and develop GUI by using MATLAB
- Getting experience with OpenFOAM, GitHUb and del.II
- Can lead short term and long term Research projects

Languages

- Excellent spoken and written skills in English, Hindi and Telugu, Fair skills in Sanskrit.

Professional Experience

Teaching Assistant
Defence Institute of Advanced Technology(DU), India August 2011 - July 2016

- Taught subjects such as on Advanced Modelling Techniques, Simulation Tools Lab for M.Tech students.
- Conducted hour-long interactive tutorials on Advanced Calculus, Applications of mathematical modelling in Defence for Indian Tri-Service officers, as a part of special courses.
- Provided assistance to postgraduate students in their M.Tech dissertation.
- Led weekly laboratory, problem-solving and discussion sections for postgraduate students.

Honors, Awards, and Memberships

- Researcher of the year (Ph.D.) - 2015 award from Defence Institute of Advanced Technology, Ministry of Defence, India.
- Recipient of the DIAT institutional fellowship (CSIR - UGC norms of India) funded by DRDO, India, during PhD, from January 2011 to January 2016.
- Scholarship of Excellence - 2010 award from Basic Research Education & Development Society, India.
- Certificate of Merit in Mathematics, Silver Jubilee Government College, Sri Krishnadevaraya University, India.
- Qualified Graduate Aptitude Test in Engineering (GATE) - 2011 in Mathematics.
- Secured an all India 1st rank in Pondicherry University PG entrance examination in mathematics.
- Life Member of Indian Society for Theoretical and Applied Mechanics.
- Member of International Association of Engineers.

Principal
National Institute of Technology (NIT)
KAICHUR-584 103
RAICHUR
Professor
Indian Institute of Technology

PUBLICATIONS/CONFERENCES PROCEEDINGS

Journal articles

1. **J. V. Ramana Reddy**, D. Srikanth and Samir K Das. Modelling and simulation of temperature and concentration dispersion in couple stress nano fluid flow through stenotic tapered arteries. *European Physical Journal Plus*, 132(8): 365, 2017. https://doi.org/10.1140/epjp/i2017-11643-1
2. **J. V. Ramana Reddy**, D. Srikanth. Modelling and simulation of micropolar fluid flow with variable viscosity through unhealthy artery. *World Journal of Modelling and Simulation*. Accepted
3. **J. V. Ramana Reddy**, D. Srikanth, P. K. Mandal. *Computational hemodynamic analysis of flow through flexible permeable stenotic tapered artery*. International Journal of Applied and Computational Mathematics, 1-27, 2017. https://doi.org/10.1007/s40819-017-0415-4
4. **J. V. Ramana Reddy**, D. Srikanth and SVSSNVG Krishna Murthy. Mathematical modelling of time dependent flow of non-Newtonian fluid through unsymmetric stenotic tapered artery: Effects of catheter and slip velocity. *Meccanica*, 51(1):55 – 69, 2016. https://doi.org/10.1007/s11012-015-0201-5
5. **J. V. Ramana Reddy** and D. Srikanth. The polar fluid model for blood flow through a tapered artery with overlapping stenosis - Effects of catheter and velocity slip. *Applied Bionics and Biomechanics*, 2015. Article ID 174387, 2015. http://dx.doi.org/10.1155/2015/174387
6. D. Srikanth, **J. V. Ramana Reddy**, Shubha Jain and Anup Kale. Unsteady polar fluid model of blood flow through tapered overlapping stenosed artery: Effects of catheter and velocity slip. *Ain Shams Engineering Journal*, 6(3):1093 – 1104, 2015. https://doi.org/10.1016/j.asej.2015.01.003
7. **J. V. Ramana Reddy**, D. Srikanth and SVSSNVG Krishna Murthy. Mathematical modelling of pulsatile flow of blood through catheterized unsymmetric stenosed artery - Effects of tapering angle and slip velocity. *European Journal of Mechanics B/Fluids*, 48:236 – 244, 2014. https://doi.org/10.1016/j.euromechflu.2014.07.001
8. **J. V. R. Reddy**, D. Srikanth and S. K. Murthy. Mathematical modelling of couple stresses on fluid flow in constricted tapered artery in the presence of slip velocity - Effects of catheter. *Applied Mathematics and Mechanics*, 35(8):947 - 958, 2014. https://doi.org/10.1007/s10483-014-1848-7

Articles in under review

1. KM Surabhi, **J. V. Ramana Reddy**, D. Srikanth, P. K. Mandal. *Hemodynamics analysis of the blood flow model with the flexible wall of the blood vessel*, Mathematical Biosciences, Under revision
2. **J. V. Ramana Reddy**, D. Srikanth, Samir K Das. *Impact of blood vessel wall flexibility on the temperature and concentration dispersion*, Indian Journal of Pure and Applied Mathematics, Under review

Conference Proceedings

1. **J. V. Ramana Reddy**, D. Srikanth and SVSSNVG Krishna Murthy. Shear stress distribution at the wall of ω-shape stenotic tapered artery in the presence of catheter and velocity slip - Effects of polar fluid. In *proceedings of the International Conference on Frontiers in Mathematics (ICFM)*, pages 56-59, 2015. ISBN: 978-81-928118-9-5
2. Shubha jain, Anup Kale, D. Srikanth, **J. V. Ramana Reddy**. Analysis of resistance to the steady flow of blood through tapered overlapping stenosed artery in the presence of catheter and slip velocity. In *International Conference on Mathematical Sciences (ICMS)*, pages 332-335, 2014. ISBN-978-93-5107-261-4
3. **J. V. R. Reddy**, D. Srikanth and Krishna Murthy SVSSNVG. Resistance to the flow of micropolar fluid through unsymmetric stenosed artery - Effects of catheter and slip velocity. In *proceedings of 57th Indian Society of Theoretical and Applied Mechanics (ISTAM)*, 251-257, 2012.

CONFERENCE ORAL PRESENTATIONS

1. J V Ramana Reddy and D Srikanth. "Two dimensional blood flow with effects of stratification through composite stenotic tapered flexible artery - A mathematical model", Presented in International Conference on Mathematics (ICM) - 2015, November 26 - 28, 2015, University of Kerala, Thiruvananthapuram, **India**
2. J V Ramana Reddy and D Srikanth. "Analysis of pulsatile blood flow through a flexible artery - Effects of tapering angle, catheter, and velocity slip", Presented in Indian Society of Theoretical and Applied Mechanics (ISTAM) - 2014, December 17 - 20 , 2014, Alliance University, Bangalore, **India**.
3. J V Ramana Reddy, D Srikanth and SVSSNVG Krishna Murthy. "Numerical investigation of pulsatile blood flow through a flexible artery - Effects of catheter, tapering angle and velocity slip", Presented in International Congress of Mathematicians (ICM) - 2014, August 13 - 21, 2014, **Seoul, Korea**

Technology (IIIT)
RAICHUR-584 103

4. J. V. Ramana Reddy, D. Srikanth and SVSSNVG. Krishna Murthy. "Shear stress distribution in the wall of w - shape stenotic tapered artery in the presence of catheter and velocity slip - Effects of power fluid". Presented in the 2nd International Conference on Applications of Fluid Dynamics (ICAFD), 2014, July 31 - 03, 2014, Sri Venkateshwara University, Tirupati, **India**.

5. J V R Reddy, D. Srikanth and Krishna Murthy SVSSNVG. "Resistance to the flow of micropolar fluid through unsymmetric stenosed artery - Effects of catheter and slip velocity". Presented in Indian Society of Theoretical and Applied Mechanics (ISTAM) - 2012, December 17 - 20, 2012, Defence Institute of Advanced Technology, Pune, **India**.

Talks presented

1. Presentation on "Mathematical Modelling of Blood Flow Through Unhealthy Arteries" at **TIFR Centre for Applicable Mathematics**, Bangalore, **India**, August 17, 2016.
2. Invited talk on "LaTeX programming", at Navodaya Institute of Technology, Raichur, Karnataka, **India**, June 9, 2016.
3. Invited talk on "Applications of Mathematics in Biology", at D. B. F. Dayanand College of Arts and Science, Solapur University, **India**, December 23, 2015.

Workshops and Summer Schools Attended

- National Program on Differential Equations: Theory, Computation & Applications (NPDE-TCA), Advanced level, organized by the Department of Mathematics, Indian Institute of Technology Ropar, May 16 - June 4, 2016.
- Stability analysis of differential equations and applications of fluid flow problems, organized by Indian Institute of Technology Kanpur, March 20 - 24, 2014.
- Computational Methods in Engineering and Science, organized by National Institute of Technology, Warangal, India, October 21 - 25, 2013.
- Non-linear Differential Equations : Dynamics of Complex Systems, organized by University of Calcutta, India, September 23 - 28, 2013.
- Finite Element Modelling with MATLAB, organized by National Institute of Technology Surat, India, May 6 - 10, 2013.
- Non-Standard Finite Element Methods, organized by Indian Institute of Technology Mumbai, India, February 11 - 15, 2013.
- Captronic Systems training on LabVIEW, organized by Defence Institute of Advanced Technology, Pune, India, August 31 - September 5, 2012.
- Recent Trends in Partial Differential Equations and Applications, University of Hyderabad, India, March 18 - 19, 2012.
- CEP on Personality Development, organized by Defence Institute of Advanced Technology, Pune, India, January 23 - 28, 2012.
- Mathematics Training and Talent Search Program (MTTS), organized by NBHM at Regional Institute of Education Mysore, May 17 - June 14, 2010.

Professional Referees

- **Dr. D Srikanth**
  Assistant Professor
  Department of Applied Mathematics,
  Defence Institute of Advanced Technology,
  Girinagar, Pune - 411025, India.
  Phone: +91 - 2024 - 304385
  Mobile: +91 - 7798229930
  Email: sri_dasari1977@yahoo.co.in
  Relationship: PhD Adviser

- **Dr. P V S N Murthy**
  Professor
  Department of Mathematics,
  Indian Institute of Technology,
  Kharagpur - 721302, India
  Phone: +91 - 3222 - 283640
  Mobile: +91 - 3222 - 283647
  Email: pvsnm@maths.iitkgp.ernet.in
  Relationship: My PhD thesis examiner

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR - 584 103

NET's
NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

~~GUEST LECTURE/SEMINR/ADD-ON COURSE~~/ Faculty Development Programme ~~WORKSHOP/ CONFERENCE/SHORT TERM TRAINING PROGRAMME/ANY OTHER PROPOSAL

Date: 17.12.2016

1	**ACADEMIC YEAR**	2016-17
2	**TYPE OF THE PROGRAMME PROPOSED**	***Faculty Development Programme***
3	**TITLE OF THE PROGRAMME:**	**"Advanced Digital Image Processing & its Applications"**
4	**BROAD OBJECTIVE OF THE PROGRAMME:**	Digital image processing deals with manipulation of digital images through a digital computer. It is a subfield of signals and systems but focus particularly on images. DIP focuses on developing a computer system that is able to perform processing on an image. The input of that system is a digital image and the system process that image using efficient algorithms, and gives an image as an output. The most common example is Adobe Photoshop. It is one of the widely used application for processing digital images.
5	**NAME OF THE INSTITUTION**	NETs NAVODAYA INSTITUTE OF TECHNOLOGY
6	**NAME OF THE DEPARTMENT(S)**	COMPUTER SCIENCE & ENGINEERING
7	**BENEFICIARY**	Faculty Members
8	**PROPOSED DATE OF THE PROGRAMME**	24.01.2017 to 28.01.2017 (05 days)
9	**ELIGIBILITY CRITERIA SET:**	Faculty members of CSE, ECE, EEE
10	**NUMBER OF DAYS REQUIRED TO ORGANISE THE PROGRAMME**	05 Days
11	**PROPOSED DURATION OF THE PROGRAMME**	30 Hours
12	**TOTAL NO. OF PERIODS REQUIRED FOR THE PROGRAMME**	30 Periods
13	**NO. OF PERIODS REQUIRED PER DAY**	06 Periods
14	**LOCATION DETAILS:**	NETs NIT Department of CSE

15	**MINIMUM NO OF PARTICIPANTS:**	15
16	**DETAILS OF SPONSORERS:**	NIT Management
17	**DETAILS OF RESOURCE PERSONS:**	1. Dr. M N Faruk Asst. Professor, Dept. of CSE QIS, Ongole 2. Mr. Bande Nawaz MD, Illumninate Skills Hyderabad
18	**REGISTRATION FEE WITH CERTIFICATION:**	---
19	**REGISTRATION FEE WITHOUT CERTIFICATION:**	---
20	**FACULTY COORDINATOR:**	1.Mr. Sivakumar Reddy
21	**EVENT ORGANISERS (STUDENTS):**	---
22	**PROGRAMME PROPOSED BY:**	Dept. of CSE
23	**ANY OTHER RELEVANT MATTER:**	Nil
24	**ESTIMATED BUDGET IN RS:**	Rs.55,000/-

PRINCIPAL

PRINCIPAL
Rajiv Gandhi Institute of Technology (RGIT)
RAICHUR-584 103

HOD

Head of Department
Computer Science & Engineering
Rajiv Gandhi Institute of Technology
RAICHUR-584 103

EVENT COORDINATOR

RAICHUR

NAVODAYA
Institute of Technology

Department of Computer Science & Engineering

Five days' FDP on
"Advanced Digital Image Processing & its Applications"
on
24th to 28th January 2017

REGISTRATION FORM

Name : _____

Branch : _____

Designation : _____

College Name & Address : _____

_____

Phone/ Mobile : _____

E-mail : _____

Registration Fee Details

DD No. : _____ Date: _____

Bank : _____

Accommodation: YES ☐ NO ☐

DECLARATION: This is to certify that the above member is bonafide Staff of this institution/ Organization.

Signature of the Participant

Sign. Of HOD/Principal/ Head of Organization (with seal)

DD to be drawn in favor of "PRINCIPAL, NIT Raichur" payable at SBI, NET Campus, Raichur.

Chief Patron:
Sri S. R. Reddy,
Hon'ble Chairman, NET® Raichur

Patron:
Sri N. Bharath Reddy,
Trustee, NET® Raichur

Chief Convener:
Dr. Shivaprakash C K
Principal,
N.I.T Raichur

Convener
Mr. Chetan Gudi
(I/C) HOD, CSE, NIT, Raichur

Coordinator:
Prof. Sivakumar Reddy, CSE, NIT
E-Mail: bskreddy64@gmail.com
Contact no. : +91 – 9686281370

Organizing Committee:

1. Prof.Rajashekhar Reddy Asst. Professor
2. Prof.Shanthi E Asst. Professor
3. Prof. Usha Asst. Professor
4. Prof. Bhagavati Asst. Professor
5. Prof. Jayashree Asst. Professor
6. Prof. Hanumesh D Asst. Professor
7. Prof. Vijaykumar Y Asst. Professor

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Five days' FDP
On
"Advanced Digital Image Processing & its Applications"
On
24th to 28th January 2017

NAVODAYA
Institute of Technology



Organized by
Department of Computer Science & Engineering
Navodaya Nagar, Raichur-584 103, Karnataka.
Tel:08532-240115,240115
Mobile No: +91-9686281370
: +91-9663426789
Fax : 08532-240054

e-mail :- hodcse.nit@navodaya.edu.in
web :- ww.navodaya.edu.in/engineering

Head of Department
Computer Science & Engineering
Navodaya Institute of Technology
RAICHUR-584 103

ABOUT THE INSTITUTE:

avodaya Institute of Technology (NIT), Raichur, established in 2008, affiliated to Visveswaraya Technological University (VTU), Belagavi nd is approved by All India Council for Technical Education (AICTE), ew Delhi. The guiding spirit and the driving force for all the progress nd achievements of the institution is our most respected Chairman Sri. . R. Reddy of Navodaya Education Trust®, Raichur. NIT focuses on romoting quality technical education in North Karnataka region in neral and Hyderabad Karnataka Region in particular. The education is f paramount importance for socioeconomic development. The Institute ocuses on integral scientific &Technical training with value based ducation to enable overall development of aspirants.

IT-R is all-inclusive campus. Administrative Block, Library Block, eparate and Independent Academic Blocks for each Department, ower back up, Hostels for Boys and Girls, Canteen, Bank, ATM, ampus Temple, Gym and Playground all housed in a vast campus of bout 30 acres land surrounded by hillocks and lake.

ABOUT THE DEPARTMENT:

The department of Computer Science & Engineering is established in he year 2008. The department offers BE program in Computer Science & engineering with annual intake 60. The Department has excellent lab acilities with state of the art equipment and has exclusive Computer Centers equipped with 60 Core-i3 systems, 30 core 2 duo personal computers, 60 thin clients & one NT server including number of Computer Science & engineering related software packages. The department is enriched with highly qualified, experienced and committed faculty members and faculty are from reputed institutes. The Department gives importance not only to academic education but also ethical values and personality development of students by practicing a close mentoring system on regular basis. The department strives hard to impart quality education and to keep the students on the competitive edge of the present-day technology and marching towards University ranks also, the department is actively involved in placement of the students through ON/OFF Campus Interviews.

VISION:

"To be acknowledged as a center of excellence in Computer Science & Engineering through teaching, research and educational programs in the context of societal needs globally and foster a culture dedicated to learning & discovery and be known as a world-class center of innovation".

MISSION:

"The mission of the department is to persistently strive for achieving excellence in computing disciplines. It is being pursued through its spectrum of academic programmes in comprising of contemporary standards. The sustained effort is to produce computing graduates with potential to design and develop systems involving the integration of software and hardware devices; innovative approaches to programming and problem solving as well as creative ways to use Computers. Large scale software systems, and computing infrastructure of an organization"

ABOUT WORKSHOP

This FDP aims at providing strong theoretical background along with practical experience in the field of image processing and how the visualization and analysis of the images could be efficiently done with the help of image processing tools. In the growing and emerging era of "Digital India" initiative, the use of digital image processing becomes pertinent in the Image Processing area since several applications of the images determine and contribute towards socio-economic status of the regions and the country as whole. The image processing tools would be delivered by eminent resource persons of image processing experts.

SELECTION CRITERIA

This course is open to students, Research Scholars, Faculties and Industrial Employees. **Number of participants is limited to 50.**

RESOURCE PERSON

1. Dr. M N Faruk
Asst. Professor, Dept. of CSE
QIS, Ongole
2. Mr. Bande Nawaz
MD, Illuminate Skills
Hyderabad

IMPORTANT DATES:

Last date for Registration	: 19/01/2017
Participants for conformation via E-mail	: 21/01/2017

REGISTRATION FEES:

Research Scholar	: Rs. 300/-
Faculties	: Rs. 500/-
Industrial Employees	: Rs. 700/-

The Demand Draft drawn in favour of **"Principal, NIT Raichur"** payable at **NIT Campus Raichur.**

ACCOMODATION:

For the period of the workshop, the participants from the AICTE approved institutions will be provided accommodation in the student's hostel of Navodaya Institute of Technology, Raichur. Hostel Accommodation will be arranged based on their request.

How to reach Raichur:

Raichur is well connected by Road and Railway to all nearby and important cities like Bengaluru, Hyderabad, Chennai,



PRINCIPAL
Navodaya Institute of Technology (NIT)

Computer Science & Engineering
Navodaya Institute of Technology

NAVODAYA INSTITUTE OF TECHNOLOGY

BIJENGERA ROAD, RAICHUR - 584103

INVITATION

Department of Computer Science & Engineering

HODs & Faculties are invited to attend Five days

Faculty Development Program

on

"Advanced Digital Image Processing & its Applications"

By

Dr. M N Faruk
Asst. Professor, Dept. of CSE
QIS, Ongole
&
Mr. Bande Nawaz
MD, Illumninate Skills
Hyderabad

From *24th Jan To 28th Jan 2017*

Venue: Department of CSE

Head of Department
Computer Science & Engineering
Navodaya Institute of Technology
RAICHUR-584 103

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Inaugural function Agenda

1. *Introduction*
2. *Invocation song*
3. *Welcome address by Mr. Chetan G -(I/C) HOD/CSE*
5. *Introduction of Resource person*
6. *Chief Guest Address*
7. *Presidential address*
8. *FDP- Session - I*

Five day FDP on

Advanced Digital Image Processing & its Applications

Valedictory Function Agenda

1. *Introduction*
2. *Feedback - Faculty participants*
3. *Feedback by HOD*
5. *Feedback by Coordinator*
6. *Feedback by Resource person/Chief Guest*
7. *Certificate distribution*
8. *Presidential address*
9. *Vote of Thanks*

Head of Department 23-1-18
Computer Science & Engineering
Navodaya Institute of Technology
RAICHUR-584 103

23.1.18
PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

INSTITUTE OF TECHNOLOGY RAICHUR

NAVODAYA
Institute of Technology
RAICHUR

Faculty development programme on

ADVANCED DIGITAL IMAGE PROCESSING & ITS APPLICATIONS

Schedule on 24.01.2017 to 28.01.2017

Day	*Session-1 (09.30 am to 11.00am)*			*Session-2 (11.15 am to 12.45pm)*		*Session-2 (2.00 pm to 3.30pm)*		*Session-3 (3.45pm to 5.15pm)*
Tuesday (24.01.2017)	*Registration 09.00 to 10.00*	*Inauguration and Key note address by* **Mr. Bande Nawaz** MD, Illumninate Skills *Hyderabad*	*Tea Break (11.00am To 11.15 am)*	**Mr. Bande Nawaz** MD, Illumninate Skills *Hyderabad* (Image acquisition methods and representation standards.)	*Lunch break (12.45pm to 02.00pm)*	**Dr. M N Faruk** Asst. Professor, Dept. of CSE QIS, Ongole (Image processing with MATLAB.)	*Tea Break (03.30pm To 03.45 pm)*	**Dr. M N Faruk** Asst. Professor, Dept. of CSE QIS, Ongole (Image processing with MATLAB.)
Wednesday (25.01.2017)	**Dr. M N Faruk** Asst. Professor, Dept. of CSE QIS, Ongole (Human visual perception and color space)			**Mr. Bande Nawaz** MD, Illumninate Skills *Hyderabad* (Human visual perception and color space)		**Mr. Bande Nawaz** MD, Illumninate Skills *Hyderabad* (Image enhancement in the spatial domain (thresholding, contrast)		**Dr. M N Faruk** Asst. Professor, Dept. of CSE QIS, Ongole (Image enhancement in the spatial domain (stretching, and histogram operations))

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-534 103

23/1/17
Computer Science & Engineering

Day	Session-1 (09.30 am to 11.00am)		Session-2 (11.15 am to 12.45pm)		Session-2 (2.00 pm to 3.30pm)		Session-3 (3.45pm to 5.15pm)
Thursday (26.01.2017)	**Dr. M N Faruk** Asst. Professor, Dept. of CSE QIS, Ongole (Spatial filtering (smoothing, order-statistics filters))		**Dr. M N Faruk** Asst. Professor, Dept. of CSE QIS, Ongole (Spatial filtering (sharpening))		**Mr. Bande Nawaz** MD, Illumninate Skills *Hyderabad* (Dithering.)		**Mr. Bande Nawaz** MD, Illumninate Skills *Hyderabad* (Frequency domain image enhancement.)
Friday (27.01.2017)	**Mr. Bande Nawaz** MD, Illumninate Skills *Hyderabad* (Image filtering in frequency domain (low pass, high pass, notch filter, and Laplacian))	Tea Break (11.00am To 11.15 am)	**Dr. M N Faruk** Asst. Professor, Dept. of CSE QIS, Ongole (Image restoration (types of noise and denoising))	Lunch break (12.45pm to 02.00pm)	**Dr. M N Faruk** Asst. Professor, Dept. of CSE QIS, Ongole (Morphological image processing (color and gray scale).)	Tea Break (03.30pm To 03.45 pm)	**Mr. Bande Nawaz** MD, Illumninate Skills *Hyderabad* (Image segmentation (point and line detection, edge detection, the Hough transform, region growing, region splitting))
Saturday (28.01.2017)	**Dr. M N Faruk** Asst. Professor, Dept. of CSE QIS, Ongole (Geometric operations (affine transformations and interpolation))		**Dr. M N Faruk** Asst. Professor, Dept. of CSE QIS, Ongole (Color image processing)		**Mr. Bande Nawaz** MD, Illumninate Skills *Hyderabad* (Image compression (lossy vs. lossless, bit plane coding, JPEG))		**Mr. Bande Nawaz** MD, Illumninate Skills *Hyderabad* (Video coding (motion compensation, MPEG family coders))

PRINCIPAL
Rajiv Gandhi Institute of Technology (RIT)
RAICHUR-584 103

Computer Science & Engineering

Date: 18.01.2017

From
Mr. Chetan Gudi,
(I/C) Head,
Department of CSE/ISE
Navodaya Institute of Technology,
Raichur-584103.

To
The Principal,
Navodaya Institute of Technology,
Raichur-584103

Sir

Subject: Req. for Auditorium hall to conduct FDP - Reg

We have proposed to conduct the FDP on the topic "**Advanced Digital Image Processing & its Applications**" on 24.01.2017 to 28.01.2017. For this Purpose, we need auditorium on above mentioned dates from 10.00AM-11:00AM for Inauguration session. Hands-on session will be conducted in CSE Department Laboratories. Kindly permit us to use the resources.

Thanking You

Yours faithfully

18/01/17

[Mr. Chetan Gudi]
Computer Science & Engineering
Navodaya Institute of Technology
RAICHUR-584103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

From

Mr. Chetan Gudi,

(I/C) HoD, CSE.,

NIT, Raichur

18.01.2017

To

The Principal,

NIT, Raichur.

Sir,

Sub: Requisition for Accommodation and Food -Reg

I bring to your kind notice that we are conducting the Five days FDP on "Advanced Digital Image Processing & its Applications" in our department from 24th Jan 2017 to 28th Jan 2017. For that we want to arrange the accommodation and food for the Resource Person on the above said days. So kindly do the needful.

Resource Person Details,

1. Dr. M N Faruk
   Asst. Professor, Dept. of CSE
   QIS, Ongole

2. Mr. Bande Nawaz
   MD, Illumninate Skills
   Hyderabad

Thanking you,

HOD, CSE

Head of Department
Computer Science & Engineering
Navodaya Institute of Technology

PRINCIPAL I/C
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

NIT's
NAVODAYA
Institute of Technology
RAICHUR

Department of Computer Science & Engineering
Faculty Development Programme (FDP) on
"Advanced Digital Image Processing & its Applications"
From 24th Jan to 28th Jan 2017

Participants Details

S.No.	Name of the Faculty	Designation & Dept.
1.	Prof. Chetan Gudi	Asst. Professor & (I/C HOD)
2.	Prof. Hanumesh D	Asst. Professor, CSE
3.	Prof. Sivakumar Reddy	Asst. Professor, CSE
4.	Prof.Gayatri Anand	Asst. Professor, CSE
5.	Mr. Vijaykumar Yadav	Asst. Professor, CSE
6.	Prof.Shanthi E	Asst. Professor, CSE
7.	Prof. Madhushree N	Asst. Professor, CSE
8.	Prof.Rajashekhar Reddy	Asst. Professor, CSE
9.	Prof. Usha	Asst. Professor, CSE
10.	Prof. Jayashree	Asst. Professor, CSE
11.	Prof. Chennaveeramma	Asst. Professor, ECE
12.	Prof. Madhuri Devi	Asst. Professor, ECE
13.	Prof. Daniel N	Asst. Professor, EEE
14.	Prof. Rammurthy	Asst. Professor, ECE
15.	Prof. Majumdar	Asst. Professor, EEE
16.	Prof. Sachin	Asst. Professor, ECE

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY * RAICHUR

28/01/17
HOD & Convener
Head of Department
Computer Science & Engineering
Navodaya Institute of Technology
RAICHUR-584 103

Report on Three days FDP

A Three days Faculty Development Program on Advanced Digital Image Processing & its Applications was arranged in Department of Computer Science & Engineering from Jan 24th to Jan 28th, 2017. The resource persons included Dr. M. N. Faruk, Asst. Professor, QIS College, Ongole and Mr. Bande Nawaz, MD, Illuminate Skills, Hyderabad. The FDP was planned to provide in depth information about image processing technologies and their applications.

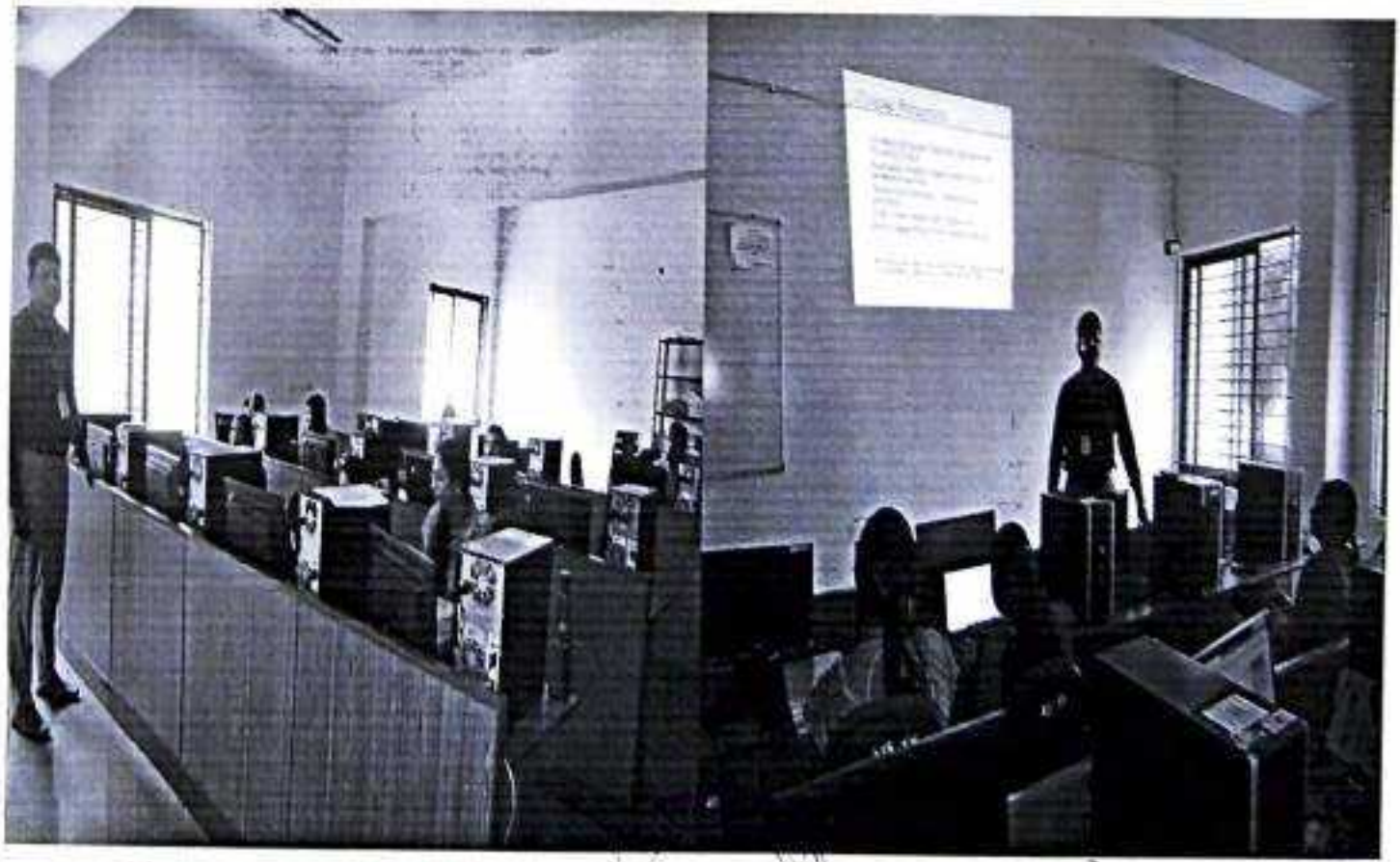
The first day session began with the discussion on basic usage of Image processing Toolbox, Geometric Operations on Images and other operations like Neighborhood & Block operations, Image Filtering techniques. The Afternoon session was concluded with the topic on Image transformations.

The second and third day's sessions included the topics of Image Analysis & Enhancement, Binary Image Operations and Region based processing. A Real time application to include Motion Image Processing was also discussed.

The fourth and fifth days' session, resource person gave brief explanation about the research thrust areas of Medical Image Processing, Wavelet transform applications with respect to image on Biomedical signal processing.

Totally 17 faculty members from the Department of Computer Science Engineering & Electronics and Communication Engineering department attended the program. The participants were given with course materials.

The department would like to thank the NIT-R Management, Dean/Principal, Navodaya Institute of Technology for encouraging and supporting us in organizing this FDP Program.



PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

28/01/17
Computer Science & Engineering

PROFILE



1. NAME: **Dr. M. N. FARUK**
2. DESIGNATION: **PROFESSOR & HEAD**
3. QUALIFICATION: **M.E., Ph.D**
4. EXPERIENCE
   a. TEACHING: 11 YEARS.
   b. INDUSTRY: 4 MONTHS.
   c. RESEARCH: __YEARS.

   TOTAL: **11.4** YEARS.
5. RESEARCH WORKS:
   (Status of the work who registered for Ph.D.)

   TOPIC:
6. ACHIEVEMENTS:
   - Received zonal level Best Faculty Award twice from Anna University, Chennai
7. (AWARDS/REWARDS)
8. RESEARCH PAPER PUBLICATIONS:

INTERNATIONAL JOURNALS

S. No.	Authors	Title of the Research paper	Name of the Journal	Volume No.	Issue No.	Page Nos.	Year of publication	Impact factor
1	Sreedhar, K.C, Faruk, M.N., Venkateswarlu, B	A Genetic TDS and BUG with Pseudo Identifier for Privacy Preservation over Incremental Datasets	Journal of Intelligent & Fuzzy Systems - IOS Press, NL	32	4	2863-2873	2017	SCI Index - 1.267
2	M N Faruk, G Lakshmi Vara Prasad, Govind Divya	A Genetic PSO Algorithm with QoS-Aware Cluster Cloud Service Composition	Advances in Signal Processing and Intelligent Recognition Systems	425	6	395-405	2015	SCI Index - 0.49

PRINCIPAL
Rajiv Gandhi Institute of Technology (RIT)
RAICHUR-584 103

Head of Department
Computer Science & Engineering
Rajiv Gandhi Institute of Technology
RAICHUR-584 103

			Springer, Cham					
3	MN Faruk, D Sivakumar	A Novel Approach for Resource Discovery using Random Projection on Cloud Computing Environments	International Journal of Engineering and Technology (Scopus)	5	2	1812-1816	2013	SJR 0.18
4	MN Faruk, D Sivakumar	Towards Self Configured Multi-Agent Resource Allocation Framework for Cloud Computing Environments	International Journal of Engineering and Technology (Scopus)	6	2	920-928	2014	SJR 0.18
5	MN Faruk, D Sivakumar	Towards Resource Discovery Framework Using Honey Bee Swarm Model for Multi Provider Cloud Environments	International Journal of Advanced Information Science and Technology	3	9	33-44	2014	SJIF 6.85
6	MN Faruk, GLV Prasad, C Nalini	AN ARTIFICIAL BEE COLONY (ABC) MODEL FOR CLOUD SERVICE DISCOVERY AND COMPOSITION	International Journal of Pure and Applied Mathematics (Scopus)	117	7	113-124	2017	SJR 7.19
7	BASR Devi, NSK Chakravarthy, MN Faruk	ANALYSIS OF MANET ROUTING PROTOCOL IN PRESENCE OF WORM-HOLE ATTACK USING ANOVA TOOL	International Journal of Pure and Applied Mathematics (Scopus)	117	15	1043-1054	2017	SJR 7.19

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Head of Department
Computer Science & Engineering
Navodaya Institute of Technology
RAICHUR-584 103

NATIONAL JOURNALS

S. No.	Authors	Title of the Research paper	Name of the Journal	Volume No.	Issue No.	Page Nos.	Year of publication
1	M N Faruk	Artificial Neural Networks in Wireless Intrusion Detection System (ANNWIDS)	International Journal of Advanced Research in Computer Science	3	1	425-430	2012
2	Thota Vishnu Priya M.N Faruk	System for Security in Cloud Computing Environment	Asian Journal of Current Engineering and Maths	6	4	5491-5497	2017

INTERNATIONAL CONFERENCES

S. No.	Authors	Title of the Research paper	Name of the Proceedings	Page Nos.	Dates of the conference.	Organized by
01	M N Faruk, Koyi Lakshmi Prasad and P Srinivasulu	Cloud based Dual Auction (DA) and A* and IDA* Searching models using BH - Strategy for resource allocation in e-markets	IEEE International Conference on Advances in Computing, Communications and Informatics (ICACCI)	446 - 452	10/08/2015-13/08/2015	SCMS School of Engineering and Technology
02	M N Faruk, M Jahnavi	An Optimal Healthcare Self-Diagnosis System using Cloud Framework	IEEE International Conference on Big Data Analytics and Computational Intelligence (ICBDAC)	339-344	23/03/2017 – 25/03/2017	Chirala Engineering College, Chirala
03	M N Faruk	Artificial Neural Network techniques	International conference on recent trends in engineering,	221-229	24/02/2012 – 25/02/2012	Pallavan college of Engineering,

PRINCIPAL
Rajiv Gandhi University of Knowledge Technologies (RGUKT)
Nuzvidu-521 202

Head of Department
Computer Science & Engineering
Rajiv Gandhi University of Knowledge Technologies
Nuzvidu-521 202

		in Intrusion Detection System	Management & Computer Application (INRTIEMC – 2012)			Kancheepuram, Chennai
04	M N Faruk	New Attribute Discovery and Wrapper adaptation for web Information Extraction	4G Wireless Computer Networks 2011(IC4GWCN-11)	124-130	04/03/2011-05/03/2011	Arunai College Engineering, Tamil Nadu

NATIONAL CONFERENCES

S. No.	Authors	Title of the Research paper	Name of the Proceedings	Page Nos.	Dates of the conference.	Organized by
01	M N Faruk	The Dynamic partitioning of Android application models using Cloud computing	Distributed computing and Networks (NCDCN'12)	140-145	14/09/2012	Dhanalakshmi College of Engineering, Chennai.
02	M N Faruk	Dynamic Identification of Resource Monitoring and Predictions of Effective data communication in Cloud Environments	Communication and Advanced Research in Electrical, Electronics & Robotics" (CAREER'13)	221-226	01/03/2013 - 02/03/2013	SNS College of Engineering, Chennai.
03	M N Faruk	Roaming agents for Distributed Intrusion Detection System (RADIDS)	National Conference on Impact of Computing Technologies in emerging scientific fields 2012 ("NCICTE'12)	89-95	23/01/2012 - 25/01/2012	SRM University, Chennai.
04	M N Faruk	New Attribute Discovery and Wrapper adaptation	DRDO - Data Mining, Spatial & Geo Spatial Database Application	325-332	02/12/2010 - 03/12/2010	Kongu Engineering College,Erodu

PRINCIPAL
Navodaya Institute of Technology (INT)
RAICHUR-584 103

Head of Department
Computer Science & Engineering
Navodaya Institute of Technology
RAICHUR-584 103

23/01/17

9. CONFERENCE / WORKSHOPS ATTENDED:

NAME OF THE CONFERENCE/ WORKSHOP	TITLE OF THE CONFERENCE/ WORKSHOP	OTHER DETAILS (Organisation/Duration/Date)
WORKSHOP	Android Application Development	Nov 3-7, QIS College of Engineering and Tech. Ongole
WORKSHOP	IEEE SmartTech Workshop-2015	Sep 25-26,2015 at Hotel Lali Ashok. Organized by IEEE Bangalore Section & IEEE Region 10 (APAC).
WORKSHOP	Web Technologies & Software Quality	VIT University, Vellore. 4th, April 2010
WORKSHOP	**Net Framework and Windows Mobile Application Development**	Arunai Engineering College, 26th, March 2010.
WORKSHOP	Web Services and tools	Arunai Engineering College, 30th, September 2010. Tamil Nadu
WORKSHOP	**Mission10X – Advanced Teaching methods**	Arunai College of Engineering. 17 – 21, January 2012 Tamil Nadu
WORKSHOP	Education Technology	ISTE Faculty Chapter. Arunai Engineering College 3rd, October 2011 Tamil Nadu
WORKSHOP	**Research issues and challenges in Mobile Adhoc Networks**	Arunai College of Engineering, 21 & 22, September 2012 Tamil Nadu
WORKSHOP	Instructional Design and Delivery Systems	Arunai College of Engineering 27th – 29th June, 2013. Tamil Nadu

10. MEMBERSHIP IN PROFESSIONAL BODY:

1. Life Time Member of Indian Society of Technical Education (ISTE) (Membership No: LM82305)

2. Professional Member of IEEE Organisation (Membership No : 90729648)

3. Life Time Member of IAENG, Member Number: 156277

11. ANY OTHER INFORMATION:

CERTIFICATION

1. Certified in "Certified Software Testing Professional (CSTP)" in STC Technologies, Chennai. Certificate No: CHN11079.

2. Certified in "ISTQB – Certified Tester Foundation level", Certificate No: ITB-CTFL-7048003

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Head of Department
Computer Science & Engineering
Navodaya Institute of Technology
RAICHUR-584 103

12. CONTACT DETAILS:

ADDRESS:

No: 72 Mettu Street

Thandarampet T.K

Tiruvannamalai. 606 707

Ph: 04188-246819

MOBILE NO: +91 9943220111

EMAIL ID: faruk.mn@navodaya.edu.in,

22/1/17

Head of Department
Computer Science & Engineering
Rajiv Gandhi Institute of Technology (RGIT)
RAICHUR-584 103

23.1.17

Rajiv Gandhi Institute of Technology (RGIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

NET's

NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

~~GUEST LECTURE/SEMINR/ADD-ON COURSE/~~ *Faculty Development Programme* ~~WORKSHOP-/ CONFERENCE/SHORT TERM TRAINING PROGRAMME/ANY OTHER~~ *PROPOSAL*

Date: 12.03.2020

1	**ACADEMIC YEAR**	2019-20
2	**TYPE OF THE PROGRAMME PROPOSED**	*Faculty Development Programme*
3	**TITLE OF THE PROGRAMME:**	**"Data Analytics with R Programming"**
4	**BROAD OBJECTIVE OF THE PROGRAMME:**	Data Analysis has become an essential part of everyday life and merged as a critical component of modern business intelligence in recent years. Data Scientists have a number of option to analyze data using statistical methods. One of the most convenient and powerful methods is to use the R Programming language. This course will help you to learn how to tackle data analysis problems using the powerful open source language R. Participants will able to understand how data is created,stored,accessed and how the organization works with data and creates the environment in which analytics can flourish. What you will learn in this course will give you a strong foundation in all the areas that support analytics. Participants will also learn how to compute various statistical measures,create meaningful data visualizations,R models to predict expected future outcomes.
5	**NAME OF THE INSTITUTION**	NETs NAVODAYA INSTITUTE OF TECHNOLOGY
6	**NAME OF THE DEPARTMENT(S)**	COMPUTER SCIENCE & ENGINEERING
7	**BENEFICIARY**	Faculty Members
8	**PROPOSED DATE OF THE PROGRAMME**	15.04.2020 to 19.04.2020
9	**ELIGIBILITY CRITERIA SET:**	Faculty Members of CSE, ECE, EEE
10	**NUMBER OF DAYS REQUIRED TO ORGANISE THE PROGRAMME**	Five Days
11	**PROPOSED DURATION OF THE PROGRAMME**	30 Hours

12	**TOTAL NO. OF PERIODS REQUIRED FOR THE PROGRAMME**	30 Periods
13	**NO. OF PERIODS REQUIRED PER DAY**	06 Periods
14	**LOCATION DETAILS:**	Dept. of CSE, NIT, Raichur
15	**MINIMUM NO OF PARTICIPANTS:**	10
16	**DETAILS OF SPONSORERS:**	---
17	**DETAILS OF RESOURCE PERSONS:**	**1. Mr. Mukund Harwalkar,** Asst. Professor, Department of CSE, SLNCE Raichur **2. Shanthi E,** Asst. Professor, Department of CSE, NIT, Raichur.
18	**REGISTRATION FEE WITH CERTIFICATION:**	---
19	**REGISTRATION FEE WITHOUT CERTIFICATION:**	---
20	**FACULTY COORDINATOR:**	1. Mr. Sivakumar Reddy 2. Chetan Gudi
21	**EVENT ORGANISERS (STUDENTS):**	---
22	**PROGRAMME PROPOSED BY:**	Dept. of CSE
23	**ANY OTHER RELEVANT MATTER:**	Nil
24	**ESTIMATED BUDGET IN RS:**	Rs. 34,000/-

12/2/20

PRINCIPAL

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

HOD

Head of Department
Computer Science & Engineering
Navodaya Institute of Technology
RAICHUR-584 103

12/02/20

EVENT COORDINATOR

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

NAVODAYA INSTITUTE OF TECHNOLOGY

Department of Computer Science & Engineering

Five days' FDP on
"Data Analytics with R Programming"
on
15th to 19th April 2020

REGISTRATION FORM

Name : _____

Branch : _____

Designation : _____

College Name : _____
& Address

_____

Phone/ Mobile: _____

E-mail : _____

Registration Fee Details

DD No. : _____ Date: _____

Bank : _____

Accommodation: YES ☐ NO ☐

DECLARATION: This is to certify that the above member is bonafide Staff of this institution/ Organization.

Signature of the Participant

Sign. Of HOD/Principal/ Head of Organization (with seal)

_____

DD to be drawn in favor of "PRINCIPAL, NIT Raichur" payable at SBI, NET Campus, Raichur.

Chief Patron:
Sri S. R. Reddy,
Hon'ble Chairman, NET® Raichur

Chief Convener:
Dr. M V Mallikarjuna
Principal, N.I.T Raichur

Convener
Dr. M. N. Faruk
HOD, CSE, NIT. Raichur

Coordinator:
Prof. Sivakumar Reddy, CSE, NIT
E-Mail: bskreddy64@gmail.com
Contact no. : +91 – 9686281370

Co-Coordinator
Prof. Chetan Gudi, CSE, NIT
E-Mail: chetangudi@navodaya.edu.in
Contact no. : +91 - 9663426789

Organizing Committee:
1. Prof. Vijay Kumar Yadav
2. Prof. Hanumesh D
3. Prof. Syeda Minhaz Fathima
4. Prof. Supriya Purohit
5. Prof. Shaziya Anjum
6. Prof. Vishakha P
7. Prof. Jayashree

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Five days' FDP
On
"Data Analytics with R Programming"
On
15th to 19th April 2020

NAVODAYA INSTITUTE OF TECHNOLOGY



Organized by
Department of Computer Science & Engineering
Navodaya Nagar, Raichur-584 103, Karnataka.
Tel:08532-240115,240115
Mobile No: +91-9686281370
: +91-9663426789
Fax : 08532-240054

e-mail :- hodcse.nit@navodaya.edu.in
web :- ww.navodaya.edu.in/engineering

Computer Science & Engineering
Navodaya Institute of Technology
RAICHUR-584 103

ABOUT THE INSTITUTE:

Navodaya Institute of Technology (NIT), Raichur, established in 2008, is affiliated to Visvesvaraya Technological University (VTU), Belagavi and is approved by All India Council for Technical Education (AICTE), New Delhi. The guiding spirit and the driving force for all the progress and achievements of the institution is our most respected Chairman Sri. **S. R. Reddy** of Navodaya Education Trust®, Raichur. NIT focuses on promoting quality technical education in North Karnataka region in general and Hyderabad Karnataka Region in particular. The education is of paramount importance for socio-economic development. The Institute focuses on integral scientific &Technical training with value based education to enable overall development of aspirants.

NIT-R is all-inclusive campus. Administrative Block, Library Block, Separate and Independent Academic Blocks for each Department, Power back up, Hostels for Boys and Girls, Canteen, Bank, ATM, Campus Temple, Gym and Playground all housed in a vast campus of about 50 acres and surrounded by hillocks and lake.

ABOUT THE DEPARTMENT:

The department of Computer Science & Engineering is established in the year 2008. The department offers BE program in Computer Science & engineering with annual intake of 120. The department has excellent infrastructure with state of the art classrooms and two exclusive Computer Centers equipped with 90 client's systems. At present it has separate computers for the clients & the NIT server including number of Computer Science & engineering related software packages. The department is enriched with highly qualified, experienced and committed faculty members and faculty are from reputed institutes. The department gives importance on both academic excellence and ethical values and personality development of students by practicing a time tabanting system of regular tests. The department strives hard to impart quality education and it aims to be students in the vanguard edge of the knowledge technology and meeting societal, industrial needs. The department is working for enrichment of the students through CN/CBCS syllabus structure.

VISION:

"To be acknowledged as a center of excellence in Computer Science & Engineering through teaching, research and educational programs in the context of societal needs globally and foster a culture dedicated to learning & discovery and be known as a world-class center of innovation"

MISSION:

"The mission of the department is to persistently strive for achieving excellence in computing discipline. It is being pursued through its spectrum of academic programmes in computing of contemporary standards. The sustained effort is to produce computing graduates with potential to design and develop systems involving the integration of software and hardware issues, innovative approaches to programming and problem solving as well as creative ways to use Computers. Large scale software systems and computing infrastructure of an organization."

ABOUT THE WORKSHOP

Data Analytics has become an essential part of everyday life and, hence, is a critical component of modern business intelligence in recent years. Data Scientists have a variety of tools to analyze data using statistical methods. One of the most convenient and powerful methods is to use the R programming language. The course will help you to learn how to write data analysis programs using the powerful open source language R. Participants will learn to understand how data is stored and retrieved and how to do preliminary analysis. They will also learn how to visualize data and create the programming skills that will help you to do your own data analysis. After the course you will have a strong foundation in all the tools that support analytics. Participants will also learn how to compute various statistical measures, manipulate data, and generate insightful visualizations.

SELECTION CRITERIA

This course is open to students, Research Scholars, Faculties and Industrial Employees. **Number of participants is limited to 50.**

RESOURCE PERSON:

1. Prof. Mukund Harwalkar
Asst. Professor, Dept. of CSE,
SLNCT, Raichur

2. Prof. Shanthi P.,
Asst. Professor, Department of CSE
NIT, Raichur.

IMPORTANT DATES:

Last date for Registration: 11/06/2018
Participants Confirmation via Email: 13/06/2018

REGISTRATION FEES:

Research Scholar: Rs. 300/-
Faculties: Rs. 500/-
Industrial Employees: Rs. 700/-

The Demand Draft Drawn in favour of "**Principal, NIT Raichur**" payable at "**NIT Campus Raichur**"

ACCOMMODATION:

For the period of the workshop, the participants from the AICTE approved institutions will be provided accommodation in the students' mess of Navodaya Institute of Technology, Raichur. Hotel accommodation will be arranged based on their request.

How to reach Raichur

Raichur is well connected to Road and Railways to all metro and important cities like Bangalore, Hyderabad, Chennai



Tel : +91 8532-200115, 240334, Fax : +91-8532-240034
E-Mail :hodcse.nit@navodaya.edu.in

CIRCULAR

CIR : 2019-20/FDP/01

02.04.2020

Chief Patron
Shri. S. R. Reddy, Hon'ble Chairman,
Navodaya Education Trust (NET®),
Raichur.

Chief Convener
Dr. M V Mallikarjuna,
Principal, NIT, Raichur

Convener
Dr. M. N. Faruk
HOD, CSE, NIT. Raichur

Coordinator
Prof. Sivakumar Reddy, CSE, NIT
E-Mail: bskreddy64@gmail.com
Contact no. : +91 - 9686281370

Co-Coordinator
Prof. Chetan Gudi, CSE, NIT
E-Mail: chetangudi@navodaya.edu.in
Contact no. : +91 - 9663426789

Organizing Committee
1. Prof.Vijay Kumar
2. Prof. Hanumesh D
3. Prof. Syeda Minhaz Fathima
4. Prof. Supriya Purohit
5. Prof. Shaziya Anjum
6. Prof. Vishakha P
7. Prof. Jayashree

Respected Sir / Madam,

Greetings from Navodaya Institute of Technology, Raichur…

The department of Computer Science & Engineering is organizing a Five day FDP on **"Data Analytics with R Programming"** The attached brochure may kindly be circulated among the faculty members / colleagues.

Data Analysis has become an essential part of everyday life and merged as a critical component of modern business intelligence in recent years. Data Scientists have a number of option to analyze data using statistical methods. One of the most convenient and powerful methods is to use the R Programming language. This course will help you to learn how to tackle data analysis problems using the powerful open source language R. Participants will able to understand how data is created,stored,accessed and how the organization works with data and creates the environment in which analytics can flourish. What you will learn in this course will give you a strong foundation in all the areas that support analytics. Participants will also learn how to compute various statistical measures,create meaningful data visualizations,R models to predict expected future outcomes.

We are pleased to inform that the department of Computer Science & Engineering, Navodaya Institute of Technology is organizing a Faculty Development Programme during 15th April - 19th April 2020.

In this context, we look forward your support and request you to encourage participation of professionals by circulating the attached brochure.

Thanking you,
Yours sincerely,

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Dr. M. N. Faruk
Convener

Head of Department
Computer Science & Engineering

NET's

NAVODAYA INSTITUTE OF TECHNOLOGY

BIJENGERA ROAD, RAICHUR - 584103

INVITATION

Department of Computer Science & Engineering

HODs & Faculties are invited to attend Five days

Faculty Development Program

on

"Data Analytics With R Programming"

By

Prof. Mr. Mukund Harwalkar
Asst. Professor, Dept. of CSE,
SLNCE, Raichur
&
Prof. Shanthi E,
Asst. Professor, Department of CSE,
NIT, Raichur.

From *15th Apr To 19th Apr 2020*

Venue: Department of CSE

Computer Science & Engineering

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Inaugural function Agenda

1. Introduction
2. Invocation song
3. Welcome address by Dr. M. N. Faruk - HOD/CSE
5. Introduction of Resource person
6. Chief Guest Address
7. Presidential address
8. FDP- Session - I

Five day FDP on

Python Programming & Web Design

1. Introduction
2. Feedback - Faculty participants
3. Feedback by HOD
5. Feedback by Coordinator
6. Feedback by Resource person/Chief Guest
7. Certificate distribution
8. Presidential address
9. Vote of Thanks

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Head of Department
Computer Science & Engineering
Navodaya Institute of Technology
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

NAVODAYA INSTITUTE OF TECHNOLOGY

Faculty development programme on

- DATA ANALYTICS WITH R PROGRAMMING

Schedule on 15.04.2020 to 19.04.2020

Day	*Session-1 (09.30 am to 11.00am)*		*Tea Break (11.00am To 11.15 am)*	*Session-2 (11.15 am to 12.45pm)*	*Lunch break (12.45pm to 02.00pm)*	*Session-2 (2.00 pm to 3.30pm)*	*Tea Break (03.30pm To 03.45 pm)*	*Session-3 (3.45pm to 5.15pm)*
Wednesday (15.04.2020)	*Registration 09.00 to 10.00*	*Inauguration and Key note address by* **Prof. Mukund H** Asst. Professor, Department. of CSE, *SLNCE, Raichur*		**Prof. Mukund H** Asst. Professor, Department. of CSE, *SLNCE, Raichur* (General Introduction into the R Ecosystem)		**Prof. Shanthi E,** Asst. Professor, Department of CSE, NIT, Raichur. (Brief Overview on R Coding Tools(Rstudio, git, GitHub)		**Prof. Shanthi E,** Asst. Professor, Department of CSE, NIT, Raichur. (R Syntax Basics- variables, constants, vectors, etc)
Thursday (16.04.2020)	**Prof. Mukund H** Asst. Professor, Department. of CSE, *SLNCE, Raichur* (Modules and Packages)			**Prof. Mukund H** Asst. Professor, Department. of CSE, *SLNCE, Raichur* (Modules and Packages)		**Prof. Shanthi E,** Asst. Professor, Department of CSE, *NIT, Raichur.* (Exception Handling)		**Prof. Shanthi E,** Asst. Professor, Department of CSE, NIT, Raichur. (Exception Handling)

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Head of Department
Computer Science & Engineering
Navodaya Institute of Technology
RAICHUR-584 103

Day	Session-1 (09.30 am to 11.00am)		Session-2 (11.15 am to 12.45pm)		Session-2 (2.00 pm to 3.30pm)		Session-3 (3.45pm to 5.15pm)
Friday (17.04.2020)	**Prof. Shanthi E,** Asst. Professor, Department of CSE, NIT, Raichur. (Applying PCA on an image for outlier-detection)	Tea Break (11.00am To 11.15 am)	**Prof. Shanthi E,** Asst. Professor, Department of CSE, NIT, Raichur. (A Systematic Introduction into Data Types)	Lunch break (12.45pm to 02.00pm)	**Prof. Mukund H** Asst. Professor, Department. of CSE, *SLNCE, Raichur* (Levels of measurement , Vector types , data.frame objects, Characteristics of tidy data)	Tea Break (03.30pm To 03.45 pm)	**Prof. Mukund H** Asst. Professor, Department. of CSE, *SLNCE, Raichur* (Basic Data Transformations)
Saturday (18.04.2020)	**Prof. Shanthi E,** Asst. Professor, Department of CSE, *NIT, Raichur.* (Introduction to data.table for More Complex Data Transformations)		**Prof. Mukund H** Asst. Professor, Department. of CSE, *SLNCE, Raichur* (Filtering and ordering data)		**Prof. Mukund H** Asst. Professor, Department. of CSE, *SLNCE, Raichur* (Summaries and aggregates)		**Prof. Shanthi E,** Asst. Professor, Department of CSE, *NIT, Raichur.* (New variables , Relational data)
Sunday (19.04.2020)	**Prof. Mukund H** Asst. Professor, Department. of CSE, *SLNCE, Raichur* (Joins on Keys)		**Prof. Mukund H** Asst. Professor, Department. of CSE, *SLNCE, Raichur* (Introduction into fuzzy joins)		**Prof. Shanthi E,** Asst. Professor, Department of CSE, *NIT, Raichur.* (Transforming wide and long tables)		**Prof. Shanthi E,** Asst. Professor, Department of CSE, *NIT, Raichur.* (Dirty Data Problems:)

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Head of the Dept.
Computer Science & Engineering
Navodaya Institute of Technology
RAICHUR-584 103

Report on "Data Analytics with R Programming"

Department of Computer Science & Engineering has organized one week Faculty Development Program (FDP) on "Data Analytics with R-Programming" from 15th April to 19th April 2020. Mr. Mukund Harwalkar in his speech addressed all the audience by his precious words. He started with the evolution of computer hardware and software. He continued to revise the change in technology and its importance. Later he discussed the needs of latest programming languages such as Python, R-programming etc. Mr. Chetan Gudi who started the FDP with the basics of R-language. The day 02 of FDP started with the introduction of data frames. The session included creation of data frames, accessing those, expanding that is adding columns and rows to data frames etc. After this the connectivity of R programming with Databases was covered in which participants used My-SQL as a database to connect with front-end. Later session was continued with data analysis in which drawing graphs and charts for the existing data was taught. Mr. Mukund discussed with us different types of Classification techniques and explained the concepts of single-label and multi-label learning. The different approaches of Multi-label learning were discussed in detail along with live examples. The further sessions were continued with the topics like Basic Data Analytics, Data Pre-processing and exploratory data analysis. Day-04 started with the advanced topics like Statistical- Regression, Time series analysis, KNN-learning and Association Rule Mining. For each and every advanced topic the suitable examples and applications were taken. The valedictory ceremony for the 5 days FDP on "Data Analytics with R Programming" was concluded at 05:15pm.

PRINCIPAL
Navodaya Institute of Technology (IIIT)
RAICHUR-584 103

HOD CSE 19/9/20
Head of Department
Computer Science & Engineering
Navodaya Institute of Technology
RAICHUR-584 103

INSTITUTE OF TECHNOLOGY RAICHUR NAVODAYA

Date: 11.04.2020

From
Dr. M. N. Faruk,
Head,
Department of CSE/ISE
Navodaya Institute of Technology,
Raichur-584103.

To
The Principal,
Navodaya Institute of Technology,
Raichur-584103

Sir

Subject: Req. for Auditorium hall to conduct FDP - Reg

We have proposed to conduct the FDP on the topic "**Data Analytics with R Programming**" from 15.04.2020 to 19.04.2020. For this Purpose, we need auditorium on 15.04.2020 for inauguration session from 10.00AM-12:00PM. Kindly permit us to use the auditorium.

Thanking You

Yours faithfully

[Dr. M. N. Faruk]

Head of Department
Computer Science & Engineering
Navodaya Institute of Technology
RAICHUR-584 103

Principal
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY * RAICHUR

11.04.2020

From

Dr. M. N. Faruk,
HOD, CSE.,
NIT, Raichur

To

The Principal,
NIT, Raichur.

Sir,

Sub: Requisition for Accommodation and Food -Reg

I bring to your kind notice that we are conducting the Five days FDP on "Data Analytics with R Programming" in our department from 15th April 2020 to 19th April 2017. For that we want to arrange the accommodation and food for the Resource Person on the above said days. So kindly do the needful.

Resource Person Details,

1. **Mr. Mukund Harwalkar**
   Asst. Professor, Dept of CSE
   SLNCE, Raichur

Thanking you,

HOD, CSE
Head of Department
Computer Science & Engineering
Navodaya Institute of Technology
RAICHUR-584 103

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

NAVODAYA INSTITUTE OF TECHNOLOGY

Department of Computer Science & Engineering

Faculty Development Programme (FDP) on

"Data Analytics with R Programming"

From 15th to 19th April 2020

Participants Details

S.No.	Name of the Faculty	Designation & Dept.
1.	Prof. M. N. Faruk	Professor & Head, CSE
2.	Prof. Chetan Gudi	Asst. Professor, CSE
3.	Prof. Sivakumar Reddy	Asst. Professor, CSE
4.	Prof. Hanumesh D	Asst. Professor, CSE
5.	Prof.Vijay Kumar	Asst. Professor, CSE
6.	Prof.Shanthi E	Asst. Professor, CSE
7.	Prof. Jayashree	Asst. Professor, CSE
8.	Prof. Majumdar	Asst. Professor, EEE
9.	Prof. Daniel N	Asst. Professor, EEE
10.	Prof. Geetha	Asst. Professor, EEE
11.	Prof. Rammurthy	Asst. Professor, ECE
12.	Prof. Sachin	Asst. Professor, ECE
13.	Prof. Chennaveeramma	Asst. Professor, ECE

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

HOD & Convener
Dept. of Computer Science & Engineering
Navodaya Institute of Technology
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY

RAICHUR- 584103, KARNATAKA

DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

CERTIFICATE

This is to certify that Mr./Ms./Dr. Chetan Gudi of CSE has attended Five day FDP on *"Data Analytics with R Programming"* conducted at Navodaya Institute of Technology, Raichur on 15-04-2020 to 19-04-2020.

Coordinator

HOD

Principal

NAVODAYA INSTITUTE OF TECHNOLOGY

RAICHUR- 584103, KARNATAKA

DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

CERTIFICATE

This is to certify that Mr./Ms./Dr. Sivakumar Reddy of CSE has attended Five day FDP on *"Data Analytics with R Programming"* conducted at Navodaya Institute of Technology, Raichur on 15-04-2020 to 19-04-2020.

Coordinator 19/04/20

HOD

Principal 19/4/20

NAVODAYA INSTITUTE OF TECHNOLOGY

RAICHUR- 584103, KARNATAKA

DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

CERTIFICATE

This is to certify that Mr./Ms./Dr. Shanthi E of CSE has attended Five day FDP on *"Data Analytics with R Programming"* conducted at Navodaya Institute of Technology, Raichur on 15-04-2020 to 19-04-2020.

Coordinator

HOD

Principal

NAVODAYA INSTITUTE OF TECHNOLOGY

RAICHUR- 584103, KARNATAKA

DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

CERTIFICATE

This is to certify that Mr./Ms./Dr. Vijaykumar Yadav of CSE has attended Five day FDP on *"Data Analytics with R Programming"* conducted at Navodaya Institute of Technology, Raichur on 15-04-2020 to 19-04-2020.

Coordinator 19/04/20

HOD

Principal 19/4/20

MUKUND V HARWALKAR
Department of Information Science & Engineering
PDA College of Engineering
mukundharwal@gmail.com
Mobile-+91 9739515417

- Completed BE (Computer Science & Engineering) from KBN College of Engineering, Gulbarga.
- Completed Mtech (Computer science & Engineering) from VTU Regional Centre, Kalaburagi.
- Presently working as assistant professor in the Department of information science & Engineering, PDA College of Engineering, Kalaburagi since 05 years.

Achievements-

- Participated along with students "Chhatra Vishwakarma Award 2018" jointly organised by AICTE - ISTE & ECI at Acharya Institute of Technology, Soladevanahalli, Hesaraghatta Road, Bangalore on 31st October 2018 .
- Faculty Coordinator for National Level Competition for Engineering Students Association with ECell Fest (IIT Kharagpur Event) on Topic Android held at PDA College of Engineering, Kalaburagi on 10th &11th November 2018.
- Organizes and attended several workshops/ Faculty development program.
- Coordinated industry institute interaction programs.

Roles & Responsibilities in the Department

- Pheonix Association Student Coordinator for the year 2017-18 and 2018-19 and conducted several co curricular and extracurricular activities in the department.
- Gate Coordinator and organized GATE Awareness Camp in the Department.
- • IEI Student Chapter Coordinator.

Member of Professional Bodies like IEI & ISTE.

PRINCIPAL
Navodaya Institute of Technology (III)
RAICHUR-584 103

NET's

NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

~~GUEST LECTURE/SEMINR/ADD-ON COURSE~~/ *Faculty Development Programme* ~~WORKSHOP~~ / ~~CONFERENCE/SHORT TERM TRAINING PROGRAMME/ANY OTHER PROPOSAL~~

Date: 24.12.2017

1	**ACADEMIC YEAR**	2017-18
2	**TYPE OF THE PROGRAMME PROPOSED**	*Faculty Development Programme*
3	**TITLE OF THE PROGRAMME:**	**"Python Programming and Web Design"**
4	**BROAD OBJECTIVE OF THE PROGRAMME:**	The language of Python is extremely powerful and very advanced for web design and development. Developers with this skill are in great demand, but it is difficult to find a high-quality web development company that uses Python for web development. There are many web development and design languages a company can use, but Python is the best programming language out there because it is so advanced and complex. Because it is such a difficult language to use, developers who are skilled at creating a website with Python tend to want higher salaries. This means that most web design companies just can't have full-time programmers. Generally, only large web design companies can afford having Python web programmers on staff full-time.
5	**NAME OF THE INSTITUTION**	NETs NAVODAYA INSTITUTE OF TECHNOLOGY
6	**NAME OF THE DEPARTMENT(S)**	COMPUTER SCIENCE & ENGINEERING
7	**BENEFICIARY**	Faculty Members
8	**PROPOSED DATE OF THE PROGRAMME**	24.01.2018 to 28.01.2018
9	**ELIGIBILITY CRITERIA SET:**	Faculty Members of CSE, ECE, EEE
10	**NUMBER OF DAYS REQUIRED TO ORGANISE THE PROGRAMME**	Five Days
11	**PROPOSED DURATION OF THE PROGRAMME**	30 Hours

12	**TOTAL NO. OF PERIODS REQUIRED FOR THE PROGRAMME**	30 Periods
13	**NO. OF PERIODS REQUIRED PER DAY**	06 Periods
14	**LOCATION DETAILS:**	Dept. of CSE, NIT, Raichur
15	**MINIMUM NO OF PARTICIPANTS:**	10
16	**DETAILS OF SPONSORERS:**	---
17	**DETAILS OF RESOURCE PERSONS:**	**1. Mrs. Pallavi Biradar** Founder, Evolution of Blackboard, Bangalore **2. Mr. Chetan Gudi,** Asst. Professor, Department of CSE, NIT, Raichur.
18	**REGISTRATION FEE WITH CERTIFICATION:**	---
19	**REGISTRATION FEE WITHOUT CERTIFICATION:**	---
20	**FACULTY COORDINATOR:**	1. Mr. Sivakumar Reddy 2. Mrs. Shanthi E
21	**EVENT ORGANISERS (STUDENTS):**	---
22	**PROGRAMME PROPOSED BY:**	Dept. of CSE
23	**ANY OTHER RELEVANT MATTER:**	Nil
24	**ESTIMATED BUDGET IN RS:**	Rs. 35,000/-

24.12.18

PRINCIPAL

PRINCIPAL
...vodaya Institute of Technology (NIT)
RAICHUR-584 103

24/12/18

HOD

...ead of Department
...omputer Science & Engineering
...vodaya Institute of Technology
RAICHUR-584 103

24/12/18

EVENT COORDINATOR

...VODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

NAVODAYA
Institute of Technology

Department of Computer Science & Engineering

Five days' FDP on
"Python Programming and Web Design"
on
24th to 28th January 2018

REGISTRATION FORM

Name : _____

Branch : _____

Designation : _____

College Name : _____
& Address

_____

Phone: Mobile: _____

E-mail : _____

Registration Fee Details

DD No. : _____ Date: _____

Bank : _____

Accommodation: YES ☐ NO ☐

DECLARATION: This is to certify that the above member is bonafide Staff of this institution/ Organization

Signature of the Participant

Sign. Of HOD/Principal/ Head of Organization (with seal)

DD to be drawn in favor of "PRINCIPAL, NIT Raichur" payable at SBI, NET Campus, Raichur.

Chief Patron:
Sri S. R. Reddy,
Hon'ble Chairman, NET[s] Raichur

Chief Convener:
Dr. M V Mallikarjuna
Principal, NIT Raichur

Convener:
Dr. M N Faruk
HOD, CSE, NIT Raichur

Coordinator:
Prof. Sivakumar Reddy, CSE, NIT
E-Mail: bskreddy64@gmail.com
Contact no. : +91 - 9686281370

Co-Coordinator:
Prof. Shanthi E, CSE, NIT
E-Mail: shanthi.e@navodaya.edu.in
Contact no. : +91 - 8310603989

Organizing Committee

1.	Prof. Vijay Kumar	Asst. Professor
2.	Prof. Hanumesh D	Asst. Professor
3.	Prof. Usha	Asst. Professor
4.	Prof. Bhagavati A	Asst. Professor
5.	Prof. Supriya P	Asst. Professor
6.	Prof. Jayashree	Asst. Professor
7.	Prof. Rajashekhar R	Asst. Professor

PRINCIPAL 23.11.17
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Five days' FDP
On
"Python Programming and Web Design"
On
24th to 28th January 2018

NAVODAYA
Institute of Technology



Organized by

Department of Computer Science & Engineering
Navodaya Nagar, Raichur-584 103, Karnataka.
Tel:08532-240115,240115
Mobile No: +91-9686281370
: +91-9663426789
Fax : 08532-240054

e-mail :- hodcse.nit@navodaya.edu.in
web :- ww.navodaya.edu.in/engineering

Computer Science & Engineering
Navodaya Institute of Technology
RAICHUR-584 103

ABOUT THE INSTITUTE:

Navodaya Institute of Technology, NIT, Raichur established in 2008, is affiliated to Visvesvaraya Technological University, VTU, Belagavi and is approved by All India Council for Technical Education (AICTE), New Delhi. The guiding spirit and the driving force for all the progress and achievements in the institution is our Hon'ble Chairman **Sri. S. B. Reddy** of Navodaya Education Trust®, Raichur. NIT, Raichur is promoting quality technical education in North Karnataka region in general and Hyderabad Karnataka Region in particular. The education is of mainstream importance for socio-economic development. The institute involves in imparting scientific &Technical training with value based education to enable overall development of technicians.

NIT-R is all-inclusive campus. Administrative Block, Library Block, separate and independent Academic Blocks for each Department, Power back up, Hostels for Boys and Girls, Canteen, Bank, ATM, Campus Temple, Gym and Play ground all should it a real campus of about 50 acres and surrounded by hillocks and sea.

ABOUT THE DEPARTMENT:

The Department of Computer Science & Engineering is established in the year 2008. The department offers BE program in Computer Science & engineering with annual intake 60. The Department has excellent lab facilities with state of the art equipments and has exclusive Computer Centers equipped with 96 Core i3 systems, 30 core 2 duo personal computers, 01 dual xeons & one NT server providing number of Computer Science & engineering related software packages. The department is enriched with highly qualified, experienced and committed faculty members and faculty are from reputed institutes. The Department gives importance not only to academic education but also ethical values and personality development of students by practicing a close mentoring system on regular basis. The department strives hard to impart quality education and to keep the students on the competitive edge of the present-day technology and marching towards University ranks also. the department is actively involved in placement of the students through ON/OFF Campus Interviews.

VISION

"To be acknowledged as a center of excellence in Computer Science & Engineering through teaching, research and consultancy programs in the context of societal needs, global trends, culture, leadership, entrepreneurship, ethics and to evolve as a world-class center of innovation."

MISSION

"The mission of the department is to periodically survey for advancing excellence in computing disciplines. It is being pursued through an objective of academic programmes in continuing to contemporary standards. The sustained effort is to produce competent graduates with potential to design and develop creative solutions via integration of software and hardware devices, intelligent approaches to programming and effective working as well as contribute well to the Community. Large scale software systems and computing infrastructures of an organization."

ABOUT WORKSHOP

The language of Python is extremely powerful and very advanced for web design and development. Developers with this skill are in great demand, but it is difficult to find a high-quality web development company that uses Python for web development. There are many web development and design languages a company can use, but Python is the best programming language out there because it is so advanced and complex. Because it is such a difficult language to use, developers who are skilled at creating a website with Python tend to want higher salaries. This means that most web design companies just can't have full-time programmers. Generally, only large web design companies can afford having Python web programmers on staff full-time.

SELECTION CRITERIA

The course is open to Students, Research Scholars, Faculties and Industrial Employees. Number of participants is limited to 50.

RESOURCE PERSON:

1. Mrs. Pallavi Biradar
Founder, Evolution of Blackboard, Bangalore

2. Mr. Chetan Gudi.
Asst. Professor, Department of CSE,
NIT, Raichur

IMPORTANT DATES:

Last date for Registration	: 20/01/2018
Participants list information via E-mail	: 22/01/2018

REGISTRATION FEES:

Research scholar	: Rs. 500/-
Faculties	: Rs. 500/-
Industrial Employees	: Rs. 700/-

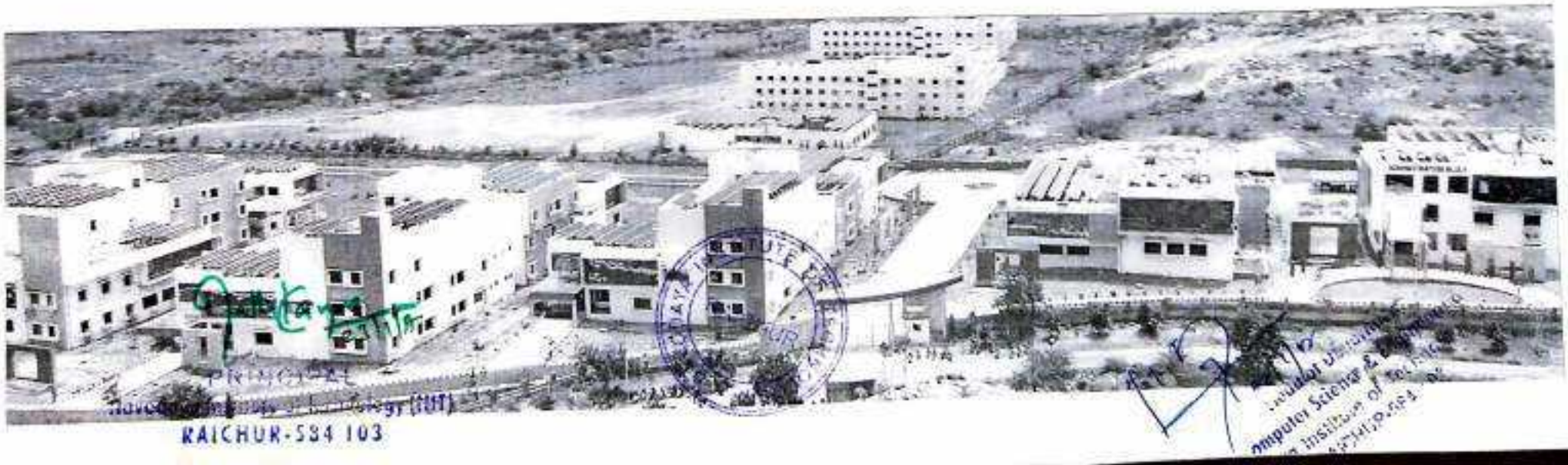
The Demand Draft drawn in favour of "Principal, NIT Raichur" payable at **NIT Campus Raichur**.

ACCOMODATION:

For the period of the workshop, the participants from the AICTE approved institutions will be provided accommodation in the student's hostel of Navodaya Institute of Technology, Raichur. Hostel Accommodation will be arranged based on their request.

How to reach Raichur:

Raichur is well connected by Road and Railway to all nearby and important cities like Bengaluru, Hyderabad, Chennai,



NET's
NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR
Tel : +91 8532-200115, 240334, Fax : +91-8532-240054
E-Mail :hodcse.nit@navodaya.edu.in

CIRCULAR

Cir: 2017-18/FDP/01

15.01.2018

Chief Patron
Shri. S. R. Reddy, Hon'ble Chairman,
Navodaya Education Trust (NET®),
Raichur.

Chief Convener
Dr. M V Mallikarjuna,
Principal, NIT, Raichur

Convener
Dr. M. N. Faruk
HOD, CSE, NIT. Raichur

Coordinator
Prof. Sivakumar Reddy, CSE, NIT
E-Mail: bskreddy64@gmail.com
Contact no. : +91 - 9686281370

Co-Coordinator
Prof. Shanthi E, CSE, NIT
E-Mail: shanthi.e@navodaya.edu.in
Contact no. : +91 - 8310603989

Organizing Committee
1. Prof.Vijay Kumar
2. Prof. Hanumesh D
3. Prof. Usha
4. Prof. Bhagavati A
5. Prof. Supriya Purohit
6. Prof. Rajashekhar R
7. Prof. Jayashree

Respected Sir / Madam,

Greetings from Navodaya Institute of Technology, Raichur...

The department of Computer Science & Engineering is organizing a Two day FDP on **"Python Programming and Web Design"** The attached brochure may kindly be circulated among the faculty members / colleagues.

The language of Python is extremely powerful and very advanced for web design and development. Developers with this skill are in great demand, but it is difficult to find a high-quality web development company that uses Python for web development. There are many web development and design languages a company can use, but Python is the best programming language out there because it is so advanced and complex. Because it is such a difficult language to use, developers who are skilled at creating a website with Python tend to want higher salaries. This means that most web design companies just can't have full-time programmers. Generally, only large web design companies can afford having Python web programmers on staff full-time.

We are pleased to inform that the department of Computer Science & Engineering, Navodaya Institute of Technology is organizing a Faculty Development Programme during 24th Jan - 28th 2018.

In this context, we look forward your support and request you to encourage participation of professionals by circulating the attached brochure.

Thanking you,
Yours sincerely,

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Dr. M. N. Faruk
Convener
Computer Science & Engineering
Navodaya Institute of Technology
RAICHUR-584 103

NET's

NAVODAYA INSTITUTE OF TECHNOLOGY

BIJENGERA ROAD, RAICHUR - 584103

INVITATION

Department of Computer Science & Engineering

HODs & Faculties are invited to attend Five days

Faculty Development Program

on

"Python Programming & Web Design"

By

Mrs. Pallavi Biradar
Founder, Evolution of Blackboard, Bangalore
&
Mr. Chetan Gudi,
Asst. Professor, Department of CSE,
NIT, Raichur.

From *24th Jan To 28th Jan 2018*

Venue: Department of CSE

RAICHUR-584 103

INSTITUTE OF TECHNOLOGY RAICHUR

Computer Science & Engineering

Inaugural function Agenda

1. Introduction
2. Invocation song
3. Welcome address by Dr. M. N. Faruk - HOD/CSE
5. Introduction of Resource person
6. Chief Guest Address
7. Presidential address
8. FDP- Session - I

Five day FDP on
Python Programming & Web Design

1. Introduction
2. Feedback - Faculty participants
3. Feedback by HOD
5. Feedback by Coordinator
6. Feedback by Resource person/Chief Guest
7. Certificate distribution
8. Presidential address
9. Vote of Thanks

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Head of Department
Computer Science & Engineering
Navodaya Institute of Technology
RAICHUR-584 103

Inaugural function Agenda

1. Introduction
2. Invocation song
3. Welcome address by Dr. M. N. Faruk - HOD/CSE
5. Introduction of Resource person
6. Chief Guest Address
7. Presidential address
8. FDP- Session - I

Five day FDP on

Python Programming & Web Design

1. Introduction
2. Feedback - Faculty participants
3. Feedback by HOD
5. Feedback by Coordinator
6. Feedback by Resource person/Chief Guest
7. Certificate distribution
8. Presidential address
9. Vote of Thanks

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Head of Department
Computer Science & Engineering
Navodaya Institute of Technology
RAICHUR-584 103

NAVODAYA Institute of Technology RAICHUR

Faculty development programme on

PYTHON PROGRAMMING & WEB DESIGN

Schedule on 24.01.2018 to 28.01.2018

Day	*Session-1 (09.30 am to 11.00am)*			*Session-2 (11.15 am to 12.45pm)*		*Session-2 (2.00 pm to 3.30pm)*		*Session-3 (3.45pm to 5.15pm)*
Wednesday (24.01.2018)	*Registration 09.00 to 10.00*	*Inauguration and Key note address by* **Mrs. Pallavi Biradar** Founder, Evolution of Blackboard, *Bangalore*	*Tea Break (11.00am To 11.15 am)*	**Mrs. Pallavi Biradar** Founder, Evolution of Blackboard, *Bangalore* (Introduction to Python)	*Lunch break (12.45pm to 02.00pm)*	**Chetan Gudi,** Asst. Professor, Department of CSE, NIT, Raichur. (Classes and Objects)	*Tea Break (03.30pm To 03.45 pm)*	**Chetan Gudi,** Asst. Professor, Department of CSE, NIT, Raichur. (Classes and Objects)
Thursday (25.01.2018)	**Mrs. Pallavi Biradar** Founder, Evolution of Blackboard, *Bangalore* (Modules and Packages)			**Mrs. Pallavi Biradar** Founder, Evolution of Blackboard, *Bangalore* (Modules and Packages)		**Chetan Gudi,** Asst. Professor, Department of CSE, *NIT, Raichur.* (Exception Handling)		**Chetan Gudi,** Asst. Professor, Department of CSE, NIT, Raichur. (Exception Handling)

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Day	Session-1 (09.30 am to 11.00am)		Session-2 (11.15 am to 12.45pm)		Session-2 (2.00 pm to 3.30pm)		Session-3 (3.45pm to 5.15pm)
Friday (26.01.2018)	**Chetan Gudi,** Asst. Professor, Department of CSE, NIT, Raichur. (Data Structures)	Tea Break (11.00am To 11.15 am)	**Chetan Gudi,** Asst. Professor, Department of CSE, NIT, Raichur. (Data Structures)	Lunch break (12.45pm to 02.00pm)	**Mrs. Pallavi Biradar** Founder, Evolution of Blackboard, *Bangalore* (Introduction to Django)	Tea Break (03.30pm To 03.45 pm)	**Mrs. Pallavi Biradar** Founder, Evolution of Blackboard, *Bangalore* (Introduction to Django)
Saturday (27.01.2018)	**Chetan Gudi,** Asst. Professor, Department of CSE, *NIT, Raichur.* (Templates)		**Mrs. Pallavi Biradar** Founder, Evolution of Blackboard, *Bangalore* (Templates)		**Mrs. Pallavi Biradar** Founder, Evolution of Blackboard, *Bangalore* (Model Layer)		**Chetan Gudi,** Asst. Professor, Department of CSE, *NIT, Raichur.* (Django Admin)
Sunday (28.01.2018)	**Mrs. Pallavi Biradar** Founder, Evolution of Blackboard, *Bangalore* (Routing in Django)		**Mrs. Pallavi Biradar** Founder, Evolution of Blackboard, *Bangalore* (Django Sessions)		**Chetan Gudi,** Asst. Professor, Department of CSE, *NIT, Raichur.* (Django Forms)		**Chetan Gudi,** Asst. Professor, Department of CSE, *NIT, Raichur.* (Django Forms)

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Computer Science & Engineering

Date: 20.01.2018

From
Dr. M. N. Faruk,
Professor and Head,
Department of CSE/ISE
Navodaya Institute of Technology,
Raichur-584103.

To
The Principal,
Navodaya Institute of Technology,
Raichur-584103

Sir

Subject: Req. for Auditorium hall to conduct FDP - Reg

We have proposed to conduct the FDP on the topic "**Python Programming & Web Design**" on 24.01.2018 to 28.01.2018. For this Purpose, we need auditorium on 24.01.2018 for inauguration session from 10.00AM-12:00PM. Kindly permit us to use the auditorium.

Thanking You

Yours faithfully

[Dr. M. N Faruk]

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

From

Dr. M. N. Faruk,

HOD, CSE.,

NIT, Raichur

20.01.2018

To

The Principal,

NIT, Raichur.

Sir,

Sub: Requisition for Accommodation and Food -Reg

I bring to your kind notice that we are conducting the Five days FDP on "Python Programming & Web Design" in our department from 24th Jan 2018 to 28th Jan 2018. For that we want to arrange the accommodation and food for the Resource Persons on the above said days. So kindly do the needful.

Resource Person Details,

1. **Mrs. Pallavi Biradar**
   Founder, Evolution of Blackboard,
   Bangalore

Thanking you,

HOD, CSE

Computer Science & Engineering
National Institute of Technology
RAICHUR-584 103

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA
Institute of Technology
RAICHUR

Department of Computer Science & Engineering

Faculty Development Programme (FDP) on

"Python Programming and Web Design"

From 24th to 28th Jan 2018

Participants Details

S.No.	Name of the Faculty	Designation & Dept.
1.	Dr. M. N. Faruk	Professor & Head, CSE
2.	Prof. Chetan Gudi	Asst. Professor, CSE
3.	Prof. Sivakumar Reddy	Asst. Professor, CSE
4.	Prof. Hanumesh D	Asst. Professor, CSE
5.	Prof.Vijay Kumar	Asst. Professor, CSE
6.	Prof.Shanthi E	Asst. Professor, CSE
7.	Prof. Jayashree	Asst. Professor, CSE
8.	Prof. Majumdar	Asst. Professor, EEE
9.	Prof. Daniel N	Asst. Professor, EEE
10.	Prof. Geetha	Asst. Professor, EEE
11.	Prof. Rammurthy	Asst. Professor, ECE
12.	Prof. Sachin	Asst. Professor, ECE
13.	Prof. Chennaveeramma	Asst. Professor, ECE
14.	Prof. Satish	Asst. Professor, EEE

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

HOD & Convener
Computer Science & Engineering
Navodaya Institute of Technology
RAICHUR-584 103

Report on Five Days

A Five days Faculty Development Program (FDP) on "Python programming and Web Design was organized by the Department of Computer Science and Engineering , NIT, Raichur during 24 – 28 January 2018.

Day 01:

- Expert Talk on Introduction to Python Programming
  - Introduction, Installation of python , variables and Data types , operators ,conditional statements , Looping , Control statements and String manipulations.
- Practical Session 01: Installation , Operator & Loops

Day 02:

- Expert Talk on Functions and List
  - Functions.
  - Concepts of OOPS
  - Implementations of OOPS concepts
- Practical Session 02: Implementation of OOPS and working with Tuple.

Day 03:

- Expert Talk on Dictionary set , file handling and Exception handling
- Regular expressions , Arrays, Functions, Recursion.
- Practical Session 03: Exception handling assignments and Regular Expressions.

Day 04:

- Expert Talk on Python Project Discussions.
- Understanding anaconda installation & Numpy
- Practical Session 04: Project Executions and Anaconda installation & Working numpy.

Day 05:

- Expert Talk on Understanding pandas and working with datasets.
  - Working with pandas
  - Exploring datasets for salary and economics purchase.
  - Understanding matplotlib, scipy
- Practical Session 05: Working with regressions, clustering , working with matplotlib, scikit learn scipy.

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

RAICHUR

Head of Department
Computer Science & Engineering
Navodaya Institute of Technology
RAICHUR-584 103

NAVODAYA
Institute of Technology
RAICHUR

RAICHUR- 584103, KARNATAKA

DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

CERTIFICATE

This is to certify that Mr./Ms./Dr. Vijaykumar Yadav of CSE has attended Five day FDP on "***Python Programming and Web Design***" conducted at Navodaya Institute of Technology, Raichur on 24-01-2018 to 28-01-2018.

Coordinator

HOD

Principal

NAVODAYA
Institute of Technology
RAICHUR

RAICHUR- 584103, KARNATAKA

DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

CERTIFICATE

This is to certify that Mr./Ms./Dr. Hanumesh D of CSE has attended Five day FDP on *"Python Programming and Web Design"* conducted at Navodaya Institute of Technology, Raichur on 24-01-2018 to 28-01-2018.

Coordinator

HOD

Principal

NAVODAYA
Institute of Technology
RAICHUR

RAICHUR- 584103, KARNATAKA

DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

CERTIFICATE

This is to certify that Mr./Ms./Dr. Jayashree of CSE has attended Five day FDP on "*Python Programming and Web Design*" conducted at Navodaya Institute of Technology, Raichur on 24-01-2018 to 28-01-2018.

Coordinator

Principal



Chetan Gudi
H.No: 4-4-223/84,
Near Hanuman Temple,
Sathyanath Colony
Mantralayam Road
Raichur – 584103

Mob: 9663426789
E-mail: chetangudi@navodaya.edu.in

Objective: To work with an institution as an employee, that will provide me a good platform to utilize my teaching & administration skills and will help me to grow my career.

Academic Education:

- Completed Masters of Technology (Computer Science & Engineering) from the Poojya Doddappa Appa College of Engineering (PDACE), Kalaburgi, in the year of 2011
- Secured Bachelor of Engineering (Information Science & Engineering) Degree from the Khaja Banda Nawaz College of Engineering(KBNCE), Kalaburgi in the year of 2008

Professional Qualifications:

- Possess about 07 years and 01 months of experience in the field of education (both teaching and administration)
- Thorough understanding of the subject with ability to convey the same to the students.
- Possess knowledge about the internal administrative tasks that are performed within Department.

Professional Experience:

Navodaya Institute of Technology, Raichur (2011-Present) (07 Years 01 Months)

Assistant Professor

- Working as alumni coordinator since from 2014 till present.
- Conducted an alumni meet in 2015. (Alumni meet and Inauguration)
- Conducted the subject lectures regularly, keeping an eye on the understanding of the students.
- Carried out practical sessions with explanation about the purpose of the experiment and the procedures that follow to seek results.
- Carried out debate sessions in regular classes to help students to understand the subject better.
- Encouraged students to build committees among themselves.
- Presented a paper titled **"LEAF CLASSIFICATION BASED ON SHAPE FEATURES USING BACKPROPOGATION NEURAL NETWORKS"** in national conference conducted in PESITM, Shimoga.
- Attended workshops on "MATLAB AND ITS APPLICATIONS", "ANDROID", "CLOUD COMPUTING", "LATEX", "DIGITAL IMAGE PROCESSING".
- Got 100% result 4 times in System software and UNIX System Programming.
- Got on an average of 95% in the subjects I have handled.

- Represented my department to the Management once, in the year 2014-15.
- Took a session on career guidance program in Manvi, Sindhanoor & Lingsugur in the year 2015.
- Served the institution as In-charge Head of The department for one and half years.
- Registered for part-time Ph.D under VTU, Belgaum in the year 2016.
- Have little experience of working for getting NAAC.
- Organized Technical and Cultural Fest 3 times in NIT as a Chief coordinator for Venue Committee.
- Given a guest Lecture on "Java/J2ee and IDE" in a polytechnic college.
- Presently I am one of the member of admission promotion team.
- Organized a workshop on "ANDROID Application Development- A hands-on Session" in the year 2016.
- Arranged interactive sessions 3 times between alumni and students of all departments.

Subjects Taught:

System Software, UNIX Shell Programming, UNIX System Programming, C Programming, Software Architecture, JAVA/J2EE, C++, Data Structures, Computer Networks, Compiler Design, Digital Image Processing, Web Programming, Data Mining & Data Warehousing, Ad-hoc Networks, Management & Entrepreneurship, PYTHON Application Programming, Computer Graphics.

Extra-Curricular:

- Conducted indoor games for both students and faculties.
- Participated in inter-college cricket match.

Personal Details:

NAME:	CHETAN GUDI
FATHER'S NAME:	G MADHAVACHAR
MOTHER'S NAME:	SMT. MANIMALA GUDI
NATIONALITY:	INDIAN
RELIGION:	HINDU
CASTE:	BRAHMIN
LANGUAGES KNOWN:	KANNADA, ENGLISH, HINDI AND TELUGU
MARITAL STATUS:	MARRIED
ADDRESS:	H.NO: 5-31, PRASHANT NAGAR (B), RAJAPUR COLONY KALABURGI-585105
CONTACT NUMBER:	+91 9663426789 / +91 9742024262
E-MAIL:	chetangudi@gmail.com / chejangudi@navodaya.edu.in

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Prof. CHETAN GUDI

Head of Department
Computer Science & Engineering
Navodaya Institute of Technology

CURRICULUM VITAE

PALLAVI WARAD

waradpallavi@gmail.com, palavi.birada@gmail.com Mob No: +91855387[illegible]

Correspondence Address
#332,
Sunita Nilaya,
3rd main,
16th cross,
4th Phase JP Nagar,
Bangalore-560078

Personal Data
Date of Birth: 04/08/1987
Gender: Female
Nationality: Indian
Marital Status: Married
Languages Known: English, Hindi, Kannada and Telgu.

Permanent Address:

C/o Basawaraj Warad,
Plot No 58,
Santosh Colony,
Aland Road,
Kalaburagi 585103
Karnataka State

OBJECTIVE

Self-motivated and hardworking seeking for an opportunity to work in a challenging environment to prove my skills and utilize my knowledge & intelligence in the growth of the organization.

ACADEMIC CREDENTIALS

- **M Tech** in Computer Science & Engineering from PDA College of Engineering, Kalaburagi
  Class : **First Class with Distinction**
  University : **Autonomous under VTU, Belagavi**
- **B. E** in **Information** Science & Engineering from Appa Institute of Engineering and Technology, Kalaburagi.
  Class : **First Class**
  University : **VTU, Belagavi**
- **Intermediate-Science** from Muktambika Girls PU College, Kalaburagi
  Percentage:**84.95%**
- **S.S.C** from Central Board D.A.V Public School, Wadi(Jn)
  Percentage:**72.00%**

PROFESSIONAL ACHIEVMENTS

Paper Publications :

- Published paper Entitled "Flow Based Directional Routing with Load Balancing for Mobile Ad Hoc Networks" in an International conference ICRTET 2013, ISBN 978-93-82208-54-9.
- Published paper Entitled "Interference Reduction in Mobile Ad Hoc Networks Using Directional Antenna" in an International conference ICRTET 2013, ISBN 978-93-82208-54-9
- Published paper Entitled "Directional Flow Routing with Load Balancing: A New Routing Paradigm Using Directional Antenna" in an International conference AETM 2013, ISBN 978-81-927182-48.

Leadership Initiatives:

- Winner in "Distinguished Facilitator of Inspire Faculty Excellence Awards 2014" Campus Connect Programs Conducted by INFOSYS Limited.
- Participated as *Resource Person* in Faculty Development Program on "Apps Building with HTML5".
- Conducted Infosys Foundation Program for Infosys Students.
- Coordinated a National Level workshop on "Pc Troubleshooting, Network & System Administration" Organized by Dept of ISE PDACEG.
- Compered various Department level activities.
- Participated in various FDP and Workshops Organized by P.D.A.C.E, Gulbarga.
- Won a Second Prize in National level Puzzle Mania of"Technovision-08" held at GNDCE, Bidar.

DEPARTMENTAL RESPONSIBILTIES

Department Level Responsibilities :

- Coordinator of Result Analysis
- Coordinator of Student's Feedback
- Coordinator of BOS(Board Of Studies)
- Student Coordinator

PRINCIPAL
Jnanodaya Institute of Technology (IIIT)
RAICHUR-584 103

ACADEMIC EXPERIENCE: 5.8 years (Till Date)

Organization : **P.D.A College of Engineering and Technology, Kalaburagi**
Designation : **Assistant Professor**, since September 2010 to January 2014.
Description : 3.8 years of teaching-learning experience in an autonomous institution.

Organization : **S.V. Shoomarraddi College of Engineering and Technology, Hubballi**
Designation : **Assistant Professor**, Since Jan 2014 to Dec 2014.
Description : 1 year of teaching-learning experience in an autonomous institution.

Organization : **Cambridge Institute of Engineering and Technology, Bengaluru**
Designation : **Assistant Professor**, Since Aug 2015 to Aug 2016.
Description : 1 year of teaching-learning experience in a reputed institution.

Subjects Handled:

- Finite automata and formal languages.
- Discrete Mathematics.
- Graph Theory.
- Operating System.
- Database Management system.

Laboratories Handled:

- Database Management Systems
- Web Technologies
- Data Structures
- Analysis and Design of Algorithms.

Projects Guided:

- Involved in guiding the UG level project and seminars for the final year students.
- Project under my Guidance was selected in KSCST- Student Projects Program (SPP) 37th series of Seminar and Exhibition.

Project carried out:

M.Tech Project: It is based on routing in Mobile Ad hoc Networks.

Title: Flow Based Directional Routing with Load Balancing for Mobile Ad Hoc Networks

Description: The project includes enhancing the performance of Ad-hoc networks by using Antennas to reduce Interference in a Network and making a comparative study between Directional and Omni-directional Antenna.

Sports & Hobbies

Hobbies : Listening music, Traveling & Diary writing.
Sports : Badminton & Throw Ball

Personality Traits

- Excellent Presentation, motivational and leadership skills.
- Can handle different ability levels of students.
- Ability to provide quality knowledge that meets or exceeds market expectations.
- Quick learning and hard working.
- Excellent Communication Skills.

Declaration I hereby declare that all the information mentioned above is true to the best of my knowledge.

Place: Bengaluru

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Yours Sincerely,
Mrs. Pallavi Warad

NET GROUP OF EDUCATIONAL INSTITUTIONS: RAICHUR
NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR
~~*GUEST LECTURE/SEMINR/ADD ON COURSE /CONFERENCE/SHORT TERM TRAINING PROGRAMME/ANY OTHER*~~ ***PROPOSAL***

Date: 11-02-2016

	ACADEMIC YEAR	2015-16
2	**TYPE OF THE PROGRAMME PROPOSED**	FACULTY DEVELOPMENT PROGRAMME
3	**TITLE OF THE PROGRAMME:**	**'FINITE ELEMENT ANALYSIS FROM CONCEPT TO APPLICATION'**
4	**BROAD OBJECTIVE OF THE PROGRAMME:**	The main objective is to enhance the knowledge of students and faculties. To learn basic principles of finite element analysis procedure. To learn the theory and characteristics of finite elements that represent engineering structures. To learn and apply finite element solutions to structural, thermal, dynamic problem to develop the knowledge and skills needed to effectively evaluate finite element analyses.
5	**NAME OF THE INSTITUTION**	NET's NAVODAYA INSTITUTE OF TECHNOLOGY
6	**NAME OF THE DEPARTMENT(S)**	MECHANICAL ENGINEERING
7	**BENEFICIARIES**	Faculty Members, Post Graduate students, Research Scholars
8	**PROPOSED DATE OF FDP**	26th March-28thMarch 2016
9	**ELIGIBILITY CRITERIA SET:**	Faculty Members, Post Graduate students, Research Scholars from Mechanical Engineering and Civil Engineering
10	**NUMBER OF DAYS REQUIRED TO ORGANISE THE PROGRAMME**	Three Days FDP
11	**PROPOSED DURATION OF THE PROGRAMME**	24 Hours



12	**TOTAL NO. OF PERIODS REQUIRED FOR THE PROGRAMME**	24Periods
13	**NO. OF PERIODS REQUIRED PER DAY**	8 Periods
14	**LOCATION DETAILS:**	NETs NIT Auditorium
15	**DETAILS OF SPONSORERS:**	NIT Management
16	**DETAILS OF RESOURCE(S) PERSONS:**	Dr. M Siva Kumar Associate Professor Bannari Amman Institute of Technology, Satyamanghalam, Erode (TN). Dr. A.Megalingam Associate Professor Bannari Amman Institute of Technology, Satyamanghalam, Eroad (TN)
17	**REGISTRATION FEE WITH CERTIFICATION:**	Academicians and Research Scholar : Rs. 500/- Faculties : Rs. 700/- Industrial Employees : Rs 1000/-
18	**REGISTRATION FEE WITHOUT CERTIFICATION:**	----------
19	**EVENT ORGANISERS (STUDENTS):**	Sharath Kumar, Anwar Ali, Shantamma.
20	**FACULTY COORDINATOR:**	Mr. Ashok Malipatil/MECH/NITR
21	**PROGRAMME PROPOSED BY:**	Department Of Mechanical Engineering
22	**ANY OTHER RELEVANT MATTER:**	------
23	**ESTIMATED BUDGET IN RS:**	Rs.50,0000 /-

This is to the Honorable chairman of NIT for kind approval:

11/2/16

PRINCIPAL

PRINCIPAL
Jnyanodaya Institute of Technology (NIL)
RAICHUR-584 103

11.02.16

HOD

Head of Department
Mechanical Engineering
Navodaya Institute of Technology (R)

EVENT COORDINATOR

APPROVED/NOTAPPROVED

INSTITUTE OF TECHNOLOGY RAICHUR

NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

Tel : +91 8532-200115, 240334, Fax : +91-8532-240054

E-Mail :hodmech.nit@navodaya.edu.in

CIRCULAR

Chief Patron
Shri. S. R. Reddy, Hon'ble Chairman,
Navodaya Education Trust (NET®),
Raichur.

Patron
Shri. N. Bharath Reddy, Hon'ble Trustee,
Navodaya Education Trust (NET®),
Raichur.

Chief Convener
Dr.Shivareddy. M.S.
Principal and Dean, NIT, Raichur

Convener
Dr.P Rathnakumar,
HOD/ ME, NIT, Raichur

Coordinator
1. Prof. Ashok Malipatil,ME, NIT
   Contact no. : +91 - 9972688143
2. Prof.MD Umar, ME, NIT.
   Contact no. : +91 – 9980339850
3. Prof. Srikar G Kulkarni, ME, NIT.
   Contact no. : +91 – 9844929937
4. Prof. Sangamesh N D,ME, NIT.
   Contact no. : +91 –8105070217

Organizing Committee
1. Prof.Rajashakarappa
2. Prof.N Pradeep Kumar
3. Prof.Rajashekhar
4. Prof.Manjnath B
5. Prof.Ravi Kulkani
6. Prof.Venkat reddy
7. Prof. Imran Basha

18.03.2016

Respected Sir / Madam,
Greetings from Navodaya Institute of Technology, Raichur…

The department of Mechanical Engineering is organizing a Three day FDP on **"FINITE ELEMENT ANALYSIS FROM CONCEPT TO APPLICATION"** The attached brochure may kindly be circulated among the students and faculty members / colleagues.

This program is aimed at providing the necessary concepts, knowledge and skills under the boundary for a successful innovative concept in finite element analysis for achieving advanced techniques and fulfilling the needs of designers. The course will offer numerous opportunities for invaluable practical experience in FEA. The participants would be required to put their design skills into practice for addressing the real life challenges and gaining valuable additional skills. This concept enables to gain a unique understanding of innovative applications in design and analysis

We are pleased to inform that the department of Mechanical Engineering, Navodaya Institute of Technology is organizing a Faculty Development Programme from 26th March-28thMarch 2016.

In this context, we look forward your support and request you to encourage participation of professionals by circulating the attached brochure.

Thanking you,
Yours sincerely,

Dr.P Rathnakumar
Convener

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

NET's
NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

IAVODAYA

Department of Mechanical Engineering

Three day's Faculty development program on
"Finite element Analysis From Concept To Application"

26th March-28th March 2016.

REGISTRATION FORM

Name : _____

Branch : _____

Designation : _____

College Name: _____
& Address

_____

Phone/ Mobile: _____

E-mail : _____

Registration Fee Details

DD No. : _____ Date: _____

Bank : _____

Accommodation: YES ☐ NO ☐

DECLARATION: This is to certify that the above member is bonafide Staff of this institution/ Organization.

Signature of the Participant

Sign. Of HOD/Principal/ Head of Organization (with seal)

DD to be drawn in favor of "PRINCIPAL, NIT Raichur" payable at SBI, NET Campus, Raichur.

Chief Patron:

Sri S. R. Reddy,
Hon'ble Chairman, NET® Raichur

Patron:
Sri N. Bharath Reddy,
Trustee, NET® Raichur

Chief Convener:
Dr.Shivareddy.M.S
Principal,
N.I.T Raichur

Convener:
Dr.P Rathnakumar
Professor and Head
Department of ME-NITR

Coordinator:
Prof.**Ravi Kulkarni**
Assistant Professor
Department of ME-NITR
Prof.**Srikar G Kulkarni**
Assistant Professor
Department of ME-NITR

Organizing Committee:

Dr.Anantachar	Professor
Prof. N Pradeep Ilay	Assistant professor
Prof. Geetha V	Assistant Professor
Prof.Priyankar D	Assistant Professor
Prof.Zeeshan Ali	Assistant Professor
Prof.Md. Umar	Assistant Professor
Prof.Vinayaka Manur	Assistant Professor
Prof.Mallikarjun	Assistant Professor
Prof.Faheem Akthar	Assistant Professor
Prof.Imran Basha	Assistant Professor
Prof.Rajashakarappa	Assistant Professor

Five day's
FACULTY DEVELPOMENT PROGRAM
"ADVANCEMENT IN COMPOSITE MATERIALS"

26th March-28th March 2016.



Organized by
Department of Mechanical Engineering
Navodaya Nagar, Raichur-584 103,
Karnataka.

Tel:08532-240115,240115
Mobile No: +91-7337764346
: +91-9980339850

E-mail:- hodmech.nit@navodaya.edu.in
ravikulkarni@navodaya.edu.in

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

ABOUT THE INSTITUTE:

Navodaya Institute of Technology (NIT), Raichur, established in 2008, is affiliated to Visveswaraya Technological University (VTU), Belagavi and is approved by All India Council for Technical Education (AICTE), New Delhi. The guiding spirit and the driving force for all the progress and achievements of the institution is our most respected Chairman Sri. **S. R. Reddy** of Navodaya Education Trust®, Raichur. NIT focuses on promoting quality technical education in North Karnataka region in general and Hyderabad Karnataka Region in particular. The education is of paramount importance for socioeconomic development. The Institute focuses on integral scientific &Technical training with value based education to enable overall development of aspirants.

NIT-R is all-inclusive campus. Administrative Block, Library Block, Separate and Independent Academic Blocks for each Department, Power back up, Hostels for Boys and Girls, Canteen, Bank, ATM, Campus Temple, Gym and Playground all housed in a vast campus of about 30 acres land surrounded by hillocks and lake.

ABOUT THE DEPARTMENT:

The department of Mechanical Engineering is established in the year 2010 and started its humble share & contribution of the Mechanical Engineering graduates for national needs. The department offers BE program in Mechanical engineering with annual intake 120 and subsequently introduced M.Tech. (Thermal power Engineering) program in 2014. The Department has excellent lab facilities with state of the art equipment and has exclusive Computer Centers equipped with 30 Core-i3 systems, one NT server including number of Mechanical engineering related software packages. The department is enriched with highly qualified, experienced and committed faculty members and faculty are from reputed institutes like IITs, NITs and other prestigious universities. The Department gives importance not only to academic education but also ethical values and personality development of students by practicing a close mentoring system on regular basis. The department strives hard to impart quality education and to keep the students on the competitive edge of the present-day technology and marching towards University ranks also, the department is actively involved in placement of the students through ON/OFF Campus Interviews.

VISION:

"To become a center of excellence in Mechanical Engineering, producing innovative and creative mechanical engineers to meet the global challenges"

MISSION:

"To provide a platform to the students towards attaining quality education in Mechanical Engineering. To educate students about professional & ethical responsibilities and train them to build leadership and entrepreneurship qualities for their career development. To create opportunities and guide students in acquiring career oriented jobs in the field of Mechanical Engineering."

ABOUT FDP

The primary purpose of this program is to enable the faculty with skills and proficiency in the domains of AERO/AUTO/CIVIL/MECH/Industrial Engineering and Production disciplines in applying FEM tools in writing mode with appreciation for practical problems to best practices that are integral with industry relevant solutions. This program offers excellent guidance on how to judge which approximations are acceptable and make it appropriate for solving a wide range of practical problems of equal importance in the manner in which the results are interpreted. Assistances are provided which would result into correct decisions to be taken based on reliable results. Interaction and deliberations are encouraged throughout the programme.

SELECTION CRITERIA This course is open to, Research Scholars, Faculties and Industrial Employees.

RESOURCE PERSONS:

Resource persons are from reputed Universities, Ph.D Scholars Experts in the subject.

IMPORTANT DATES:

Last date for Registration	:	**20/03/2016**
Participants for conformation via E-mail	:	**25/03/2016**

REGISTRATION FEES:

Students and Research Scholar	: **Rs. 300/-**
Faculties	: **Rs. 500/-**
Industrial Employees	: **Rs. 700/-**

"The Demand Draft drawn in favour of **"Principal, NIT Raichur"** payable at **NIT Campus Raichur.**

ACCOMODATION:

For the period of the workshop, the participants from the AICTE approved institutions will be provided accommodation in the student's hostel of Navodaya Institute of Technology, Raichur. Hostel Accommodation will be arranged based on their request.

How to reach Raichur:

Raichur is well connected by Road and Railway to all nearby and important cities like Bengaluru, Hyderabad, Chennai, Mumbai, Tirupathi, Kalaburgi, Hubballi, and Bellary and so



Department of Mechanical Engineering

Three days FDP on Finite Element Analysis from Concept to Application

Course content:

The FDP aims to include following themes:

- ✓ Introduction to FEM and other Numerical Methods.
- ✓ Basics of linear finite element analysis procedures.
- ✓ Variational and weighted residual formulations.
- ✓ Assembly of stiffness matrices, application of loading and boundary conditions.
- ✓ Applications of FEM to 1 -D and 2-D Problems using ANSYS.
- ✓ Theoretical and practical aspects will be given equal importance.

Day 1: 26-03-2016

09.30 am to 11.30 a.m.: Inaugural Function of 3 days FDP

11.30 am to 11.45 a.m.: Tea Break

11.45 am to 01.00 p.m.: Introduction to FEA and weighted residual formulations

01.00 pm to 02.00 p.m.: Lunch Break

02.00 pm to 03.15 p.m.: Introduction to ANSYS & Modeling

03.15 pm to 03.30 p.m.: Tea Break

03.30 pm to 04.45 p.m.: Meshing in ANSYS

Day 2: 27-03-2016

09.30 am to 11.30 am: Finite Element Formulation of Bar and Truss Element

11.30 am to 11.45 am: Tea Break

11.45 am to 01.00 pm: Finite Element Formulation of Beam Element

01.00 pm to 02.00 pm: Lunch Break

02.00 pm to 03.15 pm: Stepped bar and Truss Problem

03.15 pm to 03.30 pm: Tea Break

03.30 pm to 04.45 pm: Cantilever, SSB and Fixed Beam Problem

Day 3: 28-03-2016

09.30 am to 11.30 am: Formulation of 1-D Heat Conduction, Convection & Internal Heat Generation.

11.30 am to 11.45 am: Tea Break

11.45 am to 01.00 pm: Formulation of Vibration in 1-D bar element

01.00 pm to 02.00 pm: Lunch Break

02.00 pm to 03.30 pm: Heat Transfer Problem & Vibration Problem

03.45 pm to 04.00 pm: Tea Break

04.00 pm to 04.45 pm: Valedictory Function



NET's

NAVODAYA INSTITUTE OF TECHNOLOGY

BIJENGERA ROAD, RAICHUR - 584103

INVITATION

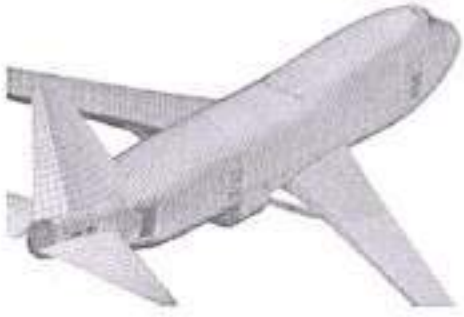
Welcome all the HOD & Faculties for Three Day's

FACULTY DEVELOPMENT PROGRAMME

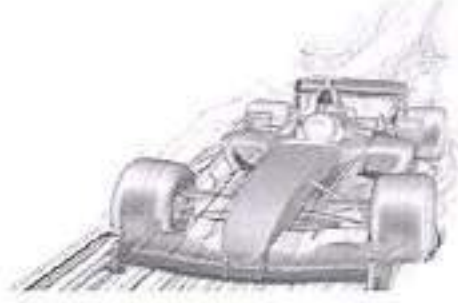
on

" Finite Element Analysis from Concept to Applications"

26,27&28th March, 2016







PRINCIPAL 25/03/16
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

26/03/16
Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology (NIT)
RAICHUR.

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

22.03.2016

2015-16/FDP-01

From
Dr. P Rathnakumar,
Professor and Head,
DeptLof ME,
Navodaya Institute of Technology,
Raichur-584103.

To
The Principal,
Navodaya Institute of Technology,
Raichur-584103

Sir

Subject: Req. for Auditorium hall to conduct FDP - Reg

We have proposed to conduct the FDP on the topic "**FINITE ELEMENT ANALYSIS FROM CONCEPT TO APPLICATION**" from 26.03.2016 to 28.03.2016. For this Purpose, we need auditorium from 26.03.2016 to 28.03.2016-Time-10.00AM-5.30PM.

Thanking You

Yours faithfully

[Dr. P Rathnakumar]

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology
RAICHUR.

22/03/16
PRINCIPAL
Navodaya Institute of Technology
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

2015-16/FDP - 02

From

23.03.2016

Dr.P Rathnakumar,

HoD/ME.,

NIT, Raichur

To

The Principal,

NIT, Raichur.

Sir,

Sub: Requisition for Accommodation and Food -Reg

I bring to your kind notice that we are conducting the Three days FDP on **"Finite Element Analysis from Concept to Application"** in our department on from 26.03.2016 to 28.03.2016. For that we want to arrange the accommodation and food for the Resource Persons in the above days. So kindly do the needful.

Resource Person Details,

1. Dr. M Siva Kumar
   Associate Professor,
   Bannari Amman Institute of Technology,
   Satyamanghalam,
   Erode (TN).

2. Dr. A.Megalingam
   Associate Professor,
   Bannari Amman Institute of Technology,
   Satyamanghalam,
   Eroad (TN)

Thanking you,

23/03/16

HOD/ME

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology (NIT)
RAICHUR.

23/03/16

PRINCIPAL
Navodaya Institute of Technology
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

NET's

NAVODAYA INSTITUTE OF TECHNOLOGY, RAICHUR

DEPARTMENT OF MECHANICAL ENGINEERING

SUMMARY OF FACULTY DEVELOPMENT PROGRAMME ON

"FINITE ELEMENT ANALYSIS FROM CONCEPT TO APPLICATION"



Three day's Faculty development program "Finite elements analysis and applications" was held 26th, 27th& 28th of March 2016. Around 25 external participants from various Colleges are successfully participated in this Program. Dr. P. Rathnakumar, Head of Mechanical Engineering Department welcomed the gathering. Dr. Shivaprakash C.K. Dean/Principal gave a presidential address and facilitated the Chief Guests with mementos. The function was inaugurated and given brief description related to Selection, Fabrication and application oriented by the two specialized Resource Persons Dr. A. Megalingam, and Prof.M.Siva Kumar Assistant Professor, Bannari Amman Institute of Technology, Erode, Tamil Nadu. Prof. MD Umar, Assistant Professor, Department of Mechanical Engineering coordinator overviewed of this FDP.

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Head of Mechanical Engineering
Navodaya Institute of Technology
RAICHUR

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

NAVODAYA Institute of Technology RAICHUR

DEPARTMENT OF MECHANICAL ENGINEERING

Faculty Development Programme (FDP) on

"FINITE ELEMENT ANALYSIS FROM CONCEPT TO APPLICATION"

From 26th March-28thMarch 2016

S.No.	Name of the Faculty	Designation & Dept.	Signature
1.	Ashok Malipatil	A.P ME / NITR	
2.	Manjunath. B	" Mech / NITR	
3.	N. pradeep	" ME / NITR	
4.	Rajashekar	" Mechanical	
5.	Kotresh Chithaiah	" CIVIL	
6.	Shambavi. T	" CIVIL	
7.	md. Adil	" CIVIL	
8.	Kirupaxappa	" CIVIL	
9.	Srikar Kulkarni	mechanical	
10.	Ravi Kulkarni	mechanical	
11.	G. Ramesh	M.Tech/Thermal	
12.	K. Balachadrappa	M.Tech/FCE	
13.	Antony. K	M.Tech/Design	
14.	Krishnan	M.Tech/Design	
15.	Adarash	Mech/KPC	
16.	Vinayaka	-"-	
17.	Mallikarjun	-"-	
18.	pradeep Kumar	-"-	
19.	Vijay Kumar	-"-	
20.	K. Veerappa	Mechanical/KPE	

28/3/16

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY * RAICHUR

HOD & Convener

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology (NIT)
RAICHUR.

NAVODAYA
Institute of Technology
RAICHUR

DEPARTMENT OF MECHANICAL ENGINEERING

Faculty Development Programme (FDP) on

'FINITE ELEMENT ANALYSIS FROM CONCEPT TO APPLICATION'

26th March-28thMarch 2016

Evaluation Form

FDP Title: FEA

Date: 28-3-2016 Time: 4pm

Please complete the following evaluation for the FDP you attended. Your feedback will help us evaluate the effectiveness of this program and allow us to make improvements in the future program. **Your feedback is important for planning future programming and services we offer.**

Thank you.

4=Strongly Agree	3=Agree	2=Disagree	1=Strongly Disagree

	Circle your response			
1. The course met my expectations.	4	3	2	1
2. The content was helpful.	4	3	2	1
3. The level of the course was appropriate.	4	3	2	1
5. The speaker had a good understanding of the topics.	4	3	2	1
6. The handouts were helpful.	4	3	2	1
7. The course was worth my time.	4	3	2	1
8. The length of the course was appropriate.	4	3	2	1
9. I recommend that the course be repeated for other students.	4	3	2	1

What were the most useful aspects of the course?

—

What changes should be made to enhance/improve this program?

—

Additional comments: Very good

Name of the Faculty: Prof. Ashok Naikpatil Signature

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Department of Mechanical Engineering

FDP/GUEST LECTURE/SEMINR/ADD-ON COURSE /CONFERENCE/SHORT TERM TRAINING PROGRAMME/ANY OTHER PROPOSAL **PROPOSAL**

Date: 10.10.2016

1	ACADEMIC YEAR	2016-17
2	TYPE OF THE PROGRAMME PROPOSED	FACULTY DEVELOPMENT PROGRAMME
3	TITLE OF THE PROGRAMME:	"ADVANCED RESEARCH METHODS AND ANALYTICS"
4	PROPOSED DATE OF THE PROGRAM	14th-18th November 2016.
5	BROAD OBJECTIVE OF THE PROGRAMME:	Concept development of the researchers and faculty to define research problem and framework Help faculty and research scholar to formulate a research proposal Guide faculty and researchers in developing suitable research methodology and data analytical tools To provide a hands-on experience of the models and tools used for conducting research in the different subject domains To provide exposure to the participants to various Bi Variate and multi-variate methods in research and understand advanced issues of research. Train researchers to draw better conclusion from the analysis Guide researchers in writing a research paper To develop the flair for reading & critiquing research articles.
6	NAME OF THE INSTITUTION	NIT-RAICHUR

TECHNOLOGY * NAVODAYA INSTITUTE RAICHUR

7	NAME OF THE DEPARTMENT(S)	MECHANICAL ENGINEERING
8	BENEFICIARIES	Faculties, Post Graduates students.
9	ELIGIBILITY CRITERIA SET:	Faculties and post Graduates students.
10	NUMBER OF DAYS REQUIRED TO ORGANISE THE PROGRAMME	05 DAYS
11	PROPOSED DURATION OF THE PROGRAMME	30 Hours
12	TOTAL NO. OF PERIODS REQUIRED FOR THE PROGRAMME	30 Periods
13	NO. OF PERIODS REQUIRED PER DAY	6 Periods
14	LOCATION DETAILS:	NETs NIT Auditorium
15	MINIMUM NO OF PARTICIPANTS:	60
16	DETAILS OF SPONSORERS:	NIT Management
17	DETAILS OF RESOURCE(S) PERSONS:	**Dr. Anantachar** Prof & Head Department of Farm machinery & Power engineering, College of Agricultural Engineering UAS Raichur **Dr K V Prakash** Professor & Head Department of Renewable energy College of Agricultural Engineering UAS Raichur **Dr. K.M.Manjunath** In charge Farm Machinery Testing Center College of Agricultural Engineering UAS Raichur **Dr Ramesh B** In charge Farm Machinery Testing Center College of Agricultural Engineering UAS Dharwad



19	EVENT ORGANISERS (STUDENTS):	Balaji PatiL, MM Ravichand, MD Arafath
20	FACULTY COORDINATOR:	Prof MD Umar, Prof Raja Shakarappa
21	PROGRAMME PROPOSED BY:	Dr P Rathnakumar
22	ANY OTHER RELEVANT MATTER:	------
23	ESTIMATED BUDGET IN RS:	50,000/-

This is to the Honorable CEO of NIT for kind approval

EVENT COORDINATOR

HOD

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology (NIT)
RAICHUR.

PRINCIPAL

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

APPROVED/NOTAPPROVED

INSTITUTE OF TECHNOLOGY * NAVODAYA * RAICHUR

Department of Mechanical Engineering

Tel: +91 8532-200115, 240334, Fax: +91-8532-240054
E-Mail:hodmech.nit@navodaya.edu.in

Chief Patron
Shri. S. R. Reddy, Hon'ble Chairman,
Navodaya Education Trust (NET®),
Raichur.

Patron
Shri. N. Bharath Reddy., Hon'ble Trustee,
Navodaya Education Trust (NET®),
Raichur.

Chief Convener
Dr. Shiva Prakash C.K
Dean/Principal,
N.I.T Raichur

Convener
Dr.P Rathnakumar,
HOD/ ME, NIT. Raichur

Coordinator
Mr. Raja Shakarappa
Assistant Professor
Department of ME-NITR
Mr. Md Umar
Assistant Professor
Department of ME-NITR

Organizing Committee
Organizing Committee:

Mr.Faheem Akthar	Assistant Professor
Mr.Imran Basha	Assistant Professor
Mr.Ashok Malipatil	Assistant Professor
Mr. Pradeepkumar N	Assistant Professor
Mr. Rajashekar	Assistant Professor
Mr. Manjunath Basude	Assistant Professor
Mr. Ravi Kulkarni	Assistant Professor
Mr. Venkat Reddy	Assistant Professor

25.10.2016

Respected Sir / Madam,
Greetings from Navodaya Institute of Technology, Raichur…

The department of Mechanical Engineering is organizing a Five day FDP on **"ADVANCED RESEARCH METHODS AND ANALYTICS"** The attached brochure may kindly be circulated among the students and faculty members / colleagues.

The FDP aims to provide Concept development of the researchers and faculty to define research problem and framework.
Help faculty and research scholar to formulate a research proposal, Guide faculty and researchers in developing suitable research methodology and data analytical tools. To provide a hands-on experience of the models and tools used for conducting research in the different subject domains.

We are pleased to inform that the department of Mechanical Engineering, Navodaya Institute of Technology is organizing a Faculty Development Programme during 14th-18th November 2016.
In this context, we look forward your support and request you to encourage participation of professionals by circulating the attached brochure.

Thanking you,

Yours sincerely,

Dr.P Rathnakumar
Convener
Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology (NIT)
RAICHUR.

Navodaya Institute of Technology (NIT)
RAICHUR-584 103

INVITATION

Department of Mechanical Engineering

HODs & Faculties are invited to attend five days

FACULTYDEVELOPMENTPROGRAMME

On

"ADVANCED RESEARCH METHODS AND ANALYTICS"

From *14th-18th November 2016*

Venue: NIT, Auditorium

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology (NIT)
RAICHUR.

FIVE DAY FDP ON

"ADVANCED RESEARCH METHODS AND ANALYTICS"

Inaugural function Agenda

1. *Introduction*
2. *Invocation song*
3. *Welcome address by Dr.P. Rathnakumar -HOD/MECH*
5. *Introduction of Resource person*
6. *Chief Guest Address*
7. *Presidential address*
8. *FDP- Session - I*

Valedictory Function Agenda

1. *Introduction*
2. *Feedback - Faculty participants*
3. *Feedback by HOD*
5. *Feedback by Coordinator*
6. *Feedback by Resource person/Chief Guest*
7. *Certificate distribution*
8. *Presidential address*
9. *Vote of Thanks*

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology
RAICHUR.

13/11/16
PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA Institute of Technology RAICHUR

Department of Mechanical Engineering
Five day's Faculty development program on

"ADVANCED RESEARCH METHODS AND ANALYTICS"

14th-18th November 2016

Schedule

Date	Session-1 (09.30 am to 11.00am)		TEA TIME	Session-2 (11.15 am to 12.45pm)	LUNCH TIME	Session-2 (2.00 pm to 3.30pm)	TEA TIME	Session-3 (3.45pm to 5.15pm)
Monday (14.11.2016)	*Registration (09.00 Am to 10.00AM)*	*Dr. Anantachar (COAE rcr) Inauguration - and Key note address*		*Dr. Anantachar (COAE rcr) Review of Research Process, Formulating Objective. Model building from Review of Literature*		*Dr. Anantachar (COAE rcr) Formulating, Research, Questionnaire*		*Dr. Anantachar (COAE rcr) Sample Size Calculation Sampling, Techniques. Formulation of Hypothesis*
Tuesday (15.11.2016)	*Dr. Anantachar (COAE rcr) Preparation of Data files: Defining variables, Variable Types, Data Entry, Merging cases and variables*			*Dr. Anantachar (COAE rcr) Data Sorting ,Data Screening: Insert variables data editing, Missing values ,outliers ,Category Merging*		*Dr K V Prakash (COAE rcr) Hypothesis, testing –Assumptions of Normality*		*Dr K V Prakash (COAE rcr) Introduction to statistical toolkit*
Wednesday (16.11.2016)	*Dr. Anantachar (COAE rcr) F –Test ,One Way Anova .,Two Way –Anova, Post HocTest, Mann Whitney, with Case study*			*Dr. Anantachar (COAE rcr) Correlation analysis, partial correlation, non-parametric correlation With case study.*		*Dr K V Prakash (COAE rcr) Predictive Modeling: - Linear Regression , Bivariate and Multivariate Regression Models*		*Dr K V Prakash (COAE rcr) Heteroscedasticity.Autocorrelation and Multicollinearity. Logistic, Regression with Case Study*

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

NAVODAYA
Institute of Technology
RAICHUR

Department of Mechanical Engineering
Five day's Faculty development program on

"ADVANCED RESEARCH METHODS AND ANALYTICS"

14th-18th November 2016

Schedule

Date	Session-1 (09.30 am to 11.00am)	TEA TIME	Session-2 (11.15 am to 12.45pm)	LUNCH TIME	Session-2 (2.00 pm to 3.30pm)	TEA TIME	Session-3 (3.45pm to 5.15pm)
Thursday (17.11.2016)	*Dr. K.M.Manjunath (COAE rcr) Forecasting: - Time Series Analysis: ARMA / ARIMA Models for forecasting and Testing for Unit Roots Conditional Variance Models: ARCH / GARCH.*		*Dr. K.M.Manjunath (COAE rcr) Data Reduction Technique: -Factor Analysis with case study. Factor Analysis, Cluster Analysis: Hierarchical and Non-Hierarchical*		*Dr Ramesh B (COAE dharwad) Hands on Training. Drawings, Tolerancing. Case study presentation and calculations Exercises individuals and group*		*Dr Ramesh B (COAE dharwad) hands-on sessional free trial version of SPSS*
Friday (18.11.2016)	*Dr. K.M.Manjunath (COAE rcr) Research Paper writing ,Plagiarism, Copyright*		*Dr Ramesh B (COAE dharwad) indexing and Citation of Journals Referencing and Referencing styles.*		*Dr Ramesh B (COAE dharwad) hands-on sessional free trial version of SPSS*		*Valedictory Function*

PRINCIPAL 12/11/16
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology (NIT)
RAICHUR.

Dr. Anantachar.M
Professor & Head
Dept. of Farm and Machinery
College of Agricultural Engineering
University of Agricultural Sciences, Raichur

Education details

- BE Mechanical: Karnataka University Dharwad
- M.E: Agri Engg, Tamilnadu Agricultural University Coimbatore
- Ph.D: Mechanical Engineering Sciences, VTU Belagavi

Achievements:

- Served as a leader for getting Accreditation of College of Agricultural Engineering and successful to get accreditation from Indian Council of Agricultural Research. New Delhi
- Training the staff and Up gradation of laboratories for .Adoption of Vth dean committee recommendation for B. Tech Agriculture engineering at CAE Raichur.
- Served as Dean College of Agricultural Engineering Raichur for three years
- Served as Professor and Head Department of Farm Machinery and Power Engineering for 10 years
- Served as Senior Scientist and Scheme Head for 20 years for Govt of India (Indian Council of Agricultural Research, New Delhi) funded Research Project on Farm Implements and Machinery
- Established the GOI approved Farm Machinery Testing Centre at the cost of 235 Lakhs at College of Agricultural Engineering Raichur and 200 test reports were released with generations. 88/- lakhs funds testing center.
- Served as a leader for getting .ISO 9001-2015 to College of Agricultural Engineering, Raichur
- Designed and adopted the new Course Curriculum for Masters and Doctoral Programme in Farm power and machinery as per iv dean committee recommendation (National Team)
- Worked as a Pre review committee member for e-course development consortium GOI Programme
- served as pre review committee member for IARI sub center recognition at CIAE Bhopal GOI Committee

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology (NIT)
RAICHUR.

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

a. National/ International level	*05*
b. Institute/ University/ State level	*25*

1	Research Papers (International journal)	*24*
	Research papers (refereed journals)	*48*
	Popular articles	*98*
2	Books	04
	Book chapters	-
	Others (teaching manuals/extension bulletins/ e-resources etc.)	37
	Total	**211**

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology (NIT)
RAICHUR

12/11/16
PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

Dr. K.V.Prakash
Professor & Head
Department of Renewable Energy Resources
College of Agricultural Engineering
UAS Raichur

Education details

- B Tech Agri Engg - University of Agricultural Engineering GKVK Bangalore
- M.Tech: Agri Engg- College of Agri Engg UAS,Raichur
- Ph.D- College of Agri Engg UAS,Raichur

Presently Dr. K.V.Prakash is working as Professor & Head in Department of Renewable Energy Resources College of Agricultural Engineering UAS Raichur

He has successfully tested and implemented some modifications in farm equipments and submitted the report to University of Agricultural sciences Raichur.

He is presently guiding the research scholars under UAS Raichur in farm engineering and Renewable Energy resources.

He presented many papers in reputed Journals.

He published many articles in local newspapers which are very helpful to farmers.

12/4/16

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology (NIT)
RAICHUR.

12/11/16

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

Dr. Ramesh B
In charge Farm Machinery Testing Center
College of Agricultural Engineering
UAS Raichur

Education details

B Tech Agri Engg - University of Agricultural Engineering GKVK Bangalore
M.Tech: Agri Engg- College of Agri Engg UAS,Raichur
Ph.D- College of Agri Engg UAS,Raichur

Worked as assistant Professor in Farm Machinery Dept. COAE UAS Raichur
Served as testing engineer in Govt of INDIA approved Farm testing center
Presently working as Associate professor in Farm Machinery Department COAE UAS, Dharwad
He is handling Farm machine center in UAS Bijapur
He has published more than 20 Journal papers in reputed national and international journals
He is publishing articles in many national papers for benefit of farmers

12/11/16
Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology (NIT)
RAICHUR.

12/11/16
PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

Dr. K.M.Manjunath
In charge Farm Machinery Testing Center
College of Agricultural Engineering
UAS Raichur

Education details

- B Tech Agri Engg - University of Agricultural Engineering GKVK Bangalore
- M.Tech: Agri Engg- College of Agri Engg UAS,Raichur
- Ph.D- College of Agri Engg UAS,Raichur

Presently Dr. K.M.Manjunath is working as testing engineer in Government of INDIA approved Farm Machining center at UAS Raichur.
He has successfully tested and implemented some modifications in farm equipments and submitted the report to University of Agricultural sciences Raichur.
He is presently guiding the research scholars under UAS Raichur in farm engineering.
He presented many papers in reputed Journals.
He published many articles in local newspapers which are very helpful to farmers.

Head of the Department
Department of Mechanical Engineering
Navodaya Institute of Technology (NIT)
RAICHUR.

PRINCIPAL 21/11/16
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY * RAICHUR

NAVODAYA
Institute of Technology
RAICHUR

DEPARTMENT OF MECHANICAL ENGINEERING

SUMMARY OF FACULTY DEVELOPMENT PROGRAMME ON

"ADVANCED RESEARCH METHODS AND ANALYTICS"

14th-18th November 2016



Dr K V Prakash delivering a lecture

Five day's Faculty development program "ADVANCED RESEARCH METHODS AND ANALYTICS" was held from14th to 18th November 2016. Participants from various Colleges are successfully participated in this Program. Dr. P. Rathnakumar, Head of Mechanical Engineering Department welcomed the gathering. Dr. Shiva Prakash C.H.Principal& Dean gave a presidential address and facilitated the Chief Guests with mementos. The function was inaugurated and given brief description related to the Theoretical and analytical tools and skills for the academicians and researchers to get an insight of the Advanced research Methods and Analytical tools.

Further five days the resource persons delivered lecture on Conceptual clarity of the research process and methodology, Insights into different research designs and their implications, Familiarity with tools and techniques of management decision making approach, Discussions and Interaction by Experts and experience sharing with co-participants, from different research areas, The Programme focused on maximizing hands on development of practical skillsand data analytical tools.

Hands on training classes conducted in afternoon session to apply practical knowledge on Case studies.

Prof Raja Shakarappa,Assistant Professor,Prof Md Umar, Assistant Professor, Department of Mechanical Engineering Co-ordinator overviewed this FDP.

PRINCIPAL 15/11/16
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology (NIT)
RAICHUR.

2016-17/FDP-01

D:10.11.2016

From
Dr.P. Rathnakumar,
Professor and Head,
Dept. of Mechanical Engineering
Navodaya Institute of Technology,
Raichur-584103

To
The Principal,
Navodaya Institute of Technology,
Raichur-584103

Sir

Subject: Req. for Auditorium hall to conduct FDP - Reg

We have proposed to conduct One Week FDP on the topic **"Advanced Research Methods and Analytics"** from 14th-18th November 2016. For this Purpose, we need auditorium from 14th-18th November 2016 Time-10.00AM-5.30PM.

Thanking You

Yours faithfully

[Dr.P. Rathnakumar]

HOD/ME

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology (NIT)
RAICHUR.

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

NET's

NAVODAYA INSTITUTE OF TECHNOLOGY

DEPARTMENT OF MECHANICAL ENGINEERING

Evaluation Form

FDP Title: Advanced Research Methods & Analytics

Date: 14-11-2016 to 18-11-2016 Time: 10am to 4pm

Please complete the following evaluation for the FDP you attended. Your feedback will help us evaluate the effectiveness of this program and allow us to make improvements in the future program. **Your feedback is important for planning future programming and services we offer.**

Thank you.

4=Strongly Agree 3=Agree 2=Disagree 1=Strongly Disagree

	Circle your response			
1. The course met my expectations.	4	3	2	1
2. The content was helpful.	4	3	2	1
3. The level of the course was appropriate.	4	3	2	1
5. The speaker had a good understanding of the topics.	4	3	2	1
6. The handouts were helpful.	4	3	2	1
7. The course was worth my time.	4	3	2	1
8. The length of the course was appropriate.	4	3	2	1
9. I recommend that the course be repeated for other students.	4	3	2	1

What were the most useful aspects of the course?

What changes should be made to enhance/improve this program?

Additional comments:

Name of the Faculty: Faheem Akthar Signature

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

NET's

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Department of Mechanical Engineering

Faculty Development Programme (FDP) on

"ADVANCED RESEARCH METHODS AND ANALYTICS"

From 14th November to 18th March 2016

S.No.	Name of the Faculty	Designation & Dept.	Signature
1.	Mr. Vinayak. M	Asst. prof ME	
2.	Mr. Mallikarjun B	Asst prof/ME	
3.	Mr. Faheem Akhtar	Asst Prof/ME	
4.	Mr. MD UMAR	Asst - Prof/ME	
5.	Mr. Zeeshan Ali:	Asst. Prof/ME	
6.	Mr. Ravi Kulkarni	Asst. prof/ME	
7.	Mr. Imran Basha	Asst Prof/ME	
8.	Mr. Rajashekarappa	Asst. prof/ME	
9.	Dr. Anantachar	Asst Prof/ME	
10.	Mr. Srikanth G Kulkarni	AP. ME	
11.	Abdul Aziz	M. Tech/TPE	
12.	Mohd. Adil	Asst Prof/CV	
13.	Bharath K	Asst. Prof/CV	
14.	Rajmohan. B	Asst Prof/CV	
15.	Asha Desai	Asst. Prof/CV	
16.			
17.			
18.			
19.			
20.			

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

HOD & Convener
Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology (NIT)
RAICHUR.

NAVODAYA
Institute of Technology
RAICHUR

DEPARTMENT OF MECHANICAL ENGINEERING

CERTIFICATE

This is to certify that Mr./~~Ms./Dr.~~ FAHEEM AKTHAR of NIT, RAICHUR has Attended Five day FDP "ADVANCED RESEARCH METHODS AND ANALYTICS" Conducted at Navodaya Institute of Technology, Raichur on 14TH-18TH NOVEMBER 2016.

Coordinator

HOD

Principal

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

NET GROUP OF EDUCATIONAL INSTITUTIONS: RAICHUR
NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

FDP/GUEST LECTURE/SEMINR/ADD-ON COURSE /CONFERENCE/SHORT TERM TRAINING PROGRAMME/ANY OTHER PROPOSAL

Date: 02-04-2018

1	**ACADEMIC YEAR**	2017-18
2	**TYPE OF THE PROGRAMME PROPOSED**	FACULTY DEVELOPMENT PROGRAMME
3	**TITLE OF THE PROGRAMME:**	**"Advancement in Composite Materials"**
4	**BROAD OBJECTIVE OF THE PROGRAMME:**	Every industry is now vying with each other to make the best use of composites. One can now notice the application of composites in many disciplines such as Civil, Mechanical, aerospace, automotive, marine, medical Electronics & telecommunications, sports goods to space vehicles. Hence based on the above concept note, the participant should be able to understand and apply state of the art knowledge of different composite materials in Structural Engineering applications.
5	**NAME OF THE INSTITUTION**	NIT-RAICHUR
6	**NAME OF THE DEPARTMENT(S)**	MECHANICAL ENGINEERING
7	**BENEFICIARIES**	Faculties, Post Graduates students, Research Scholars.
8	**ELIGIBILITY CRITERIA SET:**	Faculties, Post Graduates students, Research Scholars from Civil and Mechanical engineering
9	**PROPOSED DATE OF PROGRAM**	23rd April-27th April 2018.
10	**NUMBER OF DAYS REQUIRED TO ORGANISE THE PROGRAMME**	05 DAYS
11	**PROPOSED DURATION OF THE PROGRAMME**	30 Hours

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

12	**TOTAL NO. OF PERIODS REQUIRED FOR THE PROGRAMME**	30Periods
13	**NO. OF PERIODS REQUIRED PER DAY**	6 Periods
14	**LOCATION DETAILS:**	NETs NIT Auditorium
15	**MINIMUM NO OF PARTICIPANTS:**	40
16	**DETAILS OF SPONSORERS:**	NIT Management
17	**DETAILS OF RESOURCE(S) PERSONS:**	**Dr.Somashekar S.Hiremath** Associate Professor, Department of Mechanical Engineering IIT, Chennai. **Dr. Gujjala Raghavendra** Assistant Professor National Institute of Technology, Warangal **Dr. S. Mohamed Iqbal** Department of Mechanical Engineering Rangarajan Dr.S Sagunthala R&D Institute of Science and Technology, Chennai
18	**REGISTRATION FEE WITH CERTIFICATION:**	Academicians and Research Scholar : Rs. 500/- Faculties : Rs. 700/- Industrial Employees : Rs 1000/-
19	**REGISTRATION FEE WITHOUT CERTIFICATION:**	----------
20	**EVENT ORGANISERS (STUDENTS):**	Zeeshan Ali, Asma Begum, Manjunath.
21	**FACULTY COORDINATOR:**	Mr. Ravi Kulkarni/MECH/NITR Mr. Srikar G Kulkarni/MECH/ NITR
22	**PROGRAMME PROPOSED BY:**	Department Of Mechanical Engineering
23	**ANY OTHER RELEVANT MATTER:**	------
24	**ESTIMATED BUDGET IN RS:**	50,000/-



This is to the Honorable CEO of NIT for kind approval:

EVENT COORDINATOR

HOD

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology (NIT)
RAICHUR.

PRINCIPAL

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

APPROVED/NOTAPPROVED

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

NAVODAYA

NET's
NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

Department of Mechanical Engineering

Five day's Faculty development program on
"Advancement in Composite Materials"

23rd April-27th April 2018.

REGISTRATION FORM

Name : _____

Branch : _____

Designation : _____

College Name & Address : _____

_____

Phone/ Mobile : _____

E-mail : _____

Registration Fee Details

DD No. : _____ Date: _____

Bank : _____

Accommodation: YES ☐ NO ☐

DECLARATION: This is to certify that the above member is bonafide Staff of this institution/ Organization.

Signature of the Participant

Sign. Of HOD/Principal/ Head of Organization (with seal)

DD to be drawn in favor of "PRINCIPAL, NIT Raichur" payable at SBI, NET Campus, Raichur.

Chief Patron:
Sri S. R. Reddy,
Hon'ble Chairman, NET® Raichur

Patron:
Sri N. Bharath Reddy,
Trustee, NET® Raichur

Chief Convener:
Dr. M.V.MalliKarjun
Principal,
N.I.T Raichur

Convener:
Dr.P Rathnakumar
Professor and Head
Department of ME-NITR

Coordinator:
Prof.**Ravi Kulkarni**
Assistant Professor
Department of ME-NITR
Prof.**Srikar G Kulkarni**
Assistant Professor
Department of ME-NITR

Organizing Committee:

Dr.Anantachar	Professor
Prof. N Pradeep Hay	Assistant professor
Prof. Geetha V	Assistant Professor
Prof.Priyankar D	Assistant Professor
Prof.Zeeshan Ali	Assistant Professor
Prof.Md. Umar	Assistant Professor
Prof.Vinayaka Manur	Assistant Professor
Prof.Mallikarjun	Assistant Professor
Prof.Faheem Akthar	Assistant Professor
Prof.Imran Basha	Assistant Professor
Prof.Rajashakarappa	Assistant Professor

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Five day's
FACULTY DEVELPOMENT PROGRAM
"ADVANCEMENT IN COMPOSITE MATERIALS"

23rd April-27th April 2018.



Organized by
Department of Mechanical Engineering
Navodaya Nagar, Raichur-584 103,
Karnataka.

Tel:08532-240115,240115
Mobile No: +91-7337764346
: +91-9980339850

E-mail:- hodmech.nit@navodaya.edu.in
ravikulkarni@navodaya.edu.in

ABOUT THE INSTITUTE:

Navodaya Institute of Technology (NIT), Raichur, established in 2008, is affiliated to Visveswaraya Technological University (VTU), Belagavi and is approved by All India Council for Technical Education (AICTE), New Delhi. The guiding spirit and the driving force for all the progress and achievements of the institution is our most respected Chairman **Sri. S. R. Reddy** of Navodaya Education Trust®, Raichur. NIT focuses on promoting quality technical education in North Karnataka region in general and Hyderabad Karnataka Region in particular. The education is of paramount importance for socioeconomic development. The Institute focuses on integral scientific &Technical training with value based education to enable overall development of aspirants.

NIT-R is all-inclusive campus. Administrative Block, Library Block, Separate and Independent Academic Blocks for each Department, Power back up, Hostels for Boys and Girls, Canteen, Bank, ATM, Campus Temple, Gym and Playground all housed in a vast campus of about 30 acres land surrounded by hillocks and lake.

ABOUT THE DEPARTMENT:

The department of Mechanical Engineering is established in the year 2010 and started its humble share & contribution of the Mechanical Engineering graduates for national needs. The department offers BE program in Mechanical engineering with annual intake 120 and subsequently introduced M.Tech. (Thermal power Engineering) program in 2014. The Department has excellent lab facilities with state of the art equipment and has exclusive Computer Centers equipped with 30 Core-i3 systems, one NT server including number of Mechanical engineering related software packages. The department is enriched with highly qualified, experienced and committed faculty members and faculty are from reputed institutes like IITs, NITs and other prestigious universities. The Department gives importance not only to academic education but also ethical values and personality development of students by practicing a close mentoring system on regular basis. The department strives hard to impart quality education and to keep the students on the competitive edge of the present-day technology and marching towards University ranks also, the department is actively involved in placement of the students through ON/OFF Campus Interviews.

VISION:

"To become a center of excellence in Mechanical Engineering, producing innovative and creative mechanical engineers to meet the global challenges"

MISSION:

"To provide a platform to the students towards attaining quality education in Mechanical Engineering. To educate students about professional & ethical responsibilities and train them to build leadership and entrepreneurship qualities for their career development. To create opportunities and guide students in acquiring career oriented jobs in the field of Mechanical Engineering."

ABOUT FDP

Every industry is now vying with each other for make the best use of composites. One can now notice the application of composites in many disciplines such as Civil, Mechanical, aerospace, automotive, marine, medical Electronics & telecommunications, sports goods to space vehicles.

Hence based on the above concept note, the participant should be able to understand and apply state of the art knowledge of different composite materials in Structural Engineering applications.

RESOURCE PERSONS:

Resource persons are from reputed Universities. Ph.D Scholars Experts in the subject.

IMPORTANT DATES:

Last date for Registration	: **15/04/2018**
Participants for conformation via E-mail	: **20/04/2018**

REGISTRATION FEES:

Students and Research Scholar	: **Rs. 300/-**
Faculties	: **Rs. 500/-**
Industrial Employees	: **Rs. 700/-**

The Demand Draft drawn in favour of "**Principal, NIT Raichur**" payable at **NIT Campus Raichur.**

ACCOMODATION:

For the period of the workshop, the participants from the AICTE approved institutions will be provided accommodation in the student's hostel of Navodaya Institute of Technology, Raichur. Hostel Accommodation will be arranged based on their request.

How to reach Raichur:

Raichur is well connected by Road and Railway to all nearby and important cities like Bengaluru, Hyderabad, Chennai, Mumbai, Tirupathi, Kalaburgi, Hubballi, and Bellary and so on.



NET's
NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

Tel : +91 8532-200115, 240334, Fax : +91-8532-240054
E-Mail :hodmech.nit@navodaya.edu.in

CIRCULAR

Chief Patron
Shri. S. R. Reddy, Hon'ble Chairman,
Navodaya Education Trust (NET®),
Raichur.

Patron
Shri. N. Bharath Reddy., Hon'ble Trustee,
Navodaya Education Trust (NET®),
Raichur.

Chief Convener
Dr.M.V. Mallikarjun
Principal, NIT, Raichur

Convener
Dr. Rathna Kumar,
HOD/ ME, NIT. Raichur

Coordinator
1. Prof. Ravi Kulkarni, ME, NIT.
   Contact no. : +91 – 9980339850
2. Prof. Srikar G Kulkarni, ME, NIT.
   Contact no. : +91 – 9844929937

Organizing Committee

1. Dr. Anantachar
2. Prof. Imran Basha
3. Prof. Mallikarjun
4. Prof. Manjunath B
5. Prof. Venkat reddy
6. Prof.Raja Shakarappa
7. Prof.Md Umar
8. Prof.Faheem Akther
9. Prof.Vinayak manur
10. Prof.Krishna Nalagi
11. Prof.Ramesh J

05.04.2018

Respected Sir / Madam,
Greetings from Navodaya Institute of Technology, Raichur…

The department of Mechanical Engineering is organizing a Three day FDP on **"Advancement in Composite Materials"** The attached brochure may kindly be circulated among the students and faculty members / colleagues.

Every industry is now vying with each other to make the best use of composites. One can now notice the application of composites in many disciplines such as Civil, Mechanical, aerospace, automotive, marine, medical Electronics & telecommunications, sports goods to space vehicles. Hence based on the above concept note, the participant should be able to understand and apply state of the art knowledge of different composite materials in Structural Engineering applications.

We are pleased to inform that the department of Mechanical Engineering, Navodaya Institute of Technology is organizing a 5 days Faculty Development Programme during 23rd April-27th April 2018.

In this context, we look forward your support and request you to encourage participation of professionals by circulating the attached brochure.

Thanking you,
Yours sincerely,

Dr. Rathna Kumar
Convener

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology
RAICHUR.

PRINCIPAL
Navodaya Institute of Technology
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

NET's

NAVODAYA INSTITUTE OF TECHNOLOGY

BIJENGERA ROAD, RAICHUR - 584103

INVITATION

Department of Mechanical Engineering

HODs & Faculties are invited to attend five days

FACULTY DEVELOPMENT PROGRAMME

On

"Advancement in Composite Materials"

From 23rd April To 27th April 2018

Venue: NIT, Auditorium

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology (NIT)
RAICHUR.

22/4/18
PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

Five day FDP on

"Advancement in Composite Materials"

Inaugural function Agenda

1. *Introduction*
2. *Invocation song*
3. *Welcome address by Dr.P. Rathnakumar -HOD/MECH*
5. *Introduction of Resource person*
6. *Chief Guest Address*
7. *Presidential address*
8. *FDP- Session - I*

Valedictory Function Agenda

1. *Introduction*
2. *Feedback - Faculty participants*
3. *Feedback by HOD*
5. *Feedback by Coordinator*
6. *Feedback by Resource person/Chief Guest*
7. *Certificate distribution*
8. *Presidential address*
9. *Vote of Thanks*

PRINCIPAL 22/4/18
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology (NIT)
Raichur

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

NAVODAYA
Institute of Technology
RAICHUR

Department of Mechanical Engineering

Five day's Faculty development program on

"Advancement in Composite Materials"

23rd April-27th April 2018

Schedule

Date	Session-1 (09.30 am to 11.00am)		TEA TIME	Session-2 (11.15 am to 12.45pm)	LUNCH TIME	Session-2 (2.00 pm to 3.30pm)	TEA TIME	Session-3 (3.45pm to 5.15pm)
Monday (23.04.2018)	*Registration (09.00 Am to 10.00AM)*	*Dr.Somashekar S.Hiremath* Inauguration and Key note address		*Dr.Somashekar S.Hiremath* Introduction to composites		*Dr.Somashekar S.Hiremath* Fabrication of Metal Matrix Composites		*Dr.Somashekar S.Hiremath* Testing of Metal Matrix Composites
Tuesday (24.04.2018)	*Dr. Gujjala Raghavendra* Fabrication of Fibre Reinforced Composites			*Dr. Gujjala Raghavendra* Testing of Fibre Reinforced Composites		*Dr. Gujjala Raghavendra* Fabrication of Ceramic Matrix Composites		*Dr.S. Mohamad Iqbal* Testing of Ceramic Matrix Composites
Thursday (25.04.2018)	*Dr.Somashekar S.Hiremath* Machining of composites			*Dr.Somashekar S.Hiremath* Application of composites		*Dr.Somashekar S.Hiremath* Microstructure study of composites		*Dr.Somashekar S.Hiremath* SMART COMPOSITES

NAVODAYA INSTITUTE OF TECHNOLOGY • RAICHUR

NAVODAYA
Institute of Technology
RAICHUR

Department of Mechanical Engineering
Five day's Faculty development program on
"Advancement in Composite Materials"
23rd April-27th April 2018
Schedule

Date	*Session-1* *(09.30 am to 11.00am)*	TEA TIME	*Session-2* *(11.15 am to 12.45pm)*	LUNCH TIME	*Session-2* *(2.00 pm to 3.30pm)*	TEA TIME	*Session-3* *(3.45pm to 5.15pm)*
Wednesday (26.04.2018)	***Dr. Gujjala Raghavendra*** Polymer Composite Fabrication and Properties		***Dr. Gujjala Raghavendra*** Polymer Processing Techniques for Composites		***Dr.S. Mohamed Iqbal*** Hands on Training, Fabrication of polymer matrix composites		***Dr.S. Mohamed Iqbal*** Hands on Training,Fabrication of metal matrix composites
Friday (27.04.2018)	***Dr.S. Mohamed Iqbal*** Polymer Nanocomposites		***Dr.S. Mohamed Iqbal*** Polymer composite characterizations		***Dr.S. Mohamed Iqbal*** Hands on Training,Fabrication of Ceramics matrix composites		***Valedictory Function***

INSTITUTE OF TECHNOLOGY RAICHUR NAVODAYA

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology (NIT)
RAICHUR.

22/4/18
PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Dr. S Somashekar Hiremath

Associate Professor
Indian Institute of Technology, Madras

Dr. Somashekhar received the Doctoral Degree in 2004 from Indian Institute of Technology Madras, Chennai, Tamil Nadu, India. He has published more than 150 papers papers in National and International level. He has delivered more than 75 Invited talks on various topics of his research at Engineering Colleges, Universities, Research Centers, Industries, and Conferences. Currently he is handling many Consultancy and Sponsored Projects of various industries and R&D institutions of Defense, Government of India.
His current research areas are Mechatronic System Design-System Simulation and Modeling, Robotics, Finite Element Modeling – basically Fluid Structure Interactions, Micromachining, Advanced machining processes – basically Hybrid Processes and Fluid Power Systems.

Awards & Honors

Innovative Student Project Award 2005 – Best Ph. D. Thesis of the year 2004-05 in Mechanical Engineering Department from Indian National Academy of Engineering (INAE), New Delhi.
Prof. K. Arum gam National Award for Innovative Research Work in Engineering and Technology (for Faculty of Degree Level Technical Institution) from Indian Society for Technical Education (ISTE), New Delhi.
Certificate of Achievement for Guiding Best MS Thesis in Mechanical Engineering from Indian National Academy of Engineering (INAE), New Delhi
Certificate of Achievement for Guiding Best MS Thesis in Mechanical Engineering from Indian Society for Technical Education (ISTE), New Delhi.

Selected Publications

R. Ramakrishnan, Somashekhar S. Hiremath, M. Singaperumal "Design strategy for improving the energy efficiency in series hydraulic/electric synergy system", Energy 67 (2014), pp. 422-434.

Lijo Paul Somashekhar S. Hiremath and Jinka Ranganayakulu "Experimental investigation and parametric analysis of electro chemical discharge machining", Int. J. Manufacturing Technology and Management, Vol. 28, Nos. 1/2/3, pp. 57-79, 2014.

Ranjeet Kumar Sahu, Somashekhar S. Hiremath, Manivannan P. V. and M. Singaperumal, "Generation and Characterization of Copper Nanoparticles Using Micro-Electrical Discharge Machining" , LMMP #872263, VOL 29, ISS 4, Taylor & Francis Group, LLC, Materials and Manufacturing Processes, 29: 1–10, 2014.

22/4/18

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

22/04

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology (NIT)
RAICHUR.

INSTITUTE OF TECHNOLOGY RAICHUR

Dr. Gujjala Raghavendra

Assistant Professor

Department of Mechanical Engineering

National Institute of Technology, Warangal - 506004, Telangana, INDIA

Ph. D.	Mechanical Engineering	NIT Rourkela
M. Tech.	Mechanical Engineering	NIT Rourkela
B. Tech.	Mechanical Engineering	Bapatla Engineering college

Dr. Gujjala Raghavendra proudest achievements are - recently DST awarded Early carrier research award 2016 of worth 38lakhs. And more than 6 SCI journals and visited Europe for a conference visit

Research and Development Activities

Major Area of Research: Composites, Nano Materials

Research Interests: Nano Materials

Current Research Focus: Nano Composites

- Dr. Gujjala Raghavendra current R&D is in the field of nano materials and composites he is working as assistant professor in NIT Warangal and having project from DST of work 38lakhs. In last three years he has published more than 10 SCI journals and 10 International conference journals.
- Recently DST awarded Early carrier research award 2016.

22/4/18

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

INSTITUTE OF TECHNOLOGY RAICHUR

Dr. S. Mohamed Iqbal M.E., Ph.D., *(Mechanical)*

Associate Professor in the Department of Mechanical Engineering at Vel Tech Rangarajan Dr. Sagunthala R&D Institute of Science and Technology, Chennai

Ph. D.	**Thermal Engineering (Mechanical Engineering)** Anna University, Chennai.600025, Tamil Nadu, India **Thesis:** Study of Heat Transfer Characteristics On Forced Convection System With Nanofluid **Supervisor:** Dr.C.Sundarraj, Professor (Rtd.)	(2018)
M.E.	**Master of Engineering– Thermal Power** Annamalai University, Annamalai Nagar. - 601802, Tamil Nadu, India **Thesis:** Experimental Study on fouling factor with 8.31 CGPA	(2005)
B.E.	**Bachelor of Engineering– Mechanical Engineering** Annamalai University, Annamalai Nagar. - 601802, Tamil Nadu, India **Thesis:** Performance Analysis On Engine Emission By Using EGR And Catalytic Converter With 67.5%	(2002)

Academic Experience

- Presently – **Associate Professor** in the Department of Mechanical Engineering at Vel Tech Rangarajan Dr. Sagunthala R&D Institute of Science and Technology, Chennai since Jan 2015 to till date.
- **Assistant Professor** in the Department of Mechanical Engineering at Sree Sastha College of College of Engineering , Chennai from July 2011 to Oct 2014.
- **Lecturer,** Department of Mechanical Engineering, DMI College of Engineering, Chennai-123, from Jan 2010 to May 2011.
- **Lecturer,** Department of Mechanical Engineering, Aringnar Anna Institute of Science and Technology, Chennai from Jan 2006 to Dec 2009.

Industrial Experience

- **Technical Assistant**, Anbu Engineering Contractors in 108 MW Combined Cycle, Thirumakottai (K) Gas Turbine Power Station, TNEB, Mannargudi. From Jun. 2002 to Sep.2003.

Journal Publications

1. **S. Mohamed Iqbal,** K. Karthik, Jee Joe Michael, 2018, "Performance analysis on improved efficiency in a hybrid solar still and solar heater", International Journal of Ambient Energy, DOI: 10.1080/01430750.2018.1517685, Taylor & Francis Publications.
2. D.Yogaraj, **S. Mohamed Iqbal,** R. Gokulakrishna, Meikandan Megaraj, "Performance Test and Emission Characteristics on Diesel Engine with Alternate Fuel Blends and Nano Additives" International Journal of Ambient Energy, 2018,



3. Jee Joe Michael, **S.Mohamed Iqbal**, S. Iniyan, Ranko Goic, 2018, "Enhanced electrical performance in a solar photovoltaic module using V-trough concentrators", Energy, Vol. 148, pp. 605 – 613, Elsevier Publications. [Thomson Reuters Impact Factor: 5.182]
4. P. Rathnakumar, **S. Mohamed Iqbal**, Jee Joe Michael, S. Suresh, 2018, "Study on performance enhancement factors in turbulent flow of CNT/water nanofluid through a tube fitted with helical screw louvered rod inserts", Chemical Engineering & Processing: Process Intensification, Vol. 127, pp. 103 – 110, Elsevier Publications. [Thomson Reuters Impact Factor: 3.096]

23/4/18

PRINCIPAL
Navodaya Institute of Technology (IIIT)
RAICHUR-584 103

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology (IIIT)
RAICHUR.

NAVODAYA INSTITUTE OF TECHNOLOGY • RAICHUR

DEPARTMENT OF MECHANICAL ENGINEERING

SUMMARY OF FACULTY DEVELOPMENT PROGRAMME ON

"Advancement in Composite Materials"

Theoretical session



Practical session



NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Department of Mechanical Engineering, Navodaya Institute of Technology Raichurorganized a **5vDays Faculty Development Programme (FDP)** on **Composite Materials** from *23rd-27th April.* .

The Keynote Speaker was **Dr. Somashekar S Hiremath Associate Professor, IIT Madras.** He delivered his talk on **Nano-Technology and Nano-Materials. Dr. Gujjala raghvendra, Assistant Professor, NIT, warngal** delivered his talk on **Nano-Technology and Nano-Composites: Fabrication, Characterization and Application** and **Dr. S. Mohamed IqbalassociateProfessor, delivered** his talk on **Design of Experiments in Composite Materials: An Overview (Case Studies)** also delivered their Talks and shared their Experience with the Participants during their Sessions in this Program.

Hands on training on Fabrication of various composites was demonstrated to participants by resource persons.

28/4/18

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

28/04/18

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology (NIT)
RAICHUR.

INSTITUTE OF TECHNOLOGY RAICHUR

2017-18/FDP/01

Date: 20.04.2018

From
Dr. Rathna Kumar,
Professor and Head,
Dept. Of ME,
Navodaya Institute of Technology,
Raichur-584103.

To
The Principal,
Navodaya Institute of Technology,
Raichur-584103

Sir

Subject: Req. for Auditorium hall to conduct FDP - Reg

We have proposed to conduct the FDP on the topic **"Advancement in Composite Materials"** from 23rd April to 27th April 2018. For this Purpose, we need auditorium from 23rd April to 27th April 2018-Time-10.00AM-5.30PM.

Thanking You

20/04/18

Yours faithfully

[Dr. Rathna Kumar]

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology (NIT)
Raichur

INSTITUTE OF TECHNOLOGY RAICHUR NAVODAYA

2017-18/FDP-02

From

19.04.2018

Dr. Rathna Kumar,
Hod/ME.,
NIT, Raichur

To

The Principal,
NIT, Raichur.

Sir,

Sub: Requisition for Accommodation and Food -Reg

I bring to your kind notice that we are conducting the Five days FDP on **"Advancement in Composite Materials"** in our department on from 23.04.2018 to 27.04.2018. For that we want to arrange the accommodation and food for the Resource Persons in the above days. So kindly do the needful.

Resource Person Details,

Dr.Somashekar S.Hiremath

Associate Professor,
Department of Mechanical Engineering
IIT, Chennai.

Dr. Gujjala Raghavendra

Assistant Professor
National Institute of Technology,
Warangal

Dr. S. Mohamed Iqbal

Department of Mechanical Engineering
Rangarajan Dr.S Sagunthala R&D Institute of Science and Technology,
Chennai

Thanking you,

19/4/18

Dr.Rathna Kumar

HOD/ME

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology
RAICHUR.

19/4/18

Principal
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

INSTITUTE OF TECHNOLOGY
RAICHUR

NAVODAYA
RAICHUR

DEPARTMENT OF MECHANICAL ENGINEERING

Faculty Development Programme (FDP) on

"Advancement in Composite Materials"

From 23.04.2018 to 28.04.2018

S.No.	Name of the Faculty	Designation & Dept.	Signature
1.	Faheem Ahmed	AP / ME	
2.	Ravi Kultam	Asst. Prof / MC	
3.	Srikar G Kulkarni	Asst. Prof / MC	
4.	Vinayak Mane	Asst. Prof / MC	
5.	Venkat Reddy	AP / ME	
6.	Manjunath Bijade	AP / Mech	
7.	MD. Umar	AP / Mech	
8.	Rajashekarappa	Asst Prof / ME	
9.	Imran Bagali	AP / Mech	
10.	Dr. Amtekar	Asst Mech	
11.	Rejmohan. B	Asst / ME	
12.	Abhijit Deka	AP / CV	
13.	Adil	AP (CV)	
14.			
15.			
16.			
17.			
18.			
19.			
20.			

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

INSTITUTE OF TECHNOLOGY RAICHUR

28/04

HOD & Convener

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology (NIT)
RAICHUR

DEPARTMENT OF MECHANICAL ENGINEERING

Faculty Development Programme (FDP) on

"Advancement in Composite Materials"

From 23.04.2018 to 28.04.2018

Evaluation Form

FDP Title: Advancement in Composite Materials

Date: 23-04-2018 to 28.04.2018 Time: 10am - 4pm

Please complete the following evaluation for the FDP you attended. Your feedback will help us evaluate the effectiveness of this program and allow us to make improvements in the future program. **Your feedback is important for planning future programming and services we offer.**

Thank you.

4=Strongly Agree 3=Agree 2=Disagree 1=Strongly Disagree

	Circle your response			
1. The course met my expectations.	4	3	2	1
2. The content was helpful.	4	3	2	1
3. The level of the course was appropriate.	4	3	2	1
5. The speaker had a good understanding of the topics.	4	3	2	1
6. The handouts were helpful.	4	3	2	1
7. The course was worth my time.	4	3	2	1
8. The length of the course was appropriate.	4	3	2	1
9. I recommend that the course be repeated for other students.	4	3	2	1

What were the most useful aspects of the course?

What changes should be made to enhance/improve this program?

Additional comments:

Name ofthe Faculty: Faheem Akthar

Signature

INSTITUTE OF TECHNOLOGY RAICHUR

NAVODAYA
Institute of Technology
RAICHUR

DEPARTMENT OF MECHANICAL ENGINEERING

CERTIFICATE

This is to certify that Mr./~~Ms.~~/~~Dr.~~ MALLIKARJUN of NIT, RAICHUR.

Has attended Five day FDP on *"Advancement in Composite Materials"* Conducted at Navodaya Institute of Technology, Raichur on 23-04-2018 to 27-04-2018.

Coordinator

HOD

Principal

INSTITUTE OF TECHNOLOGY RAICHUR

NET GROUP OF EDUCATIONAL INSTITUTIONS: RAICHUR
NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

FDP-GUEST LECTURE/SEMINR/ADD ON COURSE /CONFERENCE/SHORT TERM TRAINING PROGRAMME/ANY OTHER PROPOSAL

Date: 02-02-2019

1	**ACADEMIC YEAR**	2018-19
2	**TYPE OF THE PROGRAMME PROPOSED**	FACULTY DEVELOPMENT PROGRAMME
3	**TITLE OF THE PROGRAMME:**	**"ADVANCED TRAINING ON GEOMETRIC DIMENSIOINIG & TOLERANCES"**
4	**BROAD OBJECTIVE OF THE PROGRAMME:**	The FDP aims to provide opportunities to faculty members, research scholars and post graduate students to enrich their teaching skill and research in the field of engineering design and Geometric Dimensioning & Tolerances. The Programme also intends to develop the blue print reading, manufacturing & inspection approach of participants for with advanced industrial examples in the relevant field for inculcating learning values in students and guiding and monitoring their progress.
5	**NAME OF THE INSTITUTION**	NIT-RAICHUR
6	**NAME OF THE DEPARTMENT(S)**	MECHANICAL ENGINEERING
8	**PROPOSED DATE OF FDP CONDUCTING**	12th March – 16th March 2019
9	**TARGETED GROUP OF PARTICIPANTS**	Faculties, Post Graduates students, Research Scholars.
10	**BENEFICIARIES**	Faculties, Post Graduates students, Research Scholars from Mechanical Engineering.

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

11	**NUMBER OF DAYS REQUIRED TO ORGANISE THE PROGRAMME**	05 DAYS
12	**PROPOSED DURATION OF THE PROGRAMME**	30 Hours
13	**TOTAL NO. OF PERIODS REQUIRED FOR THE PROGRAMME**	30 Periods
14	**NO. OF PERIODS REQUIRED PER DAY**	6 Periods
15	**LOCATION DETAILS:**	NETs NIT Auditorium
16	**MINIMUM NO OF PARTICIPANTS:**	40
17	**DETAILS OF SPONSORERS:**	NIT Management
18	**DETAILS OF RESOURCE(S) PERSONS:**	Dr. Chandru J Assistant Professor Prathyusha Engineering College, Aranvoyal Kuppam, Tiruvallore (TN). Dr. A.Megalingam Associate Professor Bannari Amman Institute of Technology, Satyamanghalam, Erode (TN) Dr. D.Madan Associate Professor Sri Indu College of Engineering and Technology, Hyderabad,Telangana
19	**REGISTRATION FEE WITH CERTIFICATION:**	Academicians and Research Scholar : Rs. 500/- Faculties : Rs. 700/- Industrial Employees : Rs 1000/-
20	**REGISTRATION FEE WITHOUT CERTIFICATION:**	----------

INSTITUTE OF TECHNOLOGY RAICHUR NAVODAYA

21	**EVENT ORGANISERS (STUDENTS):**	Meer Ashfaqwali, Karuna M D, Syed Mudasser
22	**FACULTY COORDINATOR:**	Mr. Ravi Kulkarni/MECH/NITR Mr. Srikar G Kulkarni/MECH/ NITR
23	**PROGRAMME PROPOSED BY:**	Department Of Mechanical Engineering
24	**ANY OTHER RELEVANT MATTER:**	------
25	**ESTIMATED BUDGET IN RS:**	Rs 50000/-

This is to the Honorable CEO of NIT for kind approval:

PRINCIPAL 2/2/19

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

02-2-19

HOD

Department of Mechanical Engineering
Navodaya Institute of Technology (NIT)
RAICHUR.

EVENT COORDINATOR

APPROVED/NOTAPPROVED

INSTITUTE OF TECHNOLOGY * NAVODAYA * RAICHUR

NET's
NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

NAVODAYA

Department of Mechanical Engineering

Five day's Faculty development program on
"Advanced Training on Geometric Dimensioning & Tolerances"

12th March-16th March 2019

REGISTRATION FORM

Name : _____

Branch : _____

Designation : _____

College Name & Address : _____

_____

Phone/ Mobile: _____

E-mail : _____

Registration Fee Details

DD No. : _____ Date: _____

Bank : _____

Accommodation: YES ☐ NO ☐

DECLARATION: This is to certify that the above member is bonafide Staff of this institution/ Organization.

Signature of the Participant

Sign. Of HOD/Principal/ Head of Organization (with seal)

DD to be drawn in favor of "PRINCIPAL, NIT Raichur" payable at SBI, NET Campus, Raichur.

Chief Patron:
Sri S. R. Reddy,
Hon'ble Chairman, NET® Raichur

Patron:
Sri N. Bharath Reddy,
Trustee, NET® Raichur

Chief Convener:
Dr. M.V.MalliKarjun
Principal,
N.I.T Raichur

Convener:
Dr.P Rathnakumar
Professor and Head
Department of ME-NITR

Coordinator:
Prof.**Ravi Kulkarni**
Assistant Professor
Department of ME-NITR
Prof.**Srikar G Kulkarni**
Assistant Professor
Department of ME-NITR

Organizing Committee:

Dr.Anantachar	Professor
Prof. N Pradeep Hay	Assistant professor
Prof. Geetha V	Assistant Professor
Prof.Priyankar D	Assistant Professor
Prof.Zeeshan Ali	Assistant Professor
Prof.Md. Umar	Assistant Professor
Prof.Vinayaka Manur	Assistant Professor
Prof.Mallikarjun	Assistant Professor
Prof.Faheem Akthar	Assistant Professor
Prof.Imran Basha	Assistant Professor
Prof.Rajashakarappa	Assistant Professor

INSTITUTE OF TECHNOLOGY * NAVODAYA RAICHUR

Five day's
FACULTY DEVELPOMENT PROGRAM
"ADVANCED TRAINING ON GEOMETRIC DIMENSIONING & TOLERANCES"

12th March-16th March 2019.



Organized by
Department of Mechanical Engineering
Navodaya Nagar, Raichur-584 103,
Karnataka.

Tel:08532-240115,240115
Mobile No: +91-7337764346
: +91-9980339850

E-mail:- hodmech.nit@navodaya.edu.in
ravikulkarni@navodaya.edu.in

ABOUT THE INSTITUTE:

Navodaya Institute of Technology (NIT), Raichur, established in 2008, is affiliated to Visveswaraya Technological University (VTU), Belagavi and is approved by All India Council for Technical Education (AICTE), New Delhi. The guiding spirit and the driving force for all the progress and achievements of the institution is our most respected Chairman **Sri. S. R. Reddy** of Navodaya Education Trust®, Raichur. NIT focuses on promoting quality technical education in North Karnataka region in general and Hyderabad Karnataka Region in particular. The education is of paramount importance for socioeconomic development. The Institute focuses on integral scientific &Technical training with value based education to enable overall development of aspirants.

NIT-R is all-inclusive campus. Administrative Block, Library Block, Separate and Independent Academic Blocks for each Department, Power back up, Hostels for Boys and Girls, Canteen, Bank, ATM, Campus Temple, Gym and Playground all housed in a vast campus of about 30 acres land surrounded by hillocks and lake.

ABOUT THE DEPARTMENT:

The department of Mechanical Engineering is established in the year 2010 and started its humble share & contribution of the Mechanical Engineering graduates for national needs. The department offers BE program in Mechanical engineering with annual intake 120 and subsequently introduced M.Tech (Thermal power Engineering) program in 2014. The Department has excellent lab facilities with state of the art equipment and has exclusive Computer Centers equipped with 30 Core-i3 systems, one NT server including number of Mechanical engineering related software packages. The department is enriched with highly qualified, experienced and committed faculty members and faculty are from reputed institutes like IITs, NITs and other prestigious universities. The Department gives importance not only to academic education but also ethical values and personality development of students by practicing a close mentoring system on regular basis. The department strives hard to impart quality education and to keep the students on the competitive edge of the present-day technology and marching towards University ranks also, the department is actively involved in placement of the students through ON/OFF Campus Interviews.

VISION:

"To become a center of excellence in Mechanical Engineering, producing innovative and creative mechanical engineers to meet the global challenges"

MISSION:

"To provide a platform to the students towards attaining quality education in Mechanical Engineering. To educate students about professional & ethical responsibilities and train them to build leadership and entrepreneurship qualities for their career development. To create opportunities and guide students in acquiring career oriented jobs in the field of Mechanical Engineering."

ABOUT FDP

The FDP aims to provide opportunities to faculty members, research scholars and post graduate students to enrich their teaching skill and research in the field of engineering design and Geometric Dimensioning & Tolerances.

The Programme also intends to develop the blue print reading, manufacturing & inspection approach of participants for with advanced industrial examples in the relevant field for inculcating learning values in students and guiding and monitoring their progress.

RESOURCE PERSONS:

Resource persons are from reputed Universities. Ph.D Scholars Experts in the subject.

IMPORTANT DATES:

Last date for Registration	: 05/03/2019
Participants for conformation via E-mail	: 15/03/2019

REGISTRATION FEES:

Students and Research Scholar	: **Rs. 300/-**
Faculties	: **Rs. 500/-**
Industrial Employees	: **Rs. 700/-**

The Demand Draft drawn in favour of "**Principal, NIT Raichur**" payable at **NET Campus Raichur.**

ACCOMODATION:

For the period of the workshop, the participants from the AICTE approved institutions will be provided accommodation in the student's hostel of Navodaya Institute of Technology, Raichur. Hostel Accommodation will be arranged based on their request.

How to reach Raichur:

Raichur is well connected by Road and Railway to all nearby and important cities like Bengaluru, Hyderabad, Chennai, Mumbai, Tirupathi, Kalaburgi, Hubballi, and Bellary and so on.



NET's
NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

Tel : +91 8532-200115, 240334, Fax : +91-8532-240054
E-Mail : hodmech.nit@navodaya.edu.in

CIRCULAR

Chief Patron
Shri. S. R. Reddy, Hon'ble Chairman,
Navodaya Education Trust (NET®),
Raichur.

Patron
Shri. N. Bharath Reddy., Hon'ble Trustee,
Navodaya Education Trust (NET®),
Raichur.

Chief Convener
Dr.M.V Mallikarjun,
Principal, NIT, Raichur

Convener
Dr.P Rathnakumar,
HOD/ ME, NIT, Raichur

Coordinator
1. Prof.Ravi Kulkarni
   Contact no. : +91 – 9611064259
2. Prof. Srikar G Kulkarni, ME, NIT.
   Contact no. : +91 – 9844929937

Organizing Committee
1. Dr. Anantachar
2. Prof.Pradeep Ilay
3. Prof.Geeta V
4. Prof.Priyankar D
5. Prof.Zeeshan Ali
6. Prof.Md. Umar
7. Prof.Vinayaka Manur
8. Prof.Mallikarjun
9. Prof.Faheem Akthar
10. Prof.Imran Basha
11. Prof.Rajashakarappa

15.02.2019

Respected Sir / Madam,
Greetings from Navodaya Institute of Technology, Raichur…

The department of Mechanical Engineering is organizing a Three day FDP on **"ADVANCED TRAINING ON GEOMETRIC DIMENSIOINIG & TOLERANCES"** The attached brochure may kindly be circulated among the students and faculty members / colleagues.

The FDP aims to provide opportunities to faculty members, research scholars and post graduate students to enrich their teaching skill and research in the field of engineering design and Geometric Dimensioning & Tolerances.

The Programme also intends to develop the blue print reading, manufacturing & inspection approach of participants for with advanced industrial examples in the relevant field for inculcating learning values in students and guiding and monitoring their progress.

We are pleased to inform that the department of Mechanical Engineering, Navodaya Institute of Technology is organizing a Faculty Development Programme during 12th March-16th March 2019.
In this context, we look forward your support and request you to encourage participation of professionals by circulating the attached brochure.

Thanking you,
Yours sincerely,

Dr.P Rathnakumar
Convener

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology
RAICHUR

15/2/19
PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

INSTITUTE OF TECHNOLOGY * NAVODAYA * RAICHUR

NET's

NAVODAYA INSTITUTE OF TECHNOLOGY

NAVODAYA

BIJENGERA ROAD, RAICHUR - 584103

INVITATION

Department of Mechanical Engineering

HODs & Faculties are invited to attend five days

FACULTY DEVELOPMENT PROGRAMME

On

"ADVANCED TRAINING ON GEOMETRIC DIMENSIOINIG & TOLERANCES"

From ***12th March-16th March 2019***

Venue: NIT, Auditorium

12/3/19

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

12/3/19

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology (NIT)
RAICHUR.

INSTITUTE OF TECHNOLOGY RAICHUR NAVODAYA

Five day FDP on

"ADVANCED TRAINING ON GEOMETRIC DIMENSIOING & TOLERANCES"

Inaugural function Agenda

1. *Introduction*
2. *Invocation song*
3. *Welcome address by Dr.P.Rathnakumar -HOD/MECH*
5. *Introduction of Resource person*
6. *Chief Guest Address*
7. *Presidential address*
8. *FDP- Session - I*

Valedictory Function Agenda

1. *Introduction*
2. *Feedback - Faculty participants*
3. *Feedback by HOD*
5. *Feedback by Coordinator*
6. *Feedback by Resource person/Chief Guest*
7. *Certificate distribution*
8. *Presidential address*
9. *Vote of Thanks*

12/3/19

PRINCIPAL
Navodaya Institute of Technology (Regd.)
RAICHUR-584 103

12/03

Head of the Department
Department of Mechanical Engineering
Navodaya Institute of Technology (Regd.)
RAICHUR.

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

NAVODAYA

NET's
NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Department of Mechanical Engineering

Five day's Faculty development program on
"Advanced Training on Geometric Dimensioning & Tolerances"
12th March-16th March 2019

Schedule

Date	*Session-1* (09.30 am to 11.00am)		TEA TIME	*Session-2* (11.15 am to 12.45pm)	LUNCH TIME	*Session-2* (2.00 pm to 3.30pm)	TEA TIME	*Session-3* (3.45pm to 5.15pm)
Tuesday (12.03.2019)	*Registration* (09.00 Am to 10.00AM)	*Dr. Chandra* Inauguration and Key note address		*Dr. Chandra* Dimension types and applications Definitions GDT Professional Certification Exam The role of dimensions		*Dr. D.Madan* Position MMC,Tooling / Production Inspection/CMM (coordinate measuring machine)/Functional gage/Manual inspection		*Dr. D. Madan* Position zero tolerance Tooling / production/Inspection.
Wednesday (13.03.2019)	*Dr. D.Madan* Position MMC(Pitch cylinder rule/To MMC (maximum material condition) or not to MMC			*Dr. D.Madan* Inspection/CMM/Functional gage/ Open setup		*Dr. A.Megalingam* Non-functional datum feature Functional secondary size datum featureRFS/MMC/Shift/ Tertiary size datum feature		*Dr. A.Megalingam* Composite & Two Single Segments
Thursday (14.03.2019)	*Dr. Chandra* Application and implications of Functional Dimensioning and Tolerancing schemes How to apply GD&T functionally to parts and mating parts in assemblies,Composite tolerancing			*Dr. Chandra* Composite tolerancing, Understanding alternative strategies for tolerancing and managing variation		*Dr. Chandra* Pros, cons and trade-offs of the alternatives on downstream operations such as manufacturing and inspection, and cost implications of the above		*Dr. A.Megalingam* Understand and apply functional dimensioning and tolerancing

12/3/19
PRINCIPAL
Navodaya Institute of Technology (NIT),
RAICHUR-584 103

12/3/19
Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology (NIT)
RAICHUR

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

NAVODAYA

NET's
NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Department of Mechanical Engineering

Five day's Faculty development program on
"Advanced Training on Geometric Dimensioning & Tolerances"
12th March-16th March 2019

Date	*Session-1* (09.30 am to 11.00am)	TEA TIME	*Session-2* (11.15 am to 12.45pm)	LUNCH TIME	*Session-2* (2.00 pm to 3.30pm)	TEA TIME	*Session-3* (3.45pm to 5.15pm)
Friday (13.09.2019)	*Dr. D.Madan* Advantages of GD &T, Myths, Claim, parallel dimensioning, Material conditions MMC, LMC and External,RFS Worst case boundary Rule1 &2.Virtual condition, Simulated datum		*Dr. D.Madan* Consequences of not defining datum Datum Feature symbol Limits, freedom rotation, translational X,Y, Z axis 3-2-1 Rule- Point, Axis, Center plane		*Dr. A.Megalingam* Hands on Training, Drawings,Tolerancing Case study presentation and calculationsExercises individuals and group		*Dr. A.Megalingam* Hands on Training, Drawings, Tolerancing Case study presentation and calculations Exercises individuals and group
Saturday (14.03.2019)	*Dr. Chandra* Position - Position tolerance symbol, interpretations, methods of measurements, Calculations, Acceptance gauges, CMM measurements,		*Dr. Chandra* Concept, different type, Boundary concept, Inspection, Application. Datum element - point's line and area, Origin Symbol		*Dr. A.Megalingam* Hands on Training, Drawings, Tolerancing Case study presentation and calculations Exercises individuals and group		*Valedictory Function*

12/3/19
PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

12/3/19
Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology (NIT)
RAICHUR.

NAVODAYA INSTITUTE OF TECHNOLOGY * RAICHUR

Dr. Chandru J

Assistant Professor

PRATHYUSHA ENGINEERING COLLEGE, ARANVOYAL KUPPAM, TIRUVALLORE.

He has completed his Bachelor degree in Mechanical Engineering at Jaya Engineering College engineering in 2011 with first class. After he did his M.E in St.Peter's University and Ph.D from Annamalai University.

Attended **CENTRE FOR FACULTY DEVELOPMENT ANNA UNIVERSITY**, Chennai-**ENGINEERING THERMODYNAMICS** from 10-Jun-2013 to17-Jun-2013.

Attended **TWO DAY NATIONAL LEVEL SEMINAR ON POWER ELECTRONICS FOR RENEWABLE ENERGY SYSTEMS** at PITAM 4-Dec-2013 to 5-Dec-2013.

Attended **THREE DAY MISSION 10X PROGRAM** at PITAM 26-Feb-2014 to 28-Feb- 2014.

Attended **ONE DAY NATIONAL LEVEL SEMINAR ON ELECTRICAL SAFETY AND CONSERVATION** at PITAM 17-Jun-2014.

Attended **FACULTY DEVELOPMENT PROGRAM FOR ENGINEERING MECHANICS** at PITAM 15-Dec-2014 to 21-Dec-2014.

CONFERENCES

Presented a paper titled **"DESIGN AND STRESS ANALYSIS OF A CENTRIFUGAL PUMP IMPELLER BY USING FINITE ELEMENT METHOD"** in National Conference at St.Peter's University Chennai on 23-3-2013

Investigating the relative contribution of operational parameters on performance and emissions of a common-rail diesel engine using

Neural network" at International Journal of Innovative Research in Science, Engineering and Technology(*IJIRSET*)Vol.4,SpecialIssue-11,Sept-2014

"Thermal desalination using diesel engine exhaust waste heat — An experimental analysis" at International Journal of Engineering Research & **Technology (IJERT),** Volume 3, Issue 1, January 2015 (ISSN 2278-0181)." **Influence on the performance and emissions of an automotive Euro 5 diesel engine fueled with F30 from Farnesane**"at International Journal of Innovative Research in Science, Engineering and Technology(*IJIRSET*)Vol.4, SpecialIssue-11,Sept-2016

"**Analysis of combustion, performance and emission characteristics of a diesel engine using low sulfur tire fuel"** International Journal of Advanced Research in Management, Technology and Engineering (IJRMATE)Vol.2,SpecialIssue6,March2016. (ISSN

12/3/19

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

12/3/19

Head of the Institution
Department of Mechanical Engineering
Navodaya Institute of Technology (NIT)
RAICHUR.

INSTITUTE OF TECHNOLOGY * NAVODAYA * RAICHUR

Dr. A.Megalingam
Associate Professor
Bannari Amman Institute of Technology,
Satyamanghalam, Erode (TN)

B E	MECHANICAL	Dr.Sivanthi Aditanar College of Engineering	Manonmaniam Sundaranar University, Tirunelveli	2003
M E	ENGG.DESIGN	GCE, Tirunelveli	Anna University, Guindy, Chennai	2005
Ph.D	Tribology MACHINE DESIGN	IIT Madras	Indian Institute of Technology Madras	2013

PRINCIPAL
Navodaya Institute of Technology (NIT),
RAICHUR-584 103

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology (NIT)
RAICHUR.

NAVODAYA INSTITUTE OF TECHNOLOGY * RAICHUR

Dr.D.Madan, M.E., Ph.D., *(Mechanical)*
Associate Professor at Sri Indu College of Engineering and Technology,
Hyderabad

Ph.D	*Mechanical engineering*	: Renewable Energy, 2016, Anna University
M.E	*Engineering design*	: ***First Class***, 2008, Anna University
B.E	*Mechanical engineering*	: ***First Class***, 1998, Bharathiyar University
D.M.E	*Mechanical engineering*	***First Class***, 1988, Tamil Nadu Technical Board

INDUSTRY EXPERIENCE

1. Worked as Asst. Design Engineer at M/s Sarvamangala Engineers (Pvt) Ltd, Cbe–21,from Jan'94 - Sep'95.
2. Worked as Quality controller at M/s .M.R. Industries, Bangalore-21,from Nov'90 - Dec'93.
3. Apprenticeship Trainee at PWD. (Ground water division) Vellore, from Oct'89 - Oct'90

International

1. D. Madan, M. Rajendran 2016, "To Examination on Optimum Utilisation of Kinetic Energy and Operational Features from Tidal Stream Turbine" Cancer Biology, vol.6, no.4, pp. 180-184.
2. D. Madan, M. Rajendran, A. Amarkarthik, M. Prabhakaran and Suresh Sagadevan, 2015 "Experimental Investigation on Impact of Container Geometry on Efficiency of Non Buoyancy Body type Wave Energy Converter", INDJST, Volume 8 Number 29.
3. D.Madan, M.Rajendran, M.Prabhakaran, 2015, "An Overview on Hydrokinetic Energy Converter", Teri Information Digest on Energy and Environment, Volume 14, N0.2, pp.159-166.
4. D.Madan, M.Rajendran, M.Prabhakaran,2015, "Wave and Tidal Renewable Energy:Review", International Journal of Applied Engineering Research, Volume 10, No.9, pp.22739-22746
5. D. Madan, M. Rajendran, M. Prabhakaran, K.Anandan and Suresh Sagadevan, 2015,"Renewable Energy Scenario in Tamil Nadu" Teri Information Digest on Energy andEnvironment, vol. 14, no. 4, pp. 481-492.

PRINCIPAL
Navodaya Institute of Technology
RAICHUR-584 103

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology (NIT)
RAICHUR.

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

NAVODAYA

NET's

NAVODAYA INSTITUTE OF TECHNOLOGY, RAICHUR

DEPARTMENT OF MECHANICAL ENGINEERING

SUMMARY OF FACULTY DEVELOPMENT PROGRAMME ON

"ADVANCED TRAINING ON GEOMETRIC DIMENSIOINIG & TOLERANCES"



Theoretical session



Hands on Training in CIM Lab

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Five day's Faculty development program"**ADVANCED TRAINING ON GEOMETRIC DIMENSIOINIG & TOLERANCES**" was held 12th to16th of March 2019. Around 40 external participants from various Colleges are successfully participated in this Program. Dr. P. Rathnakumar, Head of Mechanical Engineering Department welcomed the gathering. Dr. M.V.Mallikarjun Principal gave a presidential address and facilitated the Chief Guests with mementos. The function was inaugurated and given brief description related to Selection, Fabrication and application oriented by the three specialized Resource Persons Dr. Chandru G,Assistant Professor Prathyusha Engineering College.AranvoyalKuppam, Tiruvalloreand Dr.A.Megalingam, Associate Professor Bannari Amman Institute of Technology, Erode, Tamil Nadu and Dr. D.Madan, Associate Professor Sri Indu College of Engineering andTechnology.

Further five days Three resource persons delivered lecture on the geometric characteristic symbols explaining in detail each symbol, feature control frames, different modifiers and how they affect tolerance when placed in the feature control frame.

The course also goes reviews the types of tolerances and the individual geometric characteristic symbols and their tolerance zone. Further explanation on profile tolerances (all round, between points, unequally disposed). Comparison of different characteristics for application purposes.

Hands on training classes conducted in afternoon session to apply practical knowledge on industry drawings of many Auto components.

Prof Ravi Kulkarni,Assistant Professor,Shrikar Kulkarni,Assistant Professor, Department of Mechanical Engineering Co-ordinator overviewed this FDP.

18/3/19

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

18/03/19

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology (NIT)
RAICHUR.

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

08.03.2019

2018-19/FDP/01

From
Dr.P. Rathnakumar,
Professor and Head,
Dept. of Mechanical Engineering
Navodaya Institute of Technology,
Raichur-584103

To
The Principal,
Navodaya Institute of Technology,
Raichur-584103

Sir

Subject: Req. for Auditorium hall to conduct FDP - Reg

We have proposed to conduct One Week FDP on the topic **Advanced Training on Geometric Dimensioning & Tolerances"** from 12.03.2019 to 16.03.2019. For this Purpose, we need auditorium on 03.03.2015- Time-10.00AM-5.30PM.

Thanking You

Yours faithfully

[Dr.P. Rathnakumar]

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology (NIT)
RAICHUR.

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

2018/19/FDP/02

From

Date: 08.03.2019

Dr.P Rathnakumar,

Hod/ME.,

NIT, Raichur

To

The Principal,

NIT, Raichur.

Sir,

Sub: Requisition for Accommodation and Food -Reg

I bring to your kind notice that we are conducting the Three days FDP on "Finite Element Analysis from Concept to Application" in our department on from *12th March-16th March 2019*. For that we want to arrange the accommodation and food for the Resource Persons in the above days. So kindly do the needful.

Resource Person Details,

Dr. Chandru J
Assistant Professor
Prathyusha Engineering College,
Aranvoyal Kuppam, Tiruvallore.

Dr. A.Megalingam
Associate Professor
Bannari Amman Institute of Technology,
Satyamanghalam, Erode (TN)

Dr. D.Madan
Associate Professor
Sri Indu College of Engineering and Technology,
Hyderabad,Telangana

Thanking you,

HOD/ME

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Head of Department
Department of MECHANICAL ENGINEERING
Navodaya Institute of Technology (NIT)
RAICHUR.

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

NAVODAYA

NET's

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Department of Mechanical Engineering

Faculty Development Programme (FDP) on

"Advanced Training on Geometric Dimensioning & Tolerances"

From 12th March to 16th March 2019

S.No.	Name of the Faculty	Designation & Dept.	Signature
1.	Ravi Kulkarni	AP/Mech	
2.	Fareem Akthar	AP/Mech	
3.	Rajashekaroppa	AP/Mech	
4.	Mallikarjun B	AP/Mech	
5.	Vinayak Manu	AP/Mech	
6.	priyankar D	AP/Mech	
7.	Zeeshan Ali	AP/Mech	
8.	pradeepkumar	AP/Mech	
9.	Honnappa	MTech/TPE	
10.	Balaraju	MTech/TPE	
11.	Abdul Aziz	MTech/TPE	
12.	Basavaraj	MTech/ESE	
13.	Ajeet Kumar	MTech/ESE	
14.	Soni	Vinken Pvt.	
15.	Suman Reddy	Parker-man	
16.	Tsho Rai	Hyundai	
17.	Mallikarjun C	AP/mech	
18.	Uday Raj	employee/KPC	
19.	Kavya. M	Employee/KPC	
20.	Bharathkumar.	Employee/Gitar.	

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

16/3/19

Principal
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

16/3/19

HOD & Convener
Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology (NIT)
RAICHUR.

NAVODAYA INSTITUTE OF TECHNOLOGY

DEPARTMENT OF MECHANICAL ENGINEERING

Evaluation Form

FDP Title: Advanced Training On Geometric Dimening & Tolera

Date: 16.03.2019 Time: 2.00 pm

Please complete the following evaluation for the FDP you attended. Your feedback will help us evaluate the effectiveness of this program and allow us to make improvements in the future program. **Your feedback is important for planning future programming and services we offer.**

Thank you.

4=Strongly Agree 3=Agree 2=Disagree 1=Strongly Disagree

	Circle your response			
1. The course met my expectations.	4	3	2	1
2. The content was helpful.	4	3	2	1
3. The level of the course was appropriate.	4	3	2	1
5. The speaker had a good understanding of the topics.	4	3	2	1
6. The handouts were helpful.	4	3	2	1
7. The course was worth my time.	4	3	2	1
8. The length of the course was appropriate.	4	3	2	1
9. I recommend that the course be repeated for other students.	4	3	2	1

What were the most useful aspects of the course?

What changes should be made to enhance/improve this program?

Additional comments:

Name ofthe Faculty: Rajashekarappa

Signature

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

NAVODAYA INSTITUTE OF TECHNOLOGY

RAICHUR- 584103, KARNATAKA

DEPARTMENT OF MECHANICAL ENGINEERING

CERTIFICATE

This is to certify that Mr./~~Ms.~~/Dr. Raja Shekarappa of NIT-Raichur has attendedFive day FDP on ***"Advanced Training on Geometric Dimensioning & Tolerances"*** Conductedat Navodaya Institute of Technology, Raichuron 12-03-2019to 16-03-2019.

Coordinator

HOD

Principal

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

NET GROUP OF EDUCATIONAL INSTITUTIONS: RAICHUR
NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

FDP ~~-GUEST LECTURE/SEMINR/ADD ON COURSE /CONFERENCE/SHORT TERM TRAINING PROGRAMME/ANY OTHER~~ PROPOSAL

Date: 15-06-2020

1	**ACADEMIC YEAR**	2019 - 2020
2	**TYPE OF THE PROGRAMME PROPOSED**	FACULTY DEVELOPMENT PROGRAMME
3	**TITLE OF THE PROGRAMME:**	**"ADVANCEMENT IN THE FIELD OF MECHANICAL ENGINEERING"**
4	**BROAD OBJECTIVE OF THE PROGRAMME:**	The main objective of the Programme is to focus the various advances in different fields of mechanical engineering. The eminent personalities of various fields of Manufacturing, Thermal, Materials, Industrial engineering and management will come at one platform to discuss the various aspects during this Programme. This Programme is beneficial to faculty members to share their opinions among the various researchers and industries people. With the globalization of the world economy, India is emerging as one of the key technical hubs and there is considerable demand from industry for mechanical engineers capable of meeting the challenging requirements.
5	**NAME OF THE INSTITUTION**	NIT-RAICHUR
6	**NAME OF THE DEPARTMENT(S)**	MECHANICAL ENGINEERING
8	**PROPOSED DATE OF FDP CONDUCTING**	23rd ಜೂನ್- 27th June2020

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

9	**TARGETED GROUP OF PARTICIPANTS**	Faculties, Post Graduates students, Research Scholars.
10	**BENEFICIARIES**	Faculties, Post Graduates students, Research Scholars from Mechanical Engineering.
11	**NUMBER OF DAYS REQUIRED TO ORGANISE THE PROGRAMME**	05 DAYS
12	**PROPOSED DURATION OF THE PROGRAMME**	30 Hours
13	**TOTAL NO. OF PERIODS REQUIRED FOR THE PROGRAMME**	30 Periods
14	**NO. OF PERIODS REQUIRED PER DAY**	6 Periods
15	**LOCATION DETAILS:**	Online Platform
16	**NO OF PARTICIPANTS:**	125 (Expected)
17	**DETAILS OF SPONSORERS:**	NIT Management



18	**DETAILS OF RESOURCE(S) PERSONS:**	Dr. K V Srinivasan Low Temperature facility, Tata Institute of Fundamental Research, Mumbai-400005. Dr.Jawahar Paulraj Assistant Professor NIT Tripura, Dr.Mohammed Iqbal Assistant Professor VelTech,Chennai Dr Ranjeet Kumar Sahu Assistant Professor NIT Surathkal, Mr T M L Shankar Divisional Manager Ashok Leyland Pvt Ltd., Chennai.
19	**REGISTRATION FEE WITH CERTIFICATION:**	Academicians and Research Scholar : Nil Faculties : Nil Industrial Employees : Nil
20	**REGISTRATION FEE WITHOUT CERTIFICATION:**	----------
21	**FACULTY COORDINATOR:**	Mr. Priyankar D/MECH/ NITR Mr. Raja Shakarappa/MECH/NITR
22	**PROGRAMME PROPOSED BY:**	Department Of Mechanical Engineering
23	**ANY OTHER RELEVANT MATTER:**	------
24	**ESTIMATED BUDGET IN RS:**	--------

This is to the Honorable Chairman of NIT for kind approval:

PRINCIPAL 15/6/2020

HOD 15/6

EVENT COORDINATOR 15.06.2020

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology (NIT)
RAICHUR.

APPROVED/NOT APPROVED

NAVODAYA INSTITUTE OF TECHNOLOGY * RAICHUR

NAVODAYA INSTITUTE OF TECHNOLOGY



ABOUT THE INSTITUTE:

Navodaya Institute of Technology (NIT), Raichur, established in 2008, is affiliated to Visveswaraya Technological University (VTU), Belagavi and is approved by All India Council for Technical Education (AICTE), New Delhi. The guiding spirit and the driving force for all the progress and achievements of the institution is our most respected Chairman Sri. S. R. Reddy of Navodaya Education Trust®, Raichur. NIT focuses on promoting quality technical education in North Karnataka region in general and Hyderabad Karnataka Region in particular.

ABOUT THE DEPARTMENT:

The department of **Mechanical Engineering** is established in the year 2010 and started its humble share & contribution of the Mechanical Engineering graduates for national needs. The department offers BE program in Mechanical engineering with annual intake 60 and subsequently introduced M.Tech. (Thermal power Engineering) program in 2014. The Department has excellent lab facilities with state of the art equipment and has exclusive Computer Centers equipped with 30 Core-i3 systems, one NT server including number of Mechanical engineering related software packages. The department is enriched with highly qualified, experienced and committed faculty members and faculty are from reputed institutes like IITs, NITs and other prestigious universities. The Department gives importance not only to academic education but also ethical values and personality development of students by practicing a close mentoring system on regular basis. The department strives hard to impart quality education and to keep the students on the competitive edge of the present-day technology and marching towards University ranks also, the department is actively involved in placement of the students through ON/OFF Campus Interviews.

Chief Patron:
Sri S. R. Reddy,
Hon'ble Chairman, NET, Raichur

Chief Convener:
Dr. M.V. Mallikarjuna
Principal,
NIT-Raichur

Convener:
Dr. P. Rathna Kumar,
Professor and Head
Department of ME, NIT-Raichur
Contact No.: +91-7337704346

Coordinator:
Mr. MD Umar
Associate Professor
Department of ME, NIT-Raichur
Contact No.: +91-9980339850

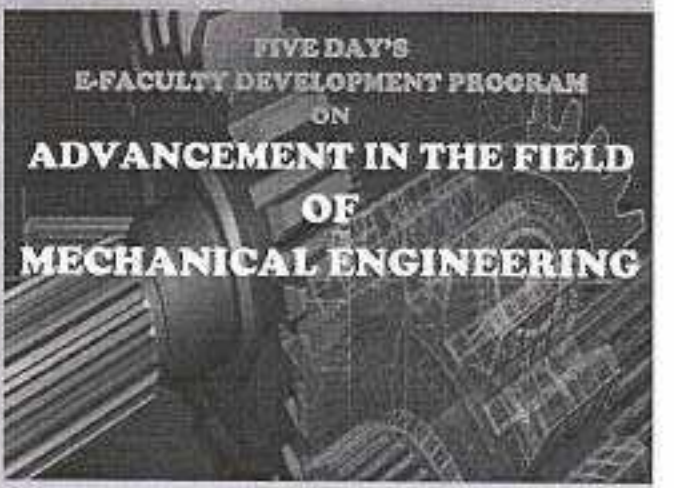
Mr. Priyankar D
Assistant Professor
Department of ME, NIT-Raichur
Contact No.: +91-9972083592

Organizing Committee:

Dr. Ananthachar	Associate Professor
Mr. Srikar G Kulkarni	Assistant Professor
Mr. Rajashekarappa	Assistant Professor
Mr. Ravi Kulkarni	Assistant Professor
Mr. Imran Basha	Assistant Professor
Mr. Faheem Akthar	Assistant Professor
Mr. Mallikarjun B	Assistant Professor
Mr. Pradeep Kumar Illay	Assistant Professor
Mr. Vinayak M	Assistant Professor
Mr. Zeeshan Ali	Assistant Professor

Website: engineering.navodaya.edu.in
Follow us on:

NAVODAYA INSTITUTE OF TECHNOLOGY



23rd - 27th June 2020

Platform: GotoMetting app & YouTube live Streaming

Organized by
DEPARTMENT OF MECHANICAL ENGINEERING
NAVODAYA INSTITUTE OF TECHNOLOGY
Navodaya Nagar, Raichur-584 103,
Karnataka.

Registration using QR Code:

NAVODAYA INSTITUTE OF TECHNOLOGY

ABOUT e-FDP:

This online FDP will be extremely beneficial to faculty members from various institutions, research scholars and Industrialists those who aspire to update the knowledge in the recent development in the various field of Mechanical Engineering

Outcome of the event:

Online Faculty Development Program for Faculty members, Research scholars have proved to be successful for improving knowledge and skills to enriched their understanding of the subject and apply to recent technologies in the field

- **Registration Fee : Free**
- **Last date of registration : 21st June 2020**
- **E-Certificate of Participation will be provided on successful completion.**
- **Timing of e-FDP : 2:30PM to 4:30PM**
- **WhatsApp group created to share the session details and joining link.**

Registration link for e-FDP:

https://forms.gle/mcZWiLX4orvJa7n28

Five Day's E-Faculty Development Program Schedule

Day & Date	Name of the Resource Person	Designation	Topic	Timing
23rd June 2020 (Tuesday)	**Dr. K V Srinivasan**	Low Temperature Facility, Tata Institute of Fundamental Research, Homi Bhaba Road, Colaba, Mumbai-400	Liquefaction of Gases using Stirling Cry cooler with the Emphasis on Indigenous Development of Regenerator Using 3D Printing.	2:30PM to 4:30PM
24th June 2020 (Wednesday)	**Dr. Jawahar Paulraj**	Assistant Professor, Department of Mechanical Engg., National Institute of Technology, Agartala, Tripura	Development of Rubber based Composite Materials	2:30PM to 4:30PM
25th June 2020 (Thursday)	**Dr. Mohamed Iqbal**	Associate Professor, Department of Mechanical Engineering, Vel-Technology, Chennai.	Preparation and Characterization of Emerging fluids for heat transfer Applications	2:30PM to 4:30PM
26th June 2020 (Friday)	**Dr. Ranjeet Kumar Sahu**	Assistant Professor Department of Mechanical Engineering National Institute of Technology Karnataka Surathkal, Mangaluru - 575025	A New Vista of Manufacturing Technology for Scientific/Industrial/Medical Society and its Prospective Limelight to Combat COVID-19 transmission	2:30PM to 4:30PM
27th June 2020 (Saturday)	**Mr. T M L Sankar**	Divisional Manager, Ashok Leyland, Chennai.	Latest Trends of Auto Industry	2:30PM to 4:30PM

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR



NET'S

Navodaya Institute of Technology, Raichur

Department of Mechanical Engineering

5 Days e-FDP on Advancements in the field of Mechanical Engineering

on 23rd -27th June 2020

Date / Day	Name of the Resource Person	Designation	Sub Topic	Timing
23.06.2020 (Tuesday)	Dr. K V Srinivasan	Low Temperature Facility, Tata Institute of Fundamental Research, HomiBhabha road, Colaba, Mumbai - 400 005. INDIA	Liquefaction of Gases using Stirling Cryocooler with the Emphasis on Indigenous Development of Regenerator Using 3D Printing.	2.30 - 4.30
24.06.2020 (Wednesday)	Dr. JawaharPaulraj	Assistant Professor, Department of Mechanical Engg., National Institute of Technology, Tripura	Development of Rubber based Composite Materials	2.30 - 4.30
25.06.2020 (Thursday)	Dr.Mohamedlqbal	Associate Professor, Department of Mechanical Engineering, Vel Technology, Chennai.	Preparation and Characterization of Emerging fluids for heat transfer Applications	2.30 - 4.30
26.06.2020 (Friday)	Dr. Ranjeet Kumar Sahu	Assistant Professor Department of Mechanical Engineering National Institute of Technology Karnataka Surathkal, Mangaluru - 575025	A New Vista of Manufacturing Technology for Scientific/Industrial/Medical Society and its Prospective Limelight to Combat COVID-19 transmission"	2.30 - 4.30
27.06.2020 (Saturday)	Mr.TMLSankar	Divisional Manager, Ashok Leyland, Chennai.	Latest Trends of Auto Industry	2.30 - 4.30

22/06/2020

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

24/6

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology (NIT)
RAICHUR.

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

2019/20 – FDP/01

Ref: Date: 16.06.2020

To

Dr.K V Srinivasan,
Low Temperature Facility,
Tata Institute of Fundamental Research,
HomiBhabha road, Colaba, Mumbai - 400 005. INDIA
Phone : 2278 2515(off) 22782365 or 2991(lab) Mobile: 0-99872 60952

Dear Sir,

Sub: **InvitationofFive - day'se-Faculty Development Program -Reg**

We are pleased to inform you that Navodaya Institute of Technology, Raichur is a renowned technical institution offering five UG and Two PG programmes. It was started in the year 2008 by a group of philanthropists of Navodaya Educational Trust. The college is affiliated to Visvesvaraya Technological University Belgaum.

We planned to conduct a 5- day FDP on **"Advancements in the field of Mechanical Engineering"** from 23rd -27th June 2020 through online platform. We cordially invite you to deliver lecture to the Mechanical Engineering faculty at 2.30pm -4.30pmon 23rd June 2020 as per discussion held. We hope that your expertise in this field will be beneficial for faculties ofvarious colleges also. I humbly request you to make favorable reply with the sub title related to the topic to **hodmech.nit@navodaya.edu.in (or)rathnakumar1972@gmail.com**

Thanking you.

HOD/Mech
(Dr.P. Rathnakumar)

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology
RAICHUR

Navodaya Education Trust runs by

- Navodaya Medical College
- Navodaya Medical College Hospital & Research Centre
- Navodaya Dental College
- Net Pharmacy College
- Navodaya College of Physiotherapy
- Navodaya College of Education
- Navodaya College of Paramedical Science
- Navodaya College of Nursing
- Navodaya Central School

16.06.2020
PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY * RAICHUR

2019-20/FDP/02

Ref: Date: 09.06.2020

To

Dr. Ranjeet Kumar Sahu B.E., M.Tech (NIT), PhD (IIT)
Assistant Professor
Department of Mechanical Engineering
National Institute of Technology,Surathkal,
Karnataka (NITK),

Dear Sir,

Sub: **Invitation -Five -day'se-Faculty Development Program-Reg**

We are pleased to inform you that Navodaya Institute of Technology, Raichur is a renowned technical institution offering five UG and Two PG programmes. It was started in the year 2008 by a group of philanthropists of Navodaya Educational Trust. The college is affiliated to Visvesvaraya Technological University Belgaum.

We planned to conduct a 5- day FDP on **"Advancementsin the field of Mechanical Engineering"** from23rd -27thJune2020 through online platform. We cordially invite you to deliver lecture to the Mechanical Engineering faculty at 2.30pm -4.30pmon 26th June 2020 as per discussion held. We hope that your expertise in this field will be beneficial for faculties ofvarious colleges also. I humbly request you to make favorable reply with the subtitle related to the topic to**hodmech.nit@navodaya.edu.in (or)rathnakumar1972@gmail.com**

Thanking you.

09/06/20

HOD/Mech
(Dr.P. Rathnakumar)

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology
RAICHUR

Navodaya Education Trust runs by

- Navodaya Medical College
- Navodaya Medical College Hospital & Research Centre
- Navodaya Dental College
- Net Pharmacy College
- Navodaya College of Physiotherapy
- Navodaya College of Education
- Navodaya College of Paramedical Science
- Navodaya College of Nursing
- Navodaya Central School

09/6/2020

Principal
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

2019-20/FDP/03

Ref: Date: 09.06.2020

To

Mr.TMLSankar, M.Tech.,
Divisional Manager
Ashok Leyland Pvt Ltd.,
Chennai.

Dear Sir,

Sub: **Invitation -Five - day'se-Faculty Development Program -Reg**

We are pleased to inform you that Navodaya Institute of Technology, Raichur is a renowned technical institution offering five UG and Two PG programmes. It was started in the year 2008 by a group of philanthropists of Navodaya Educational Trust. The college is affiliated to Visvesvaraya Technological University Belgaum.

We planned to conduct a 5- day FDP on **"Advancements in the field of Mechanical Engineering"** from 23rd -27th June 2020 through online platform. We cordially invite you to deliver lecture to the Mechanical Engineering faculty at 2.30pm -4.30pmon 27th June 2020 as per discussion held. We hope that your expertise in this field will be beneficial for faculties ofvarious colleges also. I humbly request you to make favorable reply with the sub title related to the topic to **hodmech.nit@navodaya.edu.in (or)rathnakumar1972@gmail.com**

Thanking you.

HOD/Mech
(Dr.P. Rathnakumar)

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology (NIT)
RAICHUR.

Navodaya Education Trust runs by

- Navodaya Medical College
- Navodaya Medical College Hospital & Research Centre
- Navodaya Dental College
- Net Pharmacy College
- Navodaya College of Physiotherapy
- Navodaya College of Education
- Navodaya College of Paramedical Sciences
- Navodaya College of Nursing
- Navodaya Central School

09/06/2020

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY
PAICHUR

2019-20/FDP/04

Ref: Date: 09.06.2020

To

Mr.Mallikarjun
CEO- CAD vision Research and Development Pvt.Ltd.,,
Bangalore.

Dear Sir,

Sub: **Invitation -Five - day'se-Faculty Development Program -Reg**

We are pleased to inform you that Navodaya Institute of Technology, Raichur is a renowned technical institution offering five UG and Two PG programmes. It was started in the year 2008 by a group of philanthropists of Navodaya Educational Trust. The college is affiliated to Visvesvaraya Technological University Belgaum.

We planned to conduct a 5- day FDP on **"Advancements in the field of Mechanical Engineering"** from 23rd -27th June 2020 through online platform. We cordially invite you to deliver lecture to the Mechanical Engineering faculty at 2.30pm -4.30pmon 24th June 2020 as per discussion held. We hope that your expertise in this field will be beneficial for faculties ofvarious colleges also. I humbly request you to make favorable reply with the sub title related to the topic to **hodmech.nit@navodaya.edu.in (or)rathnakumar1972@gmail.com**

Thanking you.

HOD/Mech
(Dr.P. Rathnakumar)

9/6/20
PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

2019-20/FDP/05

Ref: Date: 18.06.2020

To

Dr.JawaharPaulraj,
Assistant Professor,
Department of Mechanical Engg,
National Institute of Technology,
Tripura.

Dear Sir,

Sub: **InvitationofFive - day'se-Faculty Development Program -Reg**

We are pleased to inform you that Navodaya Institute of Technology, Raichur is a renowned technical institution offering five UG and Two PG programmes. It was started in the year 2008 by a group of philanthropists of Navodaya Educational Trust. The college is affiliated to Visvesvaraya Technological University Belgaum.

We planned to conduct a 5- day FDP on **"Advancements in the field of Mechanical Engineering"** from 23rd -27th June 2020 through online platform. We cordially invite you to deliver lecture to the Mechanical Engineering faculty at 2.30pm -4.30pmon 24th June 2020 as per discussion held. We hope that your expertise in this field will be beneficial for faculties ofvarious colleges also. I humbly request you to make favorable reply with the sub title related to the topic to **hodmech.nit@navodava.edu.in (or)rathnakumar1972@gmail.com**

Thanking you.

18/6

HOD/Mech
(Dr.P. Rathnakumar)
Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology
RAICHUR.

18/06/2020

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

2019-20/FDP/06

Ref: Date: 18.06.2020

To

Dr.MohamedIqbal,
Assistant Professor,
Department of Mechanical Engg,
VelTech,Chennai

Dear Sir,

Sub: **InvitationofFive - day'se-Faculty Development Program -Reg**

We are pleased to inform you that Navodaya Institute of Technology, Raichur is a renowned technical institution offering five UG and Two PG programmes. It was started in the year 2008 by a group of philanthropists of Navodaya Educational Trust. The college is affiliated to Visvesvaraya Technological University Belgaum.

We planned to conduct a 5- day FDP on **"Advancements in the field of Mechanical Engineering"** from 23rd -27th June 2020 through online platform. We cordially invite you to deliver lecture to the Mechanical Engineering faculty at 2.30pm -4.30pmon 25th June 2020 as per discussion held. We hope that your expertise in this field will be beneficial for faculties ofvarious colleges also. I humbly request you to make favorable reply with the sub title related to the topic to **hodmech.nit@navodaya.edu.in (or)rathnakumar1972@gmail.com**

Thanking you.

18/06

HOD/Mech
(Dr.P. Rathnakumar)

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology
RAICHUR

Navodaya Education Trust runs by

- Navodaya Medical College
- Navodaya Medical College Hospital & Research Centre
- Navodaya Dental College
- Net Pharmacy College
- Navodaya College of Physiotherapy
- Navodaya College of Education
- Navodaya College of Paramedical Sciences
- Navodaya College of Nursing
- Navodaya Central School

18/06/2020

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

NET's

NAVODAYA INSTITUTE OF TECHNOLOGY

(Affiliated to Visvesvaraya Technological University, Belgavi, approved by AICTE, New Delhi & recognized by Government of Karnataka)
Navodaya Nagar, Beljuguri Road, Raichur-584103, Karnataka
Website: https://engineering.navodaya.edu.in/

ONLINE FACULTY DEVELOPMENT PROGRAM
ON
"ADVANCEMENT IN THE FIELD OF MECHANICAL ENGINEERING"

Certificate of appreciation

This is to certify that Dr. Ranjeet Kumar Sahu, Assistant Professor from National Institute of Technology, Suratkal, Karnataka for sharing the expertise as Resource person on 26th June 2020 in the five day's online Faculty Development Program on "Advancement in the field of Mechanical Engineering", organized by Department of Mechanical Engineering, Navodaya Institute of Technology, Raichur (Karnataka) from 23rd to 27th June 2020.

Prof. Md. Umar	Dr. P Rathnakumar	Dr. M V Mallikarjuna
Coordinator	*Convener/HoD-Mech*	*Principal*

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

NEI's

NAVODAYA INSTITUTE OF TECHNOLOGY

(Affiliated to Visvesvaraya Technological University, Belgavi, approved by AICTE, New Delhi & recognized by Government of Karnataka)
Navodaya Nagar, Bypass to Road, Raichur-584103, Karnataka
Website: https://engineering.navodaya.edu.in/

ONLINE FACULTY DEVELOPMENT PROGRAM
ON
"ADVANCEMENT IN THE FIELD OF MECHANICAL ENGINEERING"

Certificate of appreciation

This is to certify that Dr. Mohamed Iqbal, Associate Professor from Vel Tech Rangarajan Dr. Sagunthala R&D Institute of Science & Technology, Chennai for sharing the expertise as Resource person on 25th June 2020 in the five day's online Faculty Development Program on "Advancement in the field of Mechanical Engineering", organized by Department of Mechanical Engineering, Navodaya Institute of Technology, Raichur (Karnataka) from 23rd to 27th June 2020.

Prof. Md. Umar	Dr. P Rathnakumar	Dr. M V Mallikarjuna
Coordinator	*Convener/HoD-Mech*	*Principal*

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

NET's

NAVODAYA INSTITUTE OF TECHNOLOGY

(Affiliated to Visvesvaraya Technological University, Belgavi, approved by AICTE, New Delhi &
recognized by Government of Karnataka)
Navodaya Nagar, Bypass to Road, Raichur-584103, Karnataka
Website: https://engineering.navodaya.edu.in/

ONLINE FACULTY DEVELOPMENT PROGRAM
ON
"ADVANCEMENT IN THE FIELD OF MECHANICAL ENGINEERING"

Certificate of appreciation

This is to certify that Dr. K V Srinivasan, Low Temperature Faculty from Tata Institute of Fundamental Research, Mumbai for sharing the expertise as Resource person on 23rd June 2020 in the five day's online Faculty Development Program on "Advancement in the field of Mechanical Engineering", organized by Department of Mechanical Engineering, Navodaya Institute of Technology, Raichur (Karnataka) from 23rd to 27th June 2020.

Prof. Md. Umar	Dr. P Rathnakumar	Dr. M V Mallikarjuna
Coordinator	*Convener/HoD-Mech*	*Principal*

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

NEI's

NAVODAYA INSTITUTE OF TECHNOLOGY

(Affiliated to Visvesvaraya Technological University, Belgavi, approved by AICTE, New Delhi & recognized by Government of Karnataka)
Navodaya Nagar, Bellagera Road, Raichur-584103, Karnataka
Website: https://engineering.navodaya.edu.in/

ONLINE FACULTY DEVELOPMENT PROGRAM
ON
"ADVANCEMENT IN THE FIELD OF MECHANICAL ENGINEERING"

Certificate of appreciation

This is to certify that Dr. Jawahar Paulraj, Assistant Professor from National Institute of Technology, Agartala, Tripura for sharing the expertise as Resource person on 24rd June 2020 in the five day's online Faculty Development Program on "Advancement in the field of Mechanical Engineering", organized by Department of Mechanical Engineering, Navodaya Institute of Technology, Raichur (Karnataka) from 23rd to 27th June 2020.

Prof. Md. Umar	**Dr. P Rathnakumar**	**Dr. M V Mallikarjuna**
Coordinator	*Convener/HoD-Mech*	*Principal*

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

NET's

NAVODAYA INSTITUTE OF TECHNOLOGY

(Affiliated to Visvesvaraya Technological University, Belgavi, approved by AICTE, New Delhi &
recognized by Government of Karnataka)
Navodaya Nagar, Begagere Road, Raichur-584103, Karnataka
Website: https://engineering.navodaya.edu.in/

ONLINE FACULTY DEVELOPMENT PROGRAM
ON
"ADVANCEMENT IN THE FIELD OF MECHANICAL ENGINEERING"

Certificate of appreciation

This is to certify that Dr. K V Srinivasan, Low Temperature Faculty from Tata Institute of Fundamental Research, Mumbai for sharing the expertise as Resource person on 23rd June 2020 in the five day's online Faculty Development Program on "Advancement in the field of Mechanical Engineering", organized by Department of Mechanical Engineering, Navodaya Institute of Technology, Raichur (Karnataka) from 23rd to 27th June 2020.

Prof. Md. Umar	Dr. P Rathnakumar	Dr. M V Mallikarjuna
Coordinator	*Convener/HoD-Mech*	*Principal*

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

NET's

NAVODAYA INSTITUTE OF TECHNOLOGY, RAICHUR

Organized by

Department of Mechanical Engineering

Free e-FDP **23rd to 27th June 2020**

A FIVE DAY FACULTY DEVELOPMENT PROGRAM ON

"ADVANCEMENT IN THE FIELD OF MECHANICAL ENGINEERING"

Session : 01
Date : 23-06-2020
Time : 2:30PM to 4:30PM

Topic : Liquefaction of Gases using Stirling Cry cooler with the Emphasis on Indigenous Development of Regenerator Using 3D Printing



Dr. K V Srinivasan
Low Temperature Facility,
Tata Institute of Fundamental Research,
Homi Bhaba Road, Colaba, Mumbai

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

22/06/2020

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

22/06

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology (NIT)
RAICHUR.

5 Days e-FDP/NIT-R (AIFME 2020) Attendees

Day-1 Summary

Meeting Date	Meeting Duration	Number of Attendees	Meeting ID
June 23, 2020 1:35 AM PDT	140 minutes	159	753-875-701

Details

Name of the Participants	Email Address	Join Time	Leave Time	Time in Session (minutes)
(141)-S. SURESHKUMAR	sureshkrengg@gmail.com	1:41 AM	2:10 AM	28
(141)-S. SURESHKUMAR	sureshkrengg@gmail.com	3:19 AM	3:33 AM	13
(141)-S. SURESHKUMAR	sureshkrengg@gmail.com	3:34 AM	3:53 AM	19
(141)-S. SURESHKUMAR	sureshkrengg@gmail.com	2:17 AM	3:16 AM	58
100(Sanjay)	sanjaysomesh@gmail.com	2:15 AM	3:32 AM	76
107 (Raja Shakarappa)	rajashakarappa@navodaya.edu.in	2:31 AM	2:35 AM	3
107 (Raja Shakarappa)	rajashakarappa@navodaya.edu.in	1:57 AM	2:04 AM	6
120 (Varun Chebrolu)	varunchebrolu@mvgrce.edu.in	1:55 AM	3:55 AM	120
144 (Dr. Arun Rane)	arun_rane@fragnel.edu.in	1:55 AM	3:55 AM	120
160 (Imran Basha A)	imranbashame.nit@navodaya.edu.in	1:57 AM	2:30 AM	33
160 (Imran Basha A)	imranbashame.nit@navodaya.edu.in	3:16 AM	3:17 AM	0
24 (Dr Bathula Venkata Siva)	venkatasivanec@gmail.com	1:56 AM	3:49 AM	112
26 Thugu Venu Reddy	venureddy.t100@gmail.com	2:39 AM	3:11 AM	32
93 (Madhan Kumar G)		2:50 AM	2:53 AM	2
93 (Madhan Kumar G)		2:53 AM	3:55 AM	62
AJAY KUMAR S	ajaykumars_mct@mgit.ac.in	1:46 AM	3:55 AM	128
Abhijith Vaidya S	abhi1vaidya@gmail.com	1:55 AM	2:53 AM	58
Ajinkya		2:16 AM	2:19 AM	2
Ajinkya		2:23 AM	3:52 AM	89
Amitkumar H	amitkumar.h3@gmail.com	1:53 AM	3:55 AM	121
Anbarasu P		2:13 AM	2:16 AM	2
Anbarasu P		3:10 AM	3:11 AM	1
Anbarasu P		3:41 AM	3:55 AM	14
Arun K K	arun.kk.mec@kct.ac.in	1:35 AM	1:45 AM	10
Arun K K	arun.kk.mec@kct.ac.in	1:52 AM	2:13 AM	20
Arun K K	arun.kk.mec@kct.ac.in	2:16 AM	3:41 AM	85
Ashish Tiwari	tiwariaj@gmail.com	3:05 AM	3:06 AM	0
Ashish Tiwari	tiwariaj@gmail.com	3:32 AM	3:44 AM	11
Ashish Tiwari	tiwariaj@gmail.com	3:44 AM	3:49 AM	4

NAVODAYA INSTITUTE OF TECHNOLOGY * RAICHUR

29/06/2020

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

29/6

Head of Department

NET's

NAVODAYA INSTITUTE OF TECHNOLOGY, RAICHUR

Organized by

Department of Mechanical Engineering

Free e-FDP **23rd to 27th June 2020**

A FIVE DAY FACULTY DEVELOPMENT PROGRAM ON

"ADVANCEMENT IN THE FIELD OF MECHANICAL ENGINEERING"

Session : 02
Date : 24-06-2020
Time : 2:30PM to 4:30PM

Topic : Development of Rubber based Composite Materials



Dr. Jawahar Paulraj
Assistant Professor,
Department of Mechanical Engg.,
National Institute of Technology, Agartala,
Tripura

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

24/06/2020

Navodaya Institute of Technology (Affl.)
RAICHUR-584 103

24/6

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology
RAICHUR.

5 Days e-FDP/NIT-R (AIFME 2020) Attendees

Day-2 Summary

Meeting Date	Meeting Duration	Number of Attendees
June 24, 2020 1:38 AM PDT	144 minutes	138

Details

Meeting ID: 753-875-701

Name	Email Address	Time in Session (35 minutes)
(141)-S. SURESHKUMAR	sureshkrengg@gmail.com	74
(41) KONDAPALLI SIVA PRASAD	kspanits@gmail.com	114
100(Sanjay)	sanjaysomesh@gmail.com	95
109 - Mr. Meghnath Sen	meghnath001@gmail.com	65
126_Prem Narayan Vishwakarma	pnvishwakarma@amity.edu	92
144 (Dr. Arun Rane)	arun_rane@fragnel.edu.in	108
148-SHAIK SHASHA VALI	shaik.shashavali@gmail.com	125
166_Victor Richardson	victorthermal92@gmail.com	132
181- PRABHU M	prabhu308397@gmail.com	126
211_A.GNANA SREE	allu71gnanasree@gmail.com	141
225_ K.PRIYADHARSHINI	priyaaero.jpr@gmail.com	143
24 (Dr Bathula Venkata Siva)	venkatasivanec@gmail.com	130
257_ SHAIK AVINASH KAPIL	avinashkapil343@gmail.com	132
26 Thugu Venu Reddy	venureddy.t100@gmail.com	95
27-Dr.Diganta Hatibaruah	dhbaruah@gmail.com	142
31. (Dr.KARTHICK K)	karthickmech@rmkcet.ac.in	56
35 Dr ARUN KK	arunmyles@gmail.com	139
53_Jayesh Jariwala	jayesh.jariwala@utu.ac.in	139
68_Ahmed Abdul Muneem	abdulmuneem@mjcollege.ac.in	93
72_Abhijith Vaidya S	abhi1vaidya@gmail.com	120
89_H.AMERESH	ameresh88@gmail.com	117
98_Kamatchi Hariharan M	hariharanj7@gmail.com	134
A 92_Jonnala Subba Reddy	jonnalasu@yahoo.co.in	141
AJAY KUMAR S	ajaykumars_mct@mgit.ac.in	118
ASHOK KUMAR R	mechevents@rmkcet.ac.in	143
AVINASH H S	avinashhs.1988@sitmng.ac.im	76
Aditya singh	apsiet79@gmail.com	135
Ajay Sharma	ajayrcert021@gmail.com	124
Amitkumar H	amitkumar.h3@gmail.com	73
Anantachar.M	anantachar3@gmail.com	65
Attendee 266	saravanakumar0915@gmail.com	120
B G Koujalagi		50
D NAVEENRAJ	navhicet@gmail.com	112

24/06/2020

PRINCIPAL
Navodaya Institute of Technology
RAICHUR-584 103

24/6/20

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology
RAICHUR.

INSTITUTE OF TECHNOLOGY * NAVODAYA * RAICHUR

NET's

NAVODAYA INSTITUTE OF TECHNOLOGY, RAICHUR

Organized by

Department of Mechanical Engineering

Free e-FDP **23rd to 27th June 2020**

A FIVE DAY FACULTY DEVELOPMENT PROGRAM ON

"ADVANCEMENT IN THE FIELD OF MECHANICAL ENGINEERING"

Session : 03
Date : 25-06-2020
Time : 2:30PM to 4:30PM

Topic : Preparation and Characterization of Emerging fluids for heat transfer Applications



Dr. Mohamed Iqbal
Associate Professor,
Department of Mechanical Engineering,
Vel Technology, Chennai

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

25/06/2020

PRINCIPAL
Navodaya Institute of Technology
RAICHUR

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology
RAICHUR

5 Days e-FDP/NIT-R (AIFME 2020) Attendees

Day-3 Summary

Meeting Date	Meeting Duration	Number of Attendees	Meeting ID
June 25, 2020 1:32 AM PDT	110 minutes	148	753-875-701

Details

Name	Email Address	Join Time	Leave Time	Time in Session
(141)-S. SURESHKUMAR	sureshkrengg@gmail.com	2:17 AM	3:23 AM	66
(41) KONDAPALLI SIVA PRASAD	kspanits@gmail.com	1:59 AM	3:06 AM	66
100(Sanjay)	sanjaysomesh@gmail.com	2:06 AM	3:23 AM	77
107 (Raja Shakarappa)	rajashakarappa@navodaya.edu.in	3:08 AM	3:09 AM	1
107 (Raja Shakarappa)	rajashakarappa@navodaya.edu.in	2:46 AM	2:47 AM	0
107 (Raja Shakarappa)	rajashakarappa@navodaya.edu.in	1:59 AM	2:10 AM	11
109 - Mr. Meghnath Sen	meghnath001@gmail.com	2:08 AM	2:33 AM	24
126_Prem Narayan Vishwakarma	pnvishwakarma@amity.edu	1:39 AM	3:23 AM	104
144 (Dr. Arun Rane)	arun_rane@fragnel.edu.in	1:56 AM	3:23 AM	86
148-SHAIK SHASHA VALI	shaik.shashavali@gmail.com	1:35 AM	3:23 AM	108
155_Suhas U	suhas.mech@citech.edu.in	2:57 AM	3:08 AM	10
155_Suhas U	suhas.mech@citech.edu.in	3:16 AM	3:23 AM	7
166_Victor Richardson	victorthermal92@gmail.com	1:38 AM	3:23 AM	105
169 Ashish Tiwari	tiwariaj@gmail.com	2:14 AM	2:58 AM	43
169 Ashish Tiwari	tiwariaj@gmail.com	2:59 AM	3:10 AM	10
169 Ashish Tiwari	tiwariaj@gmail.com	3:11 AM	3:23 AM	11
17 (Dr Bathula Venkata Siva)	venkatasivanec@gmail.com	1:53 AM	3:16 AM	82
181- PRABHU M	prabhu308397@gmail.com	1:53 AM	3:02 AM	68
181- PRABHU M	prabhu308397@gmail.com	3:09 AM	3:23 AM	14
211_A.GNANA SREE allu71gnanasree@gmail.com		1:55 AM	3:21 AM	85
225_ K.PRIYADHARSHINI	priyaaero.jpr@gmail.com	1:33 AM	3:23 AM	109
26 Thugu Venu Reddy	venureddy.t100@gmail.com	3:05 AM	3:23 AM	17
26 Thugu Venu Reddy	venureddy.t100@gmail.com	2:21 AM	2:30 AM	9
27-Dr.Diganta Hatibaruah	dhbaruah@gmail.com	1:38 AM	3:23 AM	104
31. (Dr.KARTHICK K)	karthickmech@rmkcet.ac.in	1:48 AM	3:01 AM	73
35. Dr ARUN KK	arunmyies@gmail.com	1:42 AM	3:23 AM	100
53_Jayesh Jariwala	jayesh.jariwala@utu.ac.in	1:54 AM	3:23 AM	89

NAVODAYA INSTITUTE OF TECHNOLOGY * RAICHUR

25/06/2020

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR

Department of Mechanical Engineering
Navodaya Institute of Technology (NIT)
RAICHUR.

NET's

NAVODAYA INSTITUTE OF TECHNOLOGY, RAICHUR

Organized by

Department of Mechanical Engineering

Free e-FDP **23rd to 27th June 2020**

A FIVE DAY FACULTY DEVELOPMENT PROGRAM ON

"ADVANCEMENT IN THE FIELD OF MECHANICAL ENGINEERING"

Session : 04
Date : 26-06-2020
Time : 2:30PM to 4:30PM

Topic : A New Vista of Manufacturing Technology for Scientific/Industrial/Medical Society and its Prospective Limelight to Combat COVID-19 transmission



Dr. Ranjeet Kumar Sahu
Assistant Professor
Department of Mechanical Engineering
National Institute of Technology Karnataka
Surathkal,
Mangaluru - 575025

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

28/6/2020

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology (NIT)
RAICHUR.

5 Days e-FDP/NIT-R (AIFME 2020) Attendees

Day-4 Summary

Meeting Date	Meeting Duration	Number of Attendees	Meeting ID
26-Jun-20	143 minutes	147	753-875-701

Details

Name	Email Address	Join Time	Leave Time	Time in Session (minutes)
(141)-S. SURESHKUMAR	sureshkrengg@gmail.com	1:51 AM	3:56 AM	124
(41) KONDAPALLI SIVA PRASAD	ksparils@gmail.com	2:11 AM	3:28 AM	77
100(Sanjay)	sanjaysomesh@gmail.com	1:55 AM	3:56 AM	120
107 (Raja Shakarappa)	rajashakarappa@navodaya.edu.in	1:54 AM	2:10 AM	15
109 - Mr. Meghnath Sen	meghnath001@gmail.com	2:24 AM	3:12 AM	47
126_Prem Narayan Vishwakarma	pnvishwakarma@amity.edu	1:52 AM	3:56 AM	123
144 (Dr. Arun Rane)	arun_rane@fragnel.edu.in	1:56 AM	3:24 AM	87
148-SHAIK SHASHA VALI	shaik.shashavali@gmail.com	1:48 AM	3:56 AM	127
155_Suhas U	suhas.mech@cambridge.edu.in	2:18 AM	2:35 AM	17
166_Victor Richardson	victorthermal92@gmail.com	1:41 AM	2:43 AM	62
166_Victor Richardson	victorthermal92@gmail.com	2:45 AM	3:56 AM	71
169 Ashish Tiwari	tiwarlaj@gmail.com	3:01 AM	3:49 AM	47
169 Ashish Tiwari	tiwarlaj@gmail.com	3:55 AM	3:56 AM	0
17 (Dr Bathula Venkata Siva)	venkatasivanec@gmail.com	1:53 AM	3:27 AM	93
181- PRABHU M	prabhu308397@gmail.com	2:54 AM	2:56 AM	2
181- PRABHU M	prabhu308397@gmail.com	1:43 AM	2:33 AM	50
181-M PRABHU	prabhu308397@gmail.com	3:00 AM	3:56 AM	55
196 (Diganta Hatibaruah)	dhbaruah@gmail.com	2:14 AM	3:56 AM	101

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

26/06/2020

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology (NIT)
RAICHUR

NET's

NAVODAYA INSTITUTE OF TECHNOLOGY, RAICHUR

Organized by

Department of Mechanical Engineering

Free e-FDP **23rd to 27th June 2020**

A FIVE DAY FACULTY DEVELOPMENT PROGRAM ON

"ADVANCEMENT IN THE FIELD OF MECHANICAL ENGINEERING"

Session : 05
Date : 27-06-2020
Time : 10:30AM to 12:00PM

Topic : Latest Trends of Auto Industry



Mr. T M I. Sankar
Divisional Manager,
Ashok Leyland,
Chennai.

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology (NIT)
RAICHUR.

Satish Kumar	satishkumarpolicherla@gmail.com	3:18 AM	3:54 AM	36
Satyendra		2:42 AM	3:34 AM	51
Shaik Riyaz Basha		3:01 AM	3:14 AM	13
Shaik Riyaz Basha		2:19 AM	2:27 AM	7
Shaik Riyaz Basha		2:34 AM	2:53 AM	19
Shantanu Halder		1:48 AM	3:55 AM	127
Shiva Krishna J		2:13 AM	3:40 AM	86
Shivakumar Rachoti	kartikrachoti@gmail.com	1:36 AM	1:50 AM	14
Shivakumar Rachoti	kartikrachoti@gmail.com	1:53 AM	3:31 AM	98
Shweta Bamboria	shwetajain2205@gmail.com	1:53 AM	2:25 AM	32
Shweta Bamboria	shwetajain2205@gmail.com	2:38 AM	3:02 AM	23
Shweta Bamboria	shwetajain2205@gmail.com	3:04 AM	3:55 AM	51
Siddhalingesh	pappu042@gmail.com	2:02 AM	2:02 AM	0
Siddhalingesh	pappu042@gmail.com	2:13 AM	2:16 AM	3
Srikar G Kulkarni		3:13 AM	3:54 AM	41
Srikar G Kulkarni		1:35 AM	2:36 AM	60
Srikar G Kulkarni		2:42 AM	2:59 AM	16
Srikar G Kulkarni		2:46 AM	3:00 AM	14
Srikar G Kulkarni		2:59 AM	3:55 AM	56
Srinidhi Acharya S R	sri1660@gmail.com	1:51 AM	2:33 AM	41
Srinivasan		1:35 AM	3:54 AM	138
Sriranga B.K	srirangabk@gmail.com	2:13 AM	3:49 AM	96
Srishailareddy		1:50 AM	1:57 AM	6
Suhas U		2:04 AM	2:06 AM	1
Suhas U		2:13 AM	2:14 AM	1
Suhas U		2:21 AM	3:55 AM	94
Sunil Prashanth Kumar	sunilbeck@gmail.com	3:08 AM	3:50 AM	41
Sunkappa	sunkapparjs@gmail.com	1:49 AM	3:55 AM	126
Suresh S	itssuresh.mech@gmail.com	1:36 AM	2:13 AM	36
Suresh S	itssuresh.mech@gmail.com	2:48 AM	2:59 AM	10
Sushma S		1:58 AM	2:30 AM	32
Sushma S		2:43 AM	2:52 AM	9
T SivaKrishna		2:27 AM	3:30 AM	62
TANBIR		1:37 AM	3:55 AM	138
Tejaswini. M		2:34 AM	2:35 AM	0
Tejaswini. M		2:36 AM	3:24 AM	48
USER		2:23 AM	3:41 AM	78
V. RANJAN	ranjanvengadesan@gmail.com	2:13 AM	3:55 AM	101
Victor Richardson	victorthermal92@gmail.com	1:55 AM	2:25 AM	30
Victor Richardson	victorthermal92@gmail.com	2:40 AM	3:55 AM	75
Vijay R B		1:47 AM	2:09 AM	22
Vikram Kedambadi		3:32 AM	3:55 AM	23

27/08/2020

Navodaya Institute of Technology (Affd by VTU)
RAICHUR-584 103

Head of the Department
Department of Mechanical Engineering
Navodaya Institute of Technology
RAICHUR

NAVODAYA INSTITUTE OF TECHNOLOGY * RAICHUR

Dr. JAWAHAR PAULRAJ
Assistant Professor
Department of Mechanical Engineering
National Institute of Technology , Agartala
Tripura - 799046
Email: dr.p.jawahar@gmail.com
Mobile: 9444600982.



Date of Joining NIT Agartala : March 2019

ACADEMIC QUALIFICATION

2007:	Ph.D. in Metallurgical and Materials Engineering from Indian Institute of Technology, Madras, Tamil Nadu.
2002:	M.E. in Production Engineering from Annamalai University, Tamil Nadu.
1999:	B.E. in Mechanical Engineering from Dr. Sivanthi Aditanar College of Engineering (Manonmaniam Sundaranar University), Tamil Nadu.

Completed Research Projects

Development of high wear resistive natural rubber composites for aircraft and automotive applications

Scheme	: DST – Young Scientist Fast Track Scheme
Funding Agency	: DST
Grants Allocated	: Rs. 20,85,000 / -
Duration	: 29 October 2014 to 28 April 2018

Area of Interest

- Synthesis and characterization of polymer nanocomposites
- Development of CNT-Rubber Nanocomposites.
- Liquid processing of aluminum and aluminum metal matrix composites via stir casting technique.
- Tribological studies on polymer composites

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

Post Doctoral Fellowship

Worked as a Post Doctoral Research Fellow in Department of Mechanical Engineering, Durban University of Technology, Durban, South Africa (October 2nd 2006 – December 2nd 2007).

Ph.D. Supervision

Completed

Sl.No.	Title of the Dissertation	Name of Scholars	Year of Completion
1	A Study on Development and Performance of Graphene Oxide – Glass Fibre Containing Nylon Nanocomposites	R. Geetha	2018
2	Experimental and CFD Studies on Thermal Performance of Solar Cavity Receiver	D.R. Rajendran	2018

Ongoing

Sl.No.	Research Topic	Name of Scholars
1.	Development of Polymer Gears for Engineering Application	Mr. Pradeep Kumar

Professional Society

Indian Welding Society (Membership No: L01391)



Board of Studies

Name of the University	Name of the Programme	Regulation
Anna University	Production Engineering	2017 for Affiliated Colleges
Anna University	Aerospace Engineering	2017 for Affiliated Colleges

Books / Chapter Published

1. M. Balasubramanian and **P. Jawahar,** Chapter 9: Nanocomposites Based on Inorganic Nanoparticles : Polymer Nanocomposites based on Inorganic and Organic Nanomaterials (Wiley) Scrivener Publishing. Page No. 257 – 346 July 2015. ISBN No.: 978-1-118-38509-8.

2. M. Balasubramanian and **P. Jawahar,** Chapter 13: Rubber - CNT Nanocomposites: Advances in Nanostructured Composites: Volume 1: Carbon Nanotube and Graphene Composites. CRC Press (Taylor and Francis Group). (Accepted) 2019. ISBN No.: 978-1-482-23663-7.

International Journal

1. R. Geetha, **P. Jawahar** (2018). Abrasive Wear Analysis on Graphene Oxide Based Hybrid Nanopolymer Composites. Surface Review and Letters, (Article Available Online).

2. D.R. Rajendran, E. G. Sundaram, **P. Jawahar** (2018). Experimental Studies on the effect of enhanced thermal conductivity of SiC + water nanofluid in the performance of small scale solar parabolic dish receiver. International Journal of Nanoscience. Vol. 17 (Article Id. 1760025).

3. R. Geetha, P. **Jawahar**, K. Kanny. Analysis Of Wear Behavior of Graphene Oxide - Polyamide Gears for Engineering Applications, Surface Review and Letters, Vol. 25, No. 3 (2017) 1850018

4. D.R. Rajendran, E. G. Sundaram, **P. Jawahar** (2017). Experimental Studies on the Thermal Performance of a parabolic dish Solar receiver with Heat Transfer Fluids (SiC + water nanofluid and water). Journal of Thermal Science, Vol. 26, 263 – 272.



5. **P. Jawahar**, P. Kartheeswaran (2016), Development of Rubber Nanocomposites for Engineering Application, Applied Mechanics and Materials, Vol. 852, 61-65.

6. D.R. Rajendran, E. Ganapathy Sundaram, **P. Jawahar**, R. Pachaiyappan and G. Naveen Raj (2014) Studies on Suitability of Heat Exchangers for Solar Receivers – Experimental Simulation and Numerical Investigation. Energy Procedia. Vol. 54, 557-568.

7. R. Geetha and **P. Jawahar** (2014) Comparative investigations on the mechanical and tribological properties of glass fibre reinforced thermoplastic and blended grapheme-oxide hybrid thermoplastic nanocomposites. Applied Mechanics and Materials. Vol. 591, 85-88.

8. P. Prabhu, **P. Jawahar**, M. Balasubramanian and T.P. Mohan (2013) Machinability study of Hybrid Nanoclay-Glass Fibre Reinforced Polyester Composites. International Journal of Polymer Science, 2013, Article Id. 416483.

9. Y. Shi, K. Kanny and **P. Jawahar** (2009) Hybrid nanocomposites: processing and properties, Journal of Advanced Composite Materials, 18(4), 365 – 379.

10. **P. Jawahar**, K. Kanny and M. Balasubramanian (2009) Influence of nanoclay addition on properties of unsaturated-polyester nanocomposite gel coat system. Journal of Polymer Engineering, 29, 563-580.

11. V. K. Moodley, K. Kanny and **P. Jawahar** (2007) Mechanical and tribological behavior of clay-polypropylene nanocomposites. Journal of Material Science, 43, 7230-7238.

12. **P. Jawahar** and M. Balasubramanian (2006) Influence of nanosize clay platelets on the mechanical properties of glass fiber reinforced polyester composites. Journal of Nanoscience and Nanotechnology, 6, 3973-3976.

13. **P. Jawahar**, R. Gnanamoorthy and M. Balasubramanian (2006) Tribological behaviour of clay – thermoset polyester nanocomposites. Wear, 261, 835-840.

14. **P. Jawahar** and M. Balasubramanian (2006) Preparation and properties of polyester based nanocomposite gel coat system. Journal of Nanomaterials , 2006, 21656, 1-7.

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

15. **P. Jawahar**, R. Gnanamoorthy and M. Balasubramanian (2005) Flexural and tribological properties of polyester – clay nanocomposites. Journal of Material Science, 40, 4391 – 4393.
16. **P. Jawahar** and M. Balasubramanian (2005) Thermal, mechanical and water barrier properties of clay – polyester nanocomposites. International Journal of Plastics Technology, 9, 472-481.

Conferences

1. **P. Jawahar**, I. Sharon Marishka, V. Chandran, " Influence of Multiwall Carbon Nanotube on Mechanical and tribological Properties of Natural Rubber". APM – 2018, CIPET, Bhubaneswar.
2. R. Geetha and **P. Jawahar,** "Tribological Analysis of Graphene Oxide Reinforced Hybrid Polyamide Bearings Under Radial Loading Conditions in Dry Rolling". International Conference on Recent Innovations in Production Engineering "RIPE-2017". Madras Institute of Technology, March 24-25, 2017
3. **P. Jawahar,** T. Maridurai, I. Manikandan and K. Ravichandran, "Tribological properties of hybrid clay- silica reinforced polyester nanocomposites". International Conference on Design, Analysis, Manufacturing and Simulation "ICDAMS 2016". Saveetha University, April 7-8, 2016. P. 619-623.
4. I. Manikandan, M. Gnanavel, K. Ravichandran, **P. Jawahar** and T. Maridurai. "Studies on effect of Ultrasonic Metal Welding using insert material and optimization of process parameters using response surface method and artificial neural networks.
5. **Dr. P. Jawahar** and P. Kartheeswaran, "Development Of Rubber Nanocomposites for Engineering Application". International Conference on Mechanical Engineering Design "ICMED 2016". SSN College of Engineering, 25-26, April, 2016. P. 82
6. S. Keerthiswaran and **P. Jawahar**, "Carbon Nanotube Based Epoxy Composites – A Review". International Conference on Composites, Biocomposites and



Nanocomposites. "ICCBN 2013", Durban University of Technology, December 2-4, 2013. P. 315-339.

7. R. Geetha, K. Kanny and **P. Jawahar**, "Tribological Properties of Hybrid Thermoplastic Nanocomposites". International Conference on Composites, Biocomposites and Nanocomposites. "ICCBN 2013", Durban University of Technology, December 2-4, 2013. P. 478-482.
8. S. Keerthi Easwaran, S. Devashankar and **P. Jawahar**, "Carbon Nanotubes Based Epoxy Composites". International Conference on Advances in Materials Processing and Characterizations, 2013. P. 398-401.
9. **P. Jawahar**, P. Prabhu, K. Kanny and M. Balasubramanian, An approach to use nanocomposite binders to recycle thermoset plastics. International Conference Nano Materials and Nano Technology "NANO 2010", KSR College of Engineering & technology, December 13 – 16, Tiruchengode.
10. P. Satish Kumar, S. Sundarvishak, P. Santhosh, M. Balasubramanian, M. Balasubramanian and **P. Jawahar**, "Recycling of Thermosetting Plastics: An Approach to use Nanocomposite Binders". International Conference on Nanoscience and Nanotechnology. "ICONN 2010", SRM University, February 24 – 26, 2010.
11. Y. Shi, K. Kanny and **P. Jawahar**, Characterization of layered silicate/S2-glass/epoxy nanocomposites for structural application, in: The Third International Conference on Structural Engineering, Mechanics and Computation, University of Cape Town, South Africa (2007).
12. **P. Jawahar** and K. Kanny, Hybrid nanocomposites: An experimental study for rehabilitation of concrete structures,in: The Third International Conference on Structural Engineering, Mechanics and Computation, University of Cape Town, South Africa (2007).
13. **P. Jawahar**, K. Kanny and M. Balasubramanian (2008) Influence of Nanoclay Addition on Properties of Unsaturated – Polyester Nanocomposite Gel Coat System. International conference of composite science and technology. Durban, South Africa, January 2007.



14. **P.Jawahar** and M.Balasubramanian. Influence of nanosize clay platelets on the mechanical properties of glass fiber reinforced polyester composites. International conference " ICMAT IUMRS – ICAM, Singapore, July, 2005.

22/06/2020

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology (NIT)
RAICHUR

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR



Dr. Ranjeet Kumar Sahu works as an Assistant Professor in the Department of Mechanical Engineering, National Institute of Technology Karnataka (NITK), Surathkal, Mangaluru, Karnataka.

- He received the **B.E. and M.Tech** degree in 2003 and 2011 from NIT Rourkela, Rourkela, Odisha
- He received the **Doctoral Degree** in 2016 from IIT Madras, Chennai

TEACHING EXPERIENCE

- Two years and 4 months teaching and research experience as a Research Assistant Professor in the Department of Mechanical Engineering, SRM University, Kattankulathur Campus, Chennai – **2017-2019**
- One year and 5 months teaching experience as an Associate Professor in the Department of Mechanical Engineering, QIS College of Engineering and Technology (Autonomous), Ongole, Andhra Pradesh – **2015-2017**
- Three months teaching experience as an Assistant Professor in the Department of Mechanical Engineering, ITER, SOA University (Deemed), Bhubaneswar, Orissa- **2011**
- Five years and above teaching experience as a Senior lecturer in Mechanical department, DEGREE ENGG. COLLEGE, SMIT-Berhampur (Orissa) – **2003-2009**

AWARDS AND ACHIEVEMENTS

- Dr. APJ Abdul Kalam Award for Young Scientist 2018, Marine Labs, Research and Development (in association with CSIR), Chennai
- Prof. M S Shanmugam Best PhD Thesis Award, IIT Madras, 2016
- Best M.Tech Award in M.Tech (Mechanical Engg), NIT-Rourkela, 2012
- 1st position award in M.Tech-1, Institute Day, NIT-Rourkela, 2010
- Recipient of Ministry of Human Resource Development (MHRD) scholarship at IIT Madras, Chennai, India, 2011-to-2015
- Recipient of Ministry of Human Resource Development (MHRD) scholarship at NIT Rourkela, India, 2009-2011.
- Ranked in the top 8% in all India GATE Exam, 2009
- Ranked in the top 10 (4th Rank), B.E. Mechanical Engg, 2003
- 2nd position in all states G.K competition, Rajasthan in the year 1994



- **Research Output**

  - Peer Reviewed Journals : 12
  - Conferences : 15
  - Book : 01
  - Book Chapters : 02
  - Patents : 02
  - Invited Talks : 09

- **Recent Sponsored Projects Undertaken (SERB, DST-Govt. of India):** 04 (Under Review)

- His **Current Research Areas** includes
  - Micro and Nano Machining
  - Nanomaterials Synthesis
  - Precision Engineering
  - Additive Manufacturing

- **Research Guidance**

  Completed: B.Tech: **11**; M.Tech: **05**

  Ongoing: PhD: **04**; M.Tech: **01**

- He is conducting regular **Short-term Course/Workshop/Seminars** on cutting edge technologies like **Micromachining, Industry 4.0, Advanced Materials, Mechatronics and Optimization techniques** for Engineering College Teachers and students, Industries, etc.

- He was served as a **Product Development Head** of QIS College of Engineering and Technology (Autonomous), Ongole - 523272, Andhra Pradesh.

- He was served as a **Faculty Research Establishment Head** in Machine Shop, Manufacturing Division, Department of Mechanical Engineering, SRM University, Kattankulathur.

- He has designed a **new course – Advanced Micro-machining Systems (AMMS-SDC Model)** to improve the knowledge of the undergraduate students in the area of manufacturing and design, so that they can build up their expertise skills and can apply in a given specialized industrial field.

- Last but not the least, he is a **Member of Professional Bodies** like International Association of Engineers, International Association of Computer science and Information technology, etc.

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

21/11/2020

Navodaya Institute of Technology (NIT)
RAICHUR-584 103



Profile of K *V Srinivasan*

Qualification:

- B.E (Mechanical) Year 1991 from Madurai Kamaraj University
- M.E (Cryogenics) Year – 1994 from Gujarat University
- Dr-ing in Cryogenics from VelTech University, Chennai

Professional Experience:

Joined Low Temperature Facility, TIFR in the year 1999

Current Designation: Senior Scientific Officer and Engineer In charge, Low Temperature Facility, TIFR

Academic Activities:

- Course Lecturer for the first year students of Graduate Studies, Indian Neutrino Observatory (INO) since 2011.
- Course Lecturer for the Final year students of BSc-MSc Physic (Integrated) students of Centre for Excellence in Basic Sciences (CBS), Mumbai
- Course lecturer for elective subject in Physics for the Final year MSc Students, Mumbai University.

Technical Presentation

- K V Srinivasan gave many technical presentation within TIFR for Visiting Students Research Program (VSRP), ASET forum, Departmental colloquium, Conference & Meetings etc
- Other colleges/institutions such as DAE-CBS VJTI, VIIT, IISC Bangalore IISER-TVM, Jadavpur Univ, INSPIRE program etc.
- Technical presentation in more than 9 National cryogenic Conferences & 7 International cryogenics Conferences (Cryo-Ops-CERN Geneva, ACASC 2009-Japan, LT-26- Beijing, ACASC2013-Turkey, ICEC-2016 - New Delhi, Cryo-Ops 2018- Beijing)

Publications:

More than 18 publication (since 2008) of technical papers in various journal such as IOP Science, Indian Cryogenic Council, AASCIT, Elsevier etc.

Projects / R&D

- Project guide for many BTech & MTech students [~ 15]
- Research collaboration with various institutes such as BARC-Mumbai; IIT-BOM; IIT-Chennai; VIIT-pune; RGIT-Andheri, VJTI-Matunga etc.
- BRNS – research funding for various cryogenics developments

Others:

- Elected member and Treasurer of Indian Cryogenic Council (West Zone)
- External expert member in Cryogenics for various institutes such as IPR(Gandhinagar), IISER (Bhopal), IISER (TVM), IISER (Pune), IISER(Behrampur), IISC –Bangalore, University of Hyderabad etc.

PRINCIPAL 22/06/2020
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology (NIT)
RAICHUR.

Curriculum Vitae

Dr. S. Mohamed Iqbal,M.E., Ph.D.,
HIG 448, III Main Road,
Eri Scheme, Mugappair – 600037.
E-mail: iqbalmech18@gmail.com
Ph.no. +91-9884609996.



Dr. S.Mohamed Iqbal (SCOPUS ID: 57193929230) currently working as an Associate Professor in the Department of Mechanical Engineering at Vel Tech Rangarajan Dr. Sagunthala R&D Institute of Science and Technology. I received my doctorate in Mechanical Engineering from Anna University, Chennai in 2018. My research concerted on Inserts and Nanofluids for Heat Transfer Applications. I have published over 8 articles in the peer-reviewed international journals and over 4conference proceedings.

Education

Ph. D.	**Thermal Engineering (Mechanical Engineering)** Anna University, Chennai.600025, Tamil Nadu, India **Thesis:** Study of Heat Transfer Characteristics On Forced Convection System With Nanofluid **Supervisor:** Dr.C.Sundarraj, Professor (Rtd.)	(2018)
M.E.	**Master of Engineering– Thermal Power** Annamalai University, Annamalai Nagar. - 601802, Tamil Nadu, India **Thesis:** Experimental Study on fouling factor with 8.31 CGPA	(2005)
B.E.	**Bachelor of Engineering– Mechanical Engineering** Annamalai University, Annamalai Nagar. - 601802, Tamil Nadu, India **Thesis:** Performance Analysis On Engine Emission By Using EGR And Catalytic ConverterWith 67.5%	(2002)

Academic Experience

- Presently – **Associate Professor** in the Department of Mechanical Engineering at Vel Tech Rangarajan Dr. Sagunthala R&D Institute of Science and Technology, Chennai since Jan2015 to till date.
- **Assistant Professor** in the Department of Mechanical Engineering at Sree Sastha College of College of Engineering , Chennai from July 2011 to Oct 2014.
- **Lecturer**, Department of Mechanical Engineering, DMI College of Engineering, Chennai-123, fromJan 2010 to May2011.
- **Lecturer**, Department of Mechanical Engineering, Aringnar Anna Institute of Science and Technology, Chennai fromJan 2006 to Dec 2009.

Administrative Responsibilities held

- Experienced in NBA Accreditation.
- Experienced in NAAC Accreditation.

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

- National level conference convened on 11 Sep 2014 at Sree Sastha College of Engineering.
- Department Symposium organized on 15 Feb 2014.
- Management representative for ISO certification at Sree Sastha College of Engineering
- Head of Department (i/c), in the Department of Mechanical Engineering at Sree Sastha College of Engineering from April 2013 to Oct 2014.
- Erection of Labs such as Thermal Engineering lab, heat transfer lab and Fluid mechanics and machineries Lab.

Industrial Experience

- **Technical Assistant**, Anbu Engineering Contractors in 108 MW Combined Cycle, Thirumakottai (K) Gas Turbine Power Station, TNEB, Mannargudi.From Jun. 2002 to Sep.2003.

Professional Interests

- Nanofluids
- Heat transfer enhancement techniques
- Heat exchangers.
- IC engines
- Phase change materials

Subjects Taught

- Thermodynamics
- Thermal Engineering
- Heat transfer
- Gas dynamics
- Engineering mechanics
-

Reviewer for the following International Journal:

- International Journal of Ambient Energy.

Journal Publications

1. **S. Mohamed Iqbal,** K. Karthik, Jee Joe Michael, 2018, "Performance analysis on improved efficiency in a hybrid solar still and solar heater", International Journal of Ambient Energy, DOI: 10.1080/01430750.2018.1517685, Taylor & Francis Publications.
2. D.Yogaraj, **S. Mohamed Iqbal**, R. Gokulakrishna, Meikandan Megaraj, "Performance Test and Emission Characteristics on Diesel Engine with Alternate Fuel Blends and Nano Additives" International Journal of Ambient Energy, 2018,
3. Jee Joe Michael, **S.Mohamed Iqbal**, S. Iniyan, Ranko Goic, 2018, "Enhanced electrical performance in a solar photovoltaic module using V-trough concentrators", Energy, Vol. 148, pp. 605 – 613, Elsevier Publications. [Thomson Reuters Impact Factor: 5.182]
4. P. Rathnakumar, **S. Mohamed Iqbal**, Jee Joe Michael, S. Suresh, 2018, "Study on performance enhancement factors in turbulent flow of CNT/water nanofluid through a tube fitted with helical screw louvered rod inserts", Chemical Engineering



&Processing: Process Intensification, Vol. 127, pp. 103 – 110, Elsevier Publications. [Thomson Reuters Impact Factor: 3.096]

5. **S. Mohamed Iqbal,** M. Arul Prakasajothi, N. Dilip Raja, 2018, "Investigation on the thermal behaviour of titanium dioxide nanofluid", Materials Today Proceedings, Vol. 5(9), pp. 20608 – 20613, Elsevier Publications.
6. **Shajahan Mohamed Iqbal**, ChockalingamSundar Raj, Sambandan Arul&PalanisamyRathnakumar2017, 'Heat transfer intensification of Zirconia/water nanofluid', Journal of advancesinChemistry, vol.113, no. 3, pp. 1-8. [Impact factor:0.836]
7. **Iqbal, SM**, Raj, CS, Michael, JJ &Irfan, AM 2017, 'A Comparative Investigation of Al_2O_3/H_2O, SiO_2/H_2O AND ZrO_2/H_2O Nanofluid for Heat Transfer Applications',Digest Journal of Nanomaterials and Biostructures, vol. 12, no. 2, pp. 255-263. [Thomson Reuters, Impact factor:1.131]

PG/UG Projects

- Experimental analysis on spiral, shell & coil, and concentric heat exchangers for development of cooling/heating system.
- Investigation on characterization, preparation and testing of various nanofluids for the heat transfer applications.

Conference/Workshop /FDP and Seminarsattended

- INTERNATIONAL CONFERENCE - 2
- NATIONAL CONFERENCE - 4
- SEMINARS AND WORKSHOPS - 10

Declaration

I hereby declare that all the information provided above is true to the best of my knowledge and belief.

Date: Yours faithfully

Place: Chennai

(Dr.S. Mohamed Iqbal)

PRINCIPAL 27/06/2020
Narsimha Institute of Technology (Off.)
RAICHUR-584 103

Head of Department
Department of Mechanical Engineering
Narsimha Institute of Technology (Off.)
RAICHUR

INSTITUTE OF TECHNOLOGY
RAICHUR
NARSIMHA



My brief Profile as follows:

- Name : TML Sankar.
- Qualification : M.tech, EMBA(IMD -Switzerland)
  IMD is one of the Top B-Schools in the world.

  M.Tech from NIT,Trichy,Tamil Nadu.

  B.tech from Sri VenkateshwaraUniversity,Thirupathi, Andhra Pradesh.

- Present Organization: ASHOK LEYLAND PVT LTD (10+years Experience).
- Designation : Divisional Manager.
- Awards : Ashok leyland Best Technology award 2015.
- Winner of Hinduja Excellence award 2016.

TQM-Practitioner &Certified by DEMING prize committee Japan.

- Social Impact : Leader & Member of Corporate social responsibility (CSR), in helping school students, identify the needs, infrastructure & Sanitation.
- Life Dream : To become MD/CEO.
- Hobbies : Travelling, Reading books.
- Countries Visited: Switzerland, Germany, France, Thailand etc.
- International Network: SAE, DEMING committee CEO Forum.
- Topic : Recent Trends of Auto industry & Future Challenges 28/10/2017(Saturday).

23/10/2017

PRINCIPAL
Navodaya Institute of Technology
RAICHUR-584 103

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology
RAICHUR.

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

NET's

NAVODAYA INSTITUTE OF TECHNOLOGY

(Affiliated to Visveswaraya Technological University, Belgavi, approved by AICTE, New Delhi & recognized by Government of Karnataka)

Navodaya Nagar, Begingera Road, Raichur-584103, Karnataka

Website: https://engineering.navodaya.edu.in/

ONLINE FACULTY DEVELOPMENT PROGRAM

ON

"ADVANCEMENT IN THE FIELD OF MECHANICAL ENGINEERING"

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Certificate of appreciation

This is to certify that Dr./Mr./Ms./Mrs. *Mr. Raja Shakarappa*

With sincere thanks for your valuable support in successful completion of five day's online Faculty Development Program on "Advancement in the field of Mechanical Engineering", organized by Department of Mechanical Engineering, Navodaya Institute of Technology, Raichur (Karnataka) from 23rd to 27th June 2020.

Prof. Md. Umar
Coordinator

Dr. P Rathnakumar
Convener/HoD-Mech

Dr. M V Mallikarjuna
Principal

Date: 24.1.2017

From

Dr.K.M. Palaniswamy,
Professor and Head,
Navodaya Institute of Technology,
Raichur-584103

To

The Principal,
Navodaya Institute of Technology,
Raichur-584103

Sir,

Subject: Permission for conducting two Days FDP on 17.2.2017 and 18.2.2017-Reg

From ECE dept., we are going to conduct FDP on **Digital communication System Design** in association with VEDLABS, Bangalore.

The FDP is to be carried out for two days 17.2.2017 (Friday) and 18.2.2017 Saturday. So please provide permission to conduct the above FDP. The proposed expense is attached in next page. The Expected expense is Rs. 20, 000/- whose details are provided in next page.

Thanking you

The faculty Coordinators are

1. Prof. Madhuri Devi.CH
2. Prof. Md.Shaoib Madni

Approved 24/1/17

Yours Faithfully

24/1/17

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
Raichur-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

PRINCIPAL 13/8/2018
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned with CamScanner

The first day activity will be as follows

Theory: Basics of Digital Communication. Hands on: - Simulating a Communications Link - 16-QAM with MATLAB functions - 16-QAM with simulink Blocks. - QPSK and OFDM with MATLAB system objects. - Accelerating BER simulation using the Parallel computing toolbox.(6Hours)

The Second day activity will be as follows

Hands on: - Mini Project development. - GUI toolbox introduction. - Designing GUI layout and Formatting. - Writing code for each GUI events. (6hours). Please give confirmation at the earliest. (6Hours)

In this Regard, It is feasible to fix Rs. 300/-Per head for participants.

Proposed Budget for work shop

S.No	Matter of Event	Requirement	Approximate cost
1	Travelling Charges for Two Trainees	Rs. 3000	Rs.3000
2	Brochure Preparation	For 100	Rs. 1000
3	Sending Hardcopy Brochures	Rs. 500 for 100 Brochures	Rs. 500
4	Preparing Banners for Seminar hall	Rs. 500 for 2 banners	Rs. 500
5	Certificate preparation	Rs.1000	Rs. 1000
6	Remuneration for Resource persons	Two persons	Rs.15,000
7.	Other Miscellaneous		Rs. 2000/-
		Total	Rs. 23,000/-
		Expected income	Rs. 3,000/-
		Net Expense	Rs. 20,000/-

In this Regard, We need Rs. 20, 000/- for conducting of Workshop effectively in advance. The accommodation facility for Resource persons and External Participants and Food expenditures can be considered from Management Side.

Programme Coordinator

HOD

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

13/2/2017

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Scanned with CamScanner

NET's
NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

Faculty Development Programme - PROPOSAL

Date: 07.02.2017

1	**ACADEMIC YEAR**	2016-17
2	**TYPE OF THE PROGRAMME PROPOSED**	*Faculty Development Programme*
3	**TITLE OF THE PROGRAMME:**	**"Digital Communication System Design"**
4	**BROAD OBJECTIVE OF THE PROGRAMME:**	This Program aims to give hands on training in Digital Communication System Design using MATLAB. **Major Course Contents:** • Simulating a Communications Link • 16-QAM with MATLAB functions • 16-QAM with simulink Blocks • QPSK and OFDM with MATLAB system objects • Accelerating BER simulation using the Parallel computing toolbox • Mini Project development. • GUI toolbox introduction. • Designing GUI layout and Formatting. • Writing code for each GUI events.
5	**NAME OF THE INSTITUTION**	NETs/NAVODAYA INSTITUTE OF TECHNOLOGY
6	**NAME OF THE DEPARTMENT(S)**	ELECTRONICS AND COMMINICATION ENGINEERING
7	**BENEFICIARY**	Faculty Members
8	**DATE/DURATION OF SUCH PROGRAMMES CONDUCTED IF ANY IN THIS ACADEMIC YEAR:**	17.02.2017 to 18.02.2017
9	**ELIGIBILITY CRITERIA SET:**	Faculties from Electronics and Communication Engineering.
10	**NUMBER OF DAYS REQUIRED TO ORGANISE THE PROGRAMME**	Two Day FDP
11	**PROPOSED DURATION OF THE PROGRAMME**	12 hours

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

PRINCIPAL 13/2/2017
Navodaya Institute of Technology (NIT),
RAICHUR-584 103.

Scanned with CamScanner

12	**TOTAL NO. OF PERIODS REQUIRED FOR THE PROGRAMME**	12 Periods
13	**NO. OF PERIODS REQUIRED PER DAY**	06 Periods
14	**LOCATION DETAILS:**	NETs NIT Auditorium
15	**MINIMUM NO OF PARTICIPANTS:**	30
16	**DETAILS OF SPONSORERS:**	-
17	**DETAILS OF RESOURCE PERSONS:**	**Dr. Hiren Parmar** has done his Post Graduation from UK i.e. MS (Information and communication Engineering), from University of Leicester (UK). He has graduated from North Maharastra University in Electronics. He served in Renu Electronics for 1 year in UK. Later he worked as software engineer in Rajesh Electronics Bangalore for 2 years. **Dr. Hiren P** started his own company i.e. VEDLABS Bangalore. He is Director of **Vedlabs** since 2009.
18	**REGISTRATION FEE WITH CERTIFICATION:**	Rs 300 per participant
19	**REGISTRATION FEE WITHOUT CERTIFICATION:**	Rs - per participant.
20	**FACULTY COORDINATOR:**	1. CH.Madhuri Devi 2. Md. Shoaibuddin Madni
21	**EVENT ORGANISERS (STUDENTS):**	Mr. Kiran Kumar Hiremath Ms. Ajmal Firdous
22	**PROGRAMME PROPOSED BY:**	Department of ECE
23	**ANY OTHER RELEVANT MATTER:**	Nil
24	**ESTIMATED BUDGET IN Rs:**	Rs.20,000

This is to the Honorable Chairman of NIT for kind approval:

PRINCIPAL

HOD Department of
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

EVENT COORDINATOR

APPROVED/~~NOTAPPROVED~~

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

PRINCIPAL
Navodaya Institute of Technology
RAICHUR-584 103

Scanned with CamScanner

Registration Form

(photocopy may be used if required)

Name (in BLOCK letters): _____

_____

Academic Qualification : _____

Experience: _____

Designation & Department: _____

College Address: _____

_____

_____

Name of Affiliated University: _____

E mail : _____

Contact Number: _____

DD Number/ Date _____

Declaration:

The information furnished above is true to the best of my knowledge.

Date:

Place: Signature of the Applicant

Mr./Ms./Dr. _____ is an employee (faculty)/PG student of our Institute. He / She will be permitted to attend the programme if selected.

Date:

Place:

Signature & Seal

HOD/Head of the Institution

Electronics & Communication Engineering

Navodaya Institute of Technology (NIT)

RAICHUR-584 103

Chief Patron
Sri. S. R. Reddy, Hon'ble Chairman
Navodaya Education Trust (NET®), Raichur.

Patron
Sri. N. Bharath Reddy, Trustee
Navodaya Education Trust (NET®), Raichur.

Chief Convener
Dr Shivaprakash C. K., Dean/Principal, NIT, Raichur.

Convener
Dr. K M Palaniswamy, HOD,ECE, NITR

Coordinator
Prof. CH.Madhuri Devi
Associate Professor, Dept of ECE, NITR.
Email:madhuridevi.ch@navodaya.edu.in
madhurichodey@gmail.com
Contact no. : +91- 9666713819

Co-Coordinator
Prof. Md Shoaibuddin Madni
Assistant Professor Dept of ECE, NIT
Email: shoaibuddin.madni@navodaya.edu.in
shoaib.madni7@gmail.com
Contact no. : +91- 8792374743

Organizing Committee

1. Prof. Rammurthy D
2. Prof. Channaveeramma.E
3. Prof. Priyadarshini N
4. Prof. Sachin kumar
5. Prof. LaxmikanthReddi
6. Prof. Archana Patil
7. Prof. Gauravi Shetty

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA
Institute of Technology
RAICHUR



Hands On Two Days Faculty Development Programme On

Digital Communication System Design

in association with VEDLABS, Bangalore.

February 17th & 18th 2017

ORGANIZED BY

Department of Electronics & Communication Engineering.

Navodaya Education Trust®
NAVODAYA INSTITUTE OF TECHNOLOGY
Navodaya Nagar, Bijiangera Road,
Raichur-584103, Karnataka, India.

Scanned with CamScanner

About the Institute:

Navodaya Institute of Technology(NIT), Raichur, established in 2008, is affiliated to Visvesvaraya Technological University (Belagavi). The institute is approved by All India Council for Technical Education (AICTE), New Delhi.

The guiding spirit and the driving force for all the progress and achievements of the Institution is our Honourable chairman of Navodaya Education Trust (NET®), Sri S. R. Reddy. NIT focuses on promoting quality technical education in north Karnataka region and Hyderabad-Karnataka region in particular.

The education is of paramount importance for socio-economic development. The institute focuses on integrated scientific training with value based education, to enable overall development of the aspirants.

NIT is all-inclusive campus, with Administrative block, Library block, independent Academic blocks for each department, Separate Hostels for Boys and Girls, Canteen, Bank(SBI), in-campus ATM , Temple, Gym and Playground, all housed in a vast campus of more than 30 acres land surrounded by hillocks and lake.

About the Department:

The Electronics and Communication Engineering department aims in delivering excellence in theoretical and experimental value based education to students. The Department was started in the year 2008 with an intake of 60 students with the vision to provide the quality technical education. The department has good infrastructure with well-equipped laboratories and necessary softwares.

The Department has bagged many ranks in GATE. Many students were placed in various prestigious MNC's and pursuing their higher studies in NIT's and IIT's.

The Department also offers Postgraduate programme in Digital Communication and Networking (DCN), was started in the academic year 2014-15.The department has highly qualified, experienced and dedicated faculties with PhD and Post graduation (M.E. / M.Tech.) degrees. The workshops and Training programmes are planned regularly for the benefit of students and faculty members.

Vision: To excel in education and research in Electronics & Communication Engineering, thereby providing service to the society and human kind.

Mission: To create competent professionals by training the students in the design & implementation of Electronics and Communication Engineering products helping there by students to develop the skills to solve technological problems of modern society.

About the Resource Persons:

Dr. Hiren Parmar has done his Post Graduation from UK i.e. MS (Information and communication Engineering), from University of Leicester (UK). He has graduated from North Maharastra University in Electronics.
He served in Renu Electronics for 1 year in UK. Later he worked as software engineer in Rajesh Electronics Bangalore for 2 years.

Dr. Hiren P started his own company i.e. VEDLABS Bangalore. He is Director of Vedlabs since 2009.

VED Labs is a company with professionals teamed up to offer the quality and apt solutions for the business needs. A place where imaginative minds have the opportunity to explore new ideas and implement cutting edge software solutions. Our professionals are experts and are abreast of latest technology and are proven team leaders in software industry. Their vision, technologies, tools, information organizations, methods and mind-sets have emphasized on performance and high quality standards.

About the workshop:

This workshop aims to give hands on training in Digital Communication System Design using MATLAB.

Major Course Contents:

- Simulating a Communications Link
- 16-QAM with MATLAB functions
- 16-QAM with simulink Blocks
- QPSK and OFDM with MATLAB system objects
- Accelerating BER simulation using the Parallel computing toolbox
- Mini Project development.
- GUI toolbox introduction.
- Designing GUI layout and Formatting.
- Writing code for each GUI events.

RESOURCE PERSONS:

1. Dr. Hiren Parmar, Director Vedlabs -Bangalore
Education: MS (Information and communication Engineering), from University of Leicester (UK)

2. Mr. Dileep M. D, Researcher, Vedlabs -Bangalore.

Important dates

Workshop date : February 17th & 18th 2017

Last date for registration : 15th Feb 2017.

Registration fees : Rs. 300 /-

Registration fee may be paid in the form of DD in favour of **Principal, Navodaya Institute of Technology**, payable at **SBI, NET campus, Raichur.**

VENUE : NIT Auditorium, NIT, Raichur

Note:

1. Accommodation will be provided on prior request.
2. Lunch, Tea will be provided during the training in the campus.

Scanned with CamScanner

Registration Form

(photocopy may be used if required)

Name (in BLOCK letters): Mrs. Savita Naik

Academic Qualification: M.Tech (CPE)

Experience: 09 years

Designation & Department: Lecturer Tech

College Address: GPT, near LVG College Raichur

Name of Affiliated University: DTE, Bangalore

E mail : savitanaikgpt@gmail.com

Contact Number: 9481547012

DD Number/ Date

Declaration:

The information furnished above is true to the best of my knowledge.

Date:

Place: Signature of the Applicant

Mr./Ms./Dr. Savita Naik is an employee (faculty)/PG student of our institute. He / She will be permitted to attend the programme if selected.

Date:

Place:

Signature & Seal
(HOD/Head of the Institution)
Govt. Polytechnic, RAICHUR.

Chief Patron
Sri. S. R. Reddy, Hon'ble Chairman
Navodaya Education Trust (NET®), Raichur.

Patron
Sri. N. Bharath Reddy, Trustee
Navodaya Education Trust (NET®), Raichur.

Chief Convener
Dr Shivaprakash C. K., Dean/Principal, NIT, Raichur.

Convener
Dr. K M Palaniswamy, HOD,ECE, NITR

Coordinator
Prof. C H Madhuri Devi
Associate Professor, Dept of ECE, NITR.
Email:madhuridevi.ch@navodaya.edu.in
madhurichodey@gmail.com
Contact no. : +91- 9666713819

Co-Coordinator
Prof. Md Shoaibuddin Madni
Assistant Professor Dept of ECE, NIT
Email: shoaibuddin.madni@navodaya.edu.in
shoaib.madni7@gmail.com
Contact no. : +91- 8792374743

Organizing Committee

1. Prof. Rammurthy D
2. Prof. Channaveeramma E
3. Prof. Priyadarshini N
4. Prof. Sachin kumar
5. Prof. LaxmikanthReddi
6. Prof. Archana Patil
7. Prof. Gauravi Shetty

Head of Department

NAVODAYA
Institute of Technology
RAICHUR



Hands On Two Days Faculty Development Programme On

Digital Communication System Design

in association with VEDLABS, Bangalore.

VEDA
Versatile Engineering Designs

February 17th & 18th 2017

ORGANIZED BY
Department of Electronics & Communication Engineering.

Navodaya Education Trust®
NAVODAYA INSTITUTE OF TECHNOLOGY
Navodaya Nagar, Bijjangera Road
Raichur-584103, Karnataka, India.

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned with CamScanner

Registration Form

(photocopy may be used if required)

Name (in BLOCK letters): Mrs. Murthambika. S.M

Academic Qualification : B.E (E&CE)

Experience: 07 Years

Designation & Department: Lecturer TC dept

College Address: GPT, Near LVD Raichur

Name of Affiliated University: DTE, Bangalore

E mail : murthambika.sm@gmail.com

Contact Number: 9482420449

DD Number/ Date

Declaration:

The information furnished above is true to the best of my knowledge.

Date:

Place: Signature of the Applicant

Mr./Ms./Dr. Murthambika.S.M is an employee (faculty)/PG student of our institute. He / She will be permitted to attend the programme if selected.

Date:

Place:

Signature & Seal

HOD/Head of the Institution

Govt. Polytechnic, RAICHUR.

Chief Patron
Sri. S. R. Reddy, Hon'ble Chairman
Navodaya Education Trust (NET®), Raichur.

Patron
Sri. N. Bharath Reddy, Trustee
Navodaya Education Trust (NET®), Raichur.

Chief Convener
Dr Shivaprakash C. K., Dean/Principal, NIT, Raichur.

Convener
Dr. K M Palaniswamy, HOD,ECE, NITR

Coordinator
Prof. CH.Madhuri Devi
Associate Professor, Dept of ECE, NITR.
Email:madhuridevi.ch@navodaya.edu.in
madhurichodey@gmail.com
Contact no. : +91- 9666713819

Co-Coordinator
Prof. Md Shoaibuddin Madni
Assistant Professor Dept of ECE, NIT
Email: shoaibuddin.madni@navodaya.edu.in
shoaib.madni7@gmail.com
Contact no. : +91- 8792374743

Organizing Committee

1. Prof. Rammurthy D
2. Prof. Channaveeramma.E
3. Prof. Priyadarshini N
4. Prof. Sachin kumar
5. Prof. LaxmikanthReddi
6. Prof. Archana Patil
7. Prof. Gauravi Shetty

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA
Institute of Technology
RAICHUR



Hands On Two Days Faculty Development Programme On Digital Communication System Design in association with VEDLABS, Bangalore.

VEDA
Versatile Engineering Designs

February 17th & 18th 2017

ORGANIZED BY

Department of Electronics & Communication Engineering.

Navodaya Education Trust®
NAVODAYA INSTITUTE OF TECHNOLOGY
Navodaya Nagar, Bijiangera Road,
Raichur-584103, Karnataka, India.

Scanned with CamScanner

NET's
NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Ph. No.:08532-220330, Extn-703,702,713, 07353295425
e-mail:hodece.nit@navodaya.edu.in

Chief Patron
Shri. S. R. Reddy, Hon'ble Chairman,
Navodaya Education Trust (NET®),
Raichur.

Patron
Shri. N. Bharath Reddy., Hon'ble Trustee.
Navodaya Education Trust (NET®),
Raichur.

Chief Convener
Dr.Shivaprakash C. K.,
Principal and Dean, NIT, Raichur

Convener
Dr. K M Palaniswamy,
HOD/ ECE, NIT. Raichur

Coordinator
Prof. CH. Madhuri Devi,
Associate Professor, Dept of ECE, NITR.
Email: madhuridevi.ch@navodaya.edu.in
Contact no. : +91-9666713819

Co-Coordinator
Prof. Md Shoaibuddin Madni,
Assistant Professor, Dept of ECE, NIT
Email: shoaibuddin.madni@navodaya.edu.in
Contact no. : +91- 8792374743

Organizing Committee
1. Prof. Rammurthy D
2. Prof. Channaveeramma.E
3. Prof. Priyadarshini N
4. Prof. Sachin kumar
5. Prof. LaxmikanthReddi
6. Prof. Archana Patil
7. Prof. Gauravi Shetty

10.02.2017

Faculty Development Programme
on
"Digital Communication System Design"

To

The Principal,

Warm Greeting!!!

Kind attention: Head of the Department / ECE,

Dear Sir/Madam,

Sub: Invitation for attending faculty development programme on "Digital Communication System Design" reg.

We are pleased to inform that the Department of Electronics & Communication Engineering, Navodaya Institute of Technology is organizing a Faculty Development Programme on **"Digital Communication System Design"** during February 17th & 18th 2017, (2 days) at our college premises. This programme will equip the faculty to teach the subject in better way and also enable them to offer final year project in this specialization. We invite your faculty members to participate in this FDP from your esteemed institution.

Please see the brochure enclosed with this for your perusal and circulation among the faculty members of EEE/ECE/EIE department.

For further details please feel free to contact.

Thanks & Regards

Dr. K M Palaniswamy
Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology
RAICHUR-584 103

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned with CamScanner

NET's
NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

Tel : +91 8532-200115, 240334, Fax : +91-8532-240054
E-Mail :hodece.nit@navodaya.edu.in

CIRCULAR

Cir.No:2016-17/FDP/TF/02

Chief Patron
Shri. S. R. Reddy, Hon'ble Chairman,
Navodaya Education Trust (NET®),
Raichur.

Patron
Shri. N. Bharath Reddy., Hon'ble Trustee.
Navodaya Education Trust (NET®),
Raichur.

Chief Convener
Dr.ShivaPrakash C K,
Principal and Dean, NIT, Raichur

Convener
Dr. K M Palaniswamy,
HOD/ ECE, NIT. Raichur

Coordinator
Prof. CH. Madhuri Devi,
Associate Professor, Dept of ECE, NITR.
Email: madhuridevi.ch@navodaya.edu.in
Contact no. : +91- 9666713819

Co-Coordinator
Prof. Md. Shoaibuddin Madni,
Assistant Professor, Dept of ECE, NIT
Email: shoaibuddin.madni@navodaya.edu
Contact no. : +91- 8792374743

Organizing Committee
1. Prof. Rammurthy D
2. Prof. Channaveeramma.E
3. Prof. Priyadarshini N
4. Prof. Sachin kumar
5. Prof. LaxmikanthReddi
6. Prof. Archana Patil
7. Prof. Gauravi Shetty

10.02.2017

Respected Sir / Madam,
Greetings from Navodaya Institute of Technology, Raichur…

The department of Electrical & Electronics Engineering is organizing a Two day FDP on **"Digital Communication System Design"** The attached brochure may kindly be circulated among the students and faculty members / colleagues.

At various stages in engineering study, one comes across mathematical problems of engineering applications irrespective of the engineering discipline. A simple computational tool such as MATLAB can be quite handy to solve such mathematical problems, provided we can formulate the problems appropriately. This FDP aims to give hands on training in Digital Communication System Design using MATLAB.

We are pleased to inform that the department of Electronics and Communication Engineering, Navodaya Institute of Technology is organizing a Faculty Development Programme during February 17th & 18th 2017

In this context, we look forward your support and request you to encourage participation of professionals by circulating the attached brochure.

Thanking you,
Yours sincerely,

Dr. K M Palaniswamy,
Convener

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY * RAICHUR

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned with CamScanner

NAVODAYA INSTITUTE OF TECHNOLOGY

BIJENGERA ROAD, RAICHUR - 584103

INVITATION

Department of Electronics & Communication Engineering

HODs & Faculties are invited

to attend two days



"Digital Communication System Design"

From February 17th & 18th 2017

Venue: NIT, Auditorium

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

PRINCIPAL 18/2/2017
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

10/2/17
Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned with CamScanner

Schedule for Faculty Development Program (FDP) on **"Digital Communication System Design"**

Organised By

Navodaya Institute of Technology: Raichur (17.02.2017 to 18.02.2017)

Timing of Each Day Session

Date/Day	Session I 9:30AM-11:00AM	TEA BREAK	Session II 11:30AM-1:00PM	LUNCH BREAK	Session III 2:00PM-5:00PM
17.02.2017 Friday	• Simulating a Communications Link • 16-QAM with MATLAB functions		• 16-QAM with simulink Blocks • QPSK and OFDM with MATLAB system objects		• Accelerating BER simulation using the Parallel computing toolbox
18.02.2017 Saturday	• Mini Project development.		• GUI toolbox introduction.		• Designing GUI layout and Formatting. Writing code for each GUI events

10/2/17

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

PRINCIPAL 13/2/2017
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned with CamScanner

NET's

NAVODAYA INSTITUTE OF TECHNOLOGY, RAICHUR

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

SUMMARY OF FACULTY DEVELOPMENT PROGRAMME ON

"Digital Communication System Design "

Department of ECE organized faculty development programme on **"Digital Communication System Design"** was held at Navodaya Institute of Technology, seminar hall from 17th February to 18th February 2017. The session was inaugurated with prayer song followed by the welcome speech by the Head of the department, Dr. K M Palaniswamy.

Principal presided over the function. Dr. Hiren Parmar, Director Vedlabs –Bangalore and Mr. Dileep M. D, Researcher, Vedlabs –Bangalore were the resource persons.

First day Dr. Hiren Parmar explained about the Simulating a Communications Link, 16-QAM with MATLAB functions, 16-QAM with simulink Blocks, QPSK and OFDM with MATLAB system objects and Accelerating BER simulation using the Parallel computing toolbox. Second day Mr. Dileep explained about the Mini Project development, GUI toolbox introduction, Designing GUI layout and Formatting and Writing code for each GUI events. Totally twenty five faculty members were participated and benefited.



Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned with CamScanner

Date: 16.2.2017

From

Dr. K.M. Palaniswamy,

Professor and Head,

Navodaya Institute of Technology,

Raichur-584103

To

The Principal,

Navodaya Institute of Technology,

Raichur-584103

Sir

Subject: Seminar Hall for Workshop on "**Digital Communication System Design in association with VEDLABS, Bangalore**" on **Feb** 17th & 18th 2017.

We have proposed to conduct Workshop on "**Digital Communication System Design**" in association with VEDLABS, Bangalore on **Feb** 17th & 18th 2017 Friday and Saturday, Time-9.30Am-5.30Pm.For this Purpose, We need Seminar Hall on Feb 17th & 18th 2017, Friday and Saturday, Time-9.30Am-5.30Pm.

Thanking You

Approved
To Mr. Amit Sir
16/2/17

Yours faithfully

16/2/17

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
Raichur-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103
13/2/2017

Scanned with CamScanner

08.02.2017

From

Dr. K M Palaniswamy,

HOD/ ECE,

NIT, Raichur

To

The Dean cum Principal,

NIT, Raichur.

Sir,

Sub: Requisition for Accommodation and Food -Reg

I bring to your kind notice that we are conducting the Two days FDP on "***Digital Communication System Design***" our department on February 17th & 18th 2017. For that we want to arrange the accommodation and food for the Resource Persons in the above days. So kindly do the needful.

Resource Person Details,

1. Dr. Hiren Parmar,
   Director Vedlabs –Bangalore
2. Mr. Dileep M. D,
   Researcher, Vedlabs -Bangalore.

To Mr. Ravi for N.A'

Thanking you,

Head of Department
HOD/ECE
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY • RAICHUR

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned with CamScanner

Faculty Development Program Evaluation Form

We would appreciate if you could take a few minutes to share your opinions with us so we can serve you better.

FDP title: **Digital Communication System Design**

Date: **17th and 18th February 2017**

Resource persons: **Dr. Hiren P & Mr. Dileep M D**

	Strongly agree				Strongly disagree
1. The content was as described in publicity materials	1	2	3	4	5
2. The FDP was applicable to my research	1	2	3	4	5
3. I will recommend this FDP to other conservators	1	2	3	4	5
4. The program was well paced within the allotted time	1	2	3	4	5
5. The resource persons are good communicators	1	2	3	4	5
6. The material was presented in an organized manner	1	2	3	4	5
7. The instructor was knowledgeable on the topic	1	2	3	4	5
8. I would be interested in attending a follow-up, more advanced FDP on this same subject	1	2	3	4	5

9. Given the topic, was this FDP: ☐ a. Too short ☑ b. Right length ☐ c. Too long

10. In your opinion, was this FDP: ☐ a. Introductory ☐ b. Intermediate ☑ c. Advanced

11. What did you most appreciate/enjoy/think was best about the course? Any suggestions for improvement?

Well planned & executed
Thank you

Thank you!

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY * RAICHUR

Scanned with CamScanner

Scanned with CamScanner

Faculty Development Program Evaluation Form

We would appreciate if you could take a few minutes to share your opinions with us so we can serve you better.

FDP title: **Digital Communication System Design**

Date: **17th and 18th February 2017**

Resource persons: **Dr. Hiren P & Mr. Dileep M D**

	Strongly agree				Strongly disagree
1. The content was as described in publicity materials	1	2	3	4	5
2. The FDP was applicable to my research	1	2	3	4	5
3. I will recommend this FDP to other conservators	1	2	3	4	5
4. The program was well paced within the allotted time	1	2	3	4	5
5. The resource persons are good communicators	1	2	3	4	5
6. The material was presented in an organized manner	1	2	3	4	5
7. The instructor was knowledgeable on the topic	1	2	3	4	5
8. I would be interested in attending a follow-up, more advanced FDP on this same subject	1	2	3	4	5

9. Given the topic, was this FDP: ☐ a. Too short ☑ b. Right length ☐ c. Too long

10. In your opinion, was this FDP: ☐ a. Introductory ☐ b. Intermediate ☑ c. Advanced

11. What did you most appreciate/enjoy/think was best about the course? Any suggestions for improvement?

Well planned & executed
Thank you

Thank you!

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned with CamScanner

Faculty Development Program Evaluation Form

We would appreciate if you could take a few minutes to share your opinions with us so we can serve you better.

FDP title: **Digital Communication System Design**

Date: **17th and 18th February 2017**

Resource persons: **Dr. Hiren P & Mr. Dileep M D**

	Strongly agree				Strongly disagree
1. The content was as described in publicity materials	(1)	2	3	4	5
2. The FDP was applicable to my research	1	(2)	3	4	5
3. I will recommend this FDP to other conservators	(1)	2	3	4	5
4. The program was well paced within the allotted time	(1)	2	3	4	5
5. The resource persons are good communicators	(1)	2	3	4	5
6. The material was presented in an organized manner	(1)	2	3	4	5
7. The instructor was knowledgeable on the topic	(1)	2	3	4	5
8. I would be interested in attending a follow-up, more advanced FDP on this same subject	(1)	2	3	4	5

9. Given the topic, was this FDP: ☐ a. Too short ☑ b. Right length ☐ c. Too long

10. In your opinion, was this FDP: ☐ a. Introductory ☐ b. Intermediate ☑ c. Advanced

11. What did you most appreciate/enjoy/think was best about the course? Any suggestions for improvement?

Well Planned & executed.
Thank you

Thank you!
Please return this form to the coordinator at the end of the workshop.

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

PRINCIPAL 13/2/2017
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
Raichur-584 103

Scanned with CamScanner

NETA

Navodaya Institute of Technology Raichur

Department of Electronics & Communication Engg.

Attendance slip

Date: 20/2/17

This is to certify that .....Dr. V. Suneetha.....of Sri...Krishnadevaraya.....College has participated in two days Faculty Development Program in "Digital communication system design" held on 17th & 18th Feb 2017 at NIT – Raichur.

HOD

NETA

Navodaya Institute of Technology Raichur

Department of Electronics & Communication Engg.

Attendance slip

Date: 20/2/17

This is to certify that .....Savita Naik.....of .....GPT.....College has participated in two days Faculty Development Program in "Digital communication system Design" held on 17th & 18th Feb 2017 at NIT – Raichur.

HOD

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

PRINCIPAL 13/2/2017
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

20/2/17
Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
Raichur-584 103

Scanned with CamScanner

NAVODAYA
Institute of Technology
RAICHUR

Registration form

Two day faculty development program on "**Digital Communication System Design**"

Sl. no	Name of the Participant	College and Department	e- Mail id	Mobile number	Amount paid	Signature
1.	Mukthambika. S. H.	Gavarnment Polytechnique College E&CE	mukthambika.sm@gmail.com	9482420449	300/-	
2.	Savita Naik	GPT E&CE	Savitanaik gpt@gmail.com	94815 47012	300/-	
3.	N. Anjulatha	Dept of Instrumentation SKU(Sri Krishnadevaraya University) Anantapur	dranjulatha@gmail.com	97055 38630	300/-	
4.	Kailas	Asst. Prof. VNEC, Sonpur Shorapur.			300/-	
5.	Vinod. B.	Asst. Prof (ECE) Mahaveer Institute of Science & Technology	vinodb129@gmail.com	9738486610	300/-	

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned with CamScanner

Scanned with CamScanner

NAVODAYA
Institute of Technology
RAICHUR

Registration form

Two day faculty development program on "**Digital Communication System Design**"

Sl. no	Name of the Participant	College and Department	e- Mail id	Mobile number	Amount paid	Signature
6.	Pujari Seidhar	Assoc. Prof(ECE) Mahaveer Institute of Science & Technology Vyasapuri Hyd.	seidharpujari1234@gmail.com	8143537284	300/-	
7.	Dr. V. Suneetha	Teaching Assistant (ECE) Sri. Krishnadeva Raya University Anantapur	Suneekuppala@gmail.com	9966114966	300/-	
8.	Prashil H. Lodape	Asst. Prof (ECE) Govt. Cdg of Engg. Karad	Prashilgadape@gmail.com	9637706707	300/-	
9.	Dr. K. Palani Swamy	HOD Dept. of ECE, NIT, Raichur	kadeeswit@gmail.com	7411804401		
10.	CH. Madhuri Devi	NIT, ECE Dept.				

Head of Department
Electronics & Communication Engineering
RAICHUR-584 103

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned with CamScanner

Scanned with CamScanner

NAVODAYA
Institute of Technology
RAICHUR

Registration form

Two day faculty development program on "**Digital Communication System Design**"

Sl. no	Name of the Participant	College and Department	e- Mail id	Mobile number	Amount paid	Signature
11.	S. Rammurthy	NIT, ECE Dept				
12.	E. Channaveeramma	NIT, ECE Dept.	Channaveeramma.e@navodaya.edu.in	8884977757	300/-	
13.	N. Priyadarshini	NIT, ECE Dept				
14.	Sachin kumar	NIT, ECE. Department	Sachin.kumar@navodaya.edu.in	95385 13348	300/-	
15.	Laxmikanth Reddi	NIT, ECE Department				

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
Raichur-584 103

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned with CamScanner

Scanned with CamScanner

NAVODAYA
Institute of Technology
RAICHUR

Registration form

Two day faculty development program on "Digital Communication System Design"

Sl. no	Name of the Participant	College and Department	e- Mail id	Mobile number	Amount paid	Signature
16.	Md Shoaibuddin Madni	NIT-R & ECE Dept	mdshoaibuddin madni@ navodaya.edu.in	8772-3 71743	Rs 300	
17.	Rachana Akasi-kere.	NIT-R & ECE Dept				
18.	Archana Patil	NIT, ECE Dept				
19.	Gauri Shetty	NIT, ECE Dept.				
do.						

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

PRINCIPAL 13/6/2022
Navodaya Institute of Technology (NIT)
Raichur-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Scanned with CamScanner

Scanned with CamScanner

NAVODAYA Institute of Technology RAICHUR

Attendance statement:

Two day faculty development program on **"Digital Communication System Design"**

Sl.no	Name of the participant	14/11/17 Session-1	17/11/17 Session-2	18/11/17 Session-3	18/11/17 Session-4
1.	Laxmikant Reddi				
2.	Vinod.B				
3.	Pujari Shekar				
4	Harshita.P				
5	Devika				
6	Shaayda				
7	Nidhi K Patel	Nidhi			
8	Divyalaxmi	Div			
9	Pallavi				
10	Priyanka				
11	Shweta				
12	Chandrika				
13	Shaheen				
14	Meghapatil				
15	Anjum				
16	Ajmal				
17	Nishat				

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

PRINCIPAL 13/11/2017
Navodaya Institute of Technology (NIT)
RAICHUR-584 103.

Scanned with CamScanner

Scanned with CamScanner

NAVODAYA
Institute of Technology
RAICHUR

Attendance statement:

Two day faculty development program on **"Digital Communication System Design"**

Sl.no	Name of the participant	Session-1	Session-2	Session-3	Session-4
18	SAHANA VAIDYA	Sahana	Sahana	Sahana	
19	MEGHANA	Meghana	Meghana	Meghana	
20	chaitra.M	chaitra			
21	Bhavana.B	Bhavana			
22	Aishwarya	Aishwarya	Aishwarya	Aishwarya	Aishwarya
23	Sowmya	Sowmya			
24	ASMA Banu	Asma	Asma		
25	Priyanka.c	Priya	Priya	Priya	
26	Atiya Sultana	Atiya	Atiya	Atiya	
27.	ANJUM	Anjum			
28.	RAIS FATIMA SHAFAF				
29.	NIDA ZAREEN				
30	Gauravi				
31	Laxmikant Reddi				
32	Dr. PRAMOD KUMAR				
33	Mirza Jaffar Ali Baig			Baig	

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Scanned with CamScanner

Scanned with CamScanner

NAVODAYA
Institute of Technology
RAICHUR

Attendance statement:

Two day faculty development program on "Digital Communication System Design"

Sl.no	Name of the participant	Session-1	Session-2	Session-3	Session-4
34	Naveen Kumar				
35	Amit V.C				
~~36~~	~~Nishat Nazneen~~				
36	Sarita Naik	Sarita	Sarita	Sarita	Sarita
37	Mujtabaanbis				
38.	N. Priyadarshini				
39.	Madhuri devi.ch	(M)	(M)	(M)	(M)
40.	Archana patil.C				
41	G. Chandra Veerayya				

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned with CamScanner

Scanned with CamScanner

HANDS ON WORKSHOP ON

Communication System

using

MATLAB

वेद

Versatile Engineering Designs

Conducted By:

VEDLABS,

Contact:

#24, 2nd floor, CMH road,

Indiranagar II stage, Bangalore, 080-42040494

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Scanned with CamScanner

Days	Topics	Duration
1.	**Theory:** - Basics of Digital Communication. **Hands on:** - Simulating a Communications Link - 16-QAM with MATLAB functions - 16-QAM with simulink Blocks. - QPSK and OFDM with MATLAB system objects. - Accelerating BER simulation using the Parallel computing toolbox.	30 mins. 5 hours
2.	**Hands on:** - Example of Source Coding. - Example of Block Coding. - Example of Convolutional coding . - Galois Field Computations.	6 hours
3	**Hands on:** - Visualization and Measurements - Scatter plot and Eye Diagram with MATLAB functions. - EVM and MER Measurements with simulink - ACPR and CCDF measurements with MATLAB system Object	6 hours
4	**Hands on:** - Mini Project development. - GUI toolbox introduction. - Designing GUI layout and Formatting. - Writing code for each GUI events.	6 hours

INSTITUTE OF TECHNOLOGY RAICHUR

PRINCIPAL 13/12/2022
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
Raichur-584 103

Scanned with CamScanner

Hands On Two Days Faculty Development Programme
On Digital Communication System Design in association with VEDLABS, Bangalore. On February 17th & 18th 2017.

Dr. Hiren Parmar, Director Vedlabs –Bangalore,Education: MS (Information and communication Engineering), from University of Leicester (UK) has handled the workshop and gave live demonstration and Hands on Simulation on the following Topics on MATLAB programming, SIMULINK and GUI.

Finally.

The Best performers were

1)Prof channaveeramma/Asst professor/ECE dept/NIT

2)Prof vinod/ECE dept/Mahaveer institute of technology, Hyderabad

3)NidaZareen/IV Sem M.Tech student/NIT





NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned with CamScanner

NET's

Navodaya Institute of Technology

Certificate

This is to certify that

Mr /Ms /Dr Atiya Sultana

of NIT, Raichur has participated and successfully completed two days Faculty Development Programme on "Digital Communication System Design" in association with VEDLABS, Bangalore organized by the Department of Electronics & Communication Engineering from 17th Feb to18th Feb 2017.

Coordinator

H.O.D

PRINCIPAL 13/2/2017
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Principal

Scanned with CamScanner

NET's

Navodaya Institute of Technology

Certificate

This is to certify that

Mr /Ms /Dr Gavravi Shetty

of NIT, Raichur has participated and successfully completed two days Faculty Development Programme on "Digital Communication System Design" in association with VEDLABS, Bangalore organized by the Department of Electronics & Communication Engineering from 17th Feb to18th Feb 2017.

Coordinator

H.O.D 14/2/2017

Principal

RAICHUR

PRINCIPAL 13/2/2017
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned with CamScanner

NET's

Navodaya Institute of Technology

Certificate

This is to certify that

Mr /Ms /Dr E. Channaveeramma

of NIT, Raichur has participated and successfully completed two days Faculty Development Programme on "Digital Communication System Design" in association with VEDLABS, Bangalore organized by the Department of Electronics & Communication Engineering from 17th Feb to18th Feb 2017.

Coordinator

H.O.D 18/2/2017

Principal

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned with CamScanner

Scanned with CamScanner

Gmail

Course Contents and Bio Data of Speakers/speakers

Dear Sir
Please mail the course contents of the workshop along with the bio data for approval of management and brochure preparation
The workshop is held on Feb 7th and 13th, ie Friday and Saturday
Thank you

Hello Sirajjit

Please find attachment for workshop module. Following are my details

Education
1. MS(Information and communication Engineering) from University of Leicester
2. BE(Electronics) from North Maharashtra University

Work Experience
1. VESUBS, Bangalore, Director from August 2009
2. Rajesh Global Solutions,Bangalore, Software Engineer, Apr 2008 to July 2009
3. Rahul electronics, UK, Technician, Jan-2007 to Dec-2007

Area of Expertise
1. Image Processing
2. Data mining
3. Wireless Sensor Networks
4. Digital Signal Processing
5. Optic Communications

If you need any further information then please contact me

Regards,
Irfan Pathan

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned with CamScanner

Date: 3.08.2017

From

Dr.K.M.Palaniswamy,

Professor and Head,

Navodaya Institute of Technology,

Raichur-584103

To

The Principal,

Navodaya Institute of Technology,

Raichur-584103

Sub: Permission for conducting two Days FDP on 23.8.2017 and 24.8.2017-Reg

Sir,

From ECE dept., We are going to conduct FDP on Internet Of Things(IoT) in association with Orange Research Labs, Hyderabad.

The FDP is to be carried out for two days 23.8.2017(Wednesday) and 24.8.2017(Thursday). So please provide permission to conduct the above FDP.The proposed expense is attached in next page. The Expected expense is Rs30,000/- whose details are provided in next page.

Thanking you

The faculty Coordinators are

1.Prof. N.Priyadarshini for ABKp

2.Prof.Amarappa Pagi ABKp

3.Prof. Archana Patil.G

Forwarded for kind approval to C.D.O & N.D

31/8/17

Yours Faithfully

3/8/2017

Department of
Electronics & Communication Engineering
Navodaya Institute of Technology
RAICHUR-584 103.

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

PRINCIPAL 13/8/2017
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned by CamScanner

Scanned with CamScanner

Proposed Budget for work shop

S.No	Matter of event	Requirement	Approximate cost
1	Travelling Charges for Two Trainees	Rs3000	Rs3000
2	Brochure Preparation	For 100	Rs1000
3	Sending Hardcopy Brochures	Rs 500 for 100 Brochures	Rs500
4	Preparing Banners for Seminar hall	Rs 500 for 2 banners	Rs500
5	Certificate preparation	Rs1000	Rs1000
6	Remuneration for Resource persons	Two persons	Rs25,000
7.	Other Miscellaneous		Rs2000/-
		Total	Rs33,000/-
		Expected income	Rs3,000/-
		Net Expense	Rs30,000/-

In this Regard, We need Rs30, 000/- for conducting of FDP effectively in advance. The Accommodation facility for Resource persons and External Participants and Food expenditures can be considered from Management Side.

3/5/2017

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

PRINCIPAL 13/5/17
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned by CamScanner

Scanned with CamScanner

NET GROUP OF EDUCATIONAL INSTITUTIONS: RAICHUR
NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

GUEST LECTURE/SEMINAR/ADD-ON COURSE/WORKSHOP /CONFERENCE/SHORT TERM TRAINING PROGRAMME/Internship Training/FDP

Dt:3.08.2017

1	ACADEMIC YEAR	2017-18(ODD SEM)
2	TYPE OF THE PROGRAM PROPOSED	FDP
3	TITLE OF THE PROGRAM:	FDP on Internet of Things(IoT)
4	BROAD OBJECTIVE OF THE PROGRAM	FDP on IOT makes the Faculty/students to create their own projects and to design a specific automated application for Institution /Home Applications and also helpful for Research and development.
5	NAME OF THE INSTITUTION	Navodaya Institute of Technology, Raichur
6	NAME OF THE DEPARTMENT(S)	Electronics and communication Engineering
7	TARGETED GROUP OF STUDENTS	Program : **Research Scholars** **M.TECH**:DCN,(I SEM and III SEM) Students **B.E**: VII SEM students, R
8	DATE/DURATION OF SUCH PROGRAMMES CONDUCTED IF ANY IN THIS ACADEMIC YEAR	Digital Communication System Design by VEDLabs, Banglore on 17.2.2017 and 18.2.2017
9	ELIGIBILITY CRITERIA SET:	Faculties of Engineering colleges,Polytechnic, Research Scholars M.Tech Students, B.E Students
10	NUMBER OF DAYS REQUIRED TO ORGANIZE THE PROGRAM	2 Days
11	PROPOSED DURATION OF THE PROGRAM	23.8.2017 and 24.8.2017
12	TOTAL NO.OF PERIODS REQUIRED FOR THE PROGRAMME	14 Hours
13	NO.OF PERIODS REQUIRED PER DAY	7 Hours

NAVODAYA INSTITUTE OF TECHNOLOGY * RAICHUR

PRINCIPAL 13/8/2017
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned by CamScanner

Scanned with CamScanner

	LOCATION DETAILS:	Navodaya Institute of Technology, Raichur
15	MINIMUM NO OF PARTICIPANTS:	20 faculty+60 Students
16	DETAILS OF SPONSORERS:	NIT Management
17	DETAILS OF RESOURCE PERSONS:	1.Mr. Madhu Parvathaneni, Founder& CEO, ORL Makers Garage 2.Mr. Surya Kumar
18	REGISTRATION FEE WITH CERTIFICATION:	Rs 400per External participant(Free accommodation for Outside participants and Lunch will be provided free of Cost for Local participants)
19	REGISTRATION FEE WITHOUT CERTIFICATION	Nil
20	EVENT ORGANISERS(STUDENTS)	1. Shana Vaidya (VII ECE) 2. Nikitha (VII ECE) 3.Amit V (V ECE)
21	FACULTY COORDINATOR	1. Miss Priyadarshini,Asst prof/ECE 2.Mr.Amarappa pagi, Asst prof/ECE 3.Mrs.Archana patil, Asst prof/ECE
22	PROGRAMME PROPOSED BY:	Miss Priyadarshini,Asst prof/ECE
23	ANY OTHER RELEVANT MATTER:	Nil
24	ESTIMATED BUDGET IN Rs:	Rs30,000

This is to the Honorable CEO of NIT for kind approval:

PRINCIPAL 3/8/17 VICE PRINCIPAL 3/8/17 HOD 3/8/17 EVENT COORDINATOR 3/8/17

Coordinators (Amarappa Pagi) (Archana)

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology
RAICHUR-584 103.

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR ASSOC

APPROVED/NOT APPROVED

C.E.O OF NIT

PRINCIPAL 13/8/2017
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned by CamScanner

Scanned with CamScanner

NAVODAYA
Institute of Technology
RAICHUR

Navodaya Nagar, Bijnigera Road
RAICHUR - 584 103, Karnataka, INDIA
Ph: +91 8532-240034
Fax: 240034 / 240061
E-mail: principal.nit@navodaya.edu.in
Web: www.navodayaengg.in

Date:11-08-2017.

Forwarded to the Honorable Chairman of NET through Respected CEO of NIT, Raichur for kind approval:

Requisition No.:	NET/NIT/NGT/1641/16thAug
Academic year:	2017-18
Date of Request:	11-08-2017
Subject:	2-day Workshop on IoT
Amount:	Rs.30,000/-
Priority:	Urgent / Ordinary
Contact Person:	HOD-ECE- NIT
Domain:	Academics – Co-Curricular Activities
If any other please specify:	

Remarks of the Principal:

Experts from ORL Industries pvt. Ltd. Referred by Priyadarshi faculty of ECE also she acted as coordinator also. Hence it is recommended. Please do needful from your end.

Signature of the Principal

Remarks of the C.E.O

Signature of the CEO

Remarks of the Chairman:

Signature of the Chairman

INSTITUTE OF TECHNOLOGY RAICHUR

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

PRINCIPAL 13/8/17
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned by CamScanner

Scanned with CamScanner

NET's
NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

Tel : +91 8532-200115, 240334, Fax : +91-8532-240054
E-Mail :hodece.nit@navodaya.edu.in

CIRCULAR

Cir.No:2017-18/FDP/TF/01

8.08.2017

Chief Patron
Shri. S. R. Reddy, Hon'ble Chairman,
Navodaya Education Trust (NET®),
Raichur.

Patron
Shri. N. Bharath Reddy., Hon'ble Trustee.
Navodaya Education Trust (NET®),
Raichur.

Chief Convener
Dr. Mallikarjun Sharma,
Principal and Dean, NIT, Raichur

Convener
Dr. K M Palaniswamy,
HOD/ ECE, NIT. Raichur

Coordinator
Prof. D.Rammurthy
Assistant Professor, Dept of ECE, NITR.

Co-Coordinator
1. Prof. N.Priyadarshini
Assistant Professor, Dept of ECE, NITR.
Email:priyadarshini.n@navodaya.edu.in
Contact no. : +91- 9986239962

2. Prof. Archana Patil G
Assistant Professor Dept of ECE, NIT
Email: archana.patil@navodaya.edu.in
Contact no. : +91- 9148945705

3. Prof. Amarappa pagi
Assistant Professor Dept of ECE, NIT
Email: amarappa.pagi@gmail.com
Contact no. : +91-9663338639

Organizing Committee
1. Prof. Channaveeramma.E
2. Prof. Madhuri devi Ch
3. Prof. Sachin kumar
4. Prof. LaxmikanthReddi
5. Prof. Gauravi Shetty

Respected Sir / Madam,
Greetings from Navodaya Institute of Technology, Raichur...

The department of Electronics and Communication Engineering is organizing a Two day FDP on **"Internet of Things (IoT)"** The attached brochure may kindly be circulated among the students and faculty members / colleagues.

The Internet of things is a system of interrelated computing devices, mechanical and digital machines provided with unique identifiers and ability to transfer data over a network without requiring human-to-human or human-to-computer interactions. This FDP aims to give hands on training in Internet of Things and programming Raspberry Pi using Python.

We are pleased to inform that the department of Electronics and Communication Engineering, Navodaya Institute of Technology is organizing a Faculty Development Programme during August 23rd & 24th 2017.

In this context, we look forward your support and request you to encourage participation of professionals by circulating the attached brochure.

Thanking you,
Yours sincerely,

Dr. K M Palaniswamy,
Convener

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY • RAICHUR

PRINCIPAL 13/8/2017
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned with CamScanner

Principal

ECE

From: bharath reddy [bharath.reddy@navodaya.edu.in]
Sent: Tuesday, August 15, 2017 4:40 AM
To: Dr.M.V.Mallikarjuna
Subject: Re: ECE Dept FDP Scanned Documents

Dr. Mallikarjuna,

Please consider this approved.

Regards,
Bharath

Sent from my iPhone

On 14-Aug-2017, at 10:06 AM, Dr.M.V.Mallikarjuna <principal.nit@navodaya.edu.in> wrote:

- Dear sir
  Ece proposed to organize FDP.

---------- Forwarded message ----------
From: Human Resource-Dept <hr.nit@navodaya.edu.in>
Date: Aug 12, 2017 11:09 AM
Subject: ECE Dept FDP Scanned Documents
To: 'Bharath Reddy N' <bharath.reddy@navodaya.edu.in>
Cc: principal.nit@navodaya.edu.in

> Dear Sir,
>
>
>
> ECE department planned for 2 day FDP on 23rd and 24th august.
>
- > Budget: Amount required – Rs. 30,000/-
>
> Expected Revenue to be generated is Rs. 3000/-
>
> For your kind approval.
>
>
>
>
>
>
>
>
>
> Regards

Padmini M

may be approved
31/8/17

1

31/8/17

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

ECT/HOD/NIT/2013-14/0287
16/8/17

ECT/HOD/NIT/2013-14/0313
31/8/17

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned by CamScanner

Scanned with CamScanner

NET's
NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Ph. No. 08532-220330, Extn-703,702,713, 07353295425
e-mail.hodece.nit@navodaya.edu.in

Chief Patron
Shri. S. R. Reddy, Hon'ble Chairman,
Navodaya Education Trust (NET®),
Raichur.

Patron
Shri. N. Bharath Reddy., Hon'ble Trustee.
Navodaya Education Trust (NET®), Raichur.

Chief Convener
Dr. Mallikarjun Sharma,
Principal and Dean, NIT, Raichur

Convener
Dr. K M Palaniswamy,
HOD/ ECE, NIT, Raichur

Coordinator
Prof. D.Rammurthy
Assistant Professor, Dept of ECE, NITR.

Co-Coordinator
1. Prof. N.Priyadarshini
Assistant Professor, Dept of ECE, NITR.
Email:priyadarshini.n@navodaya.edu.in
2. Prof. Archana Patil G
Assistant Professor Dept of ECE, NIT
Email: archana.patil@navodaya.edu.in
3. Prof. Amarappa pagi
Assistant Professor Dept of ECE, NIT
Email: amarappa.pagi@gmail.com

Organizing Committee
1. Prof. Channaveeramma.E
2. Prof. Madhuri devi Ch
3. Prof. Sachin kumar
4. Prof. LaxmikanthReddi
5. Prof. Gauravi Shetty

14.08.2017

Faculty Development Programme on
"Internet of Things (IoT)"

To

The Principal,

Warm Greeting!!!

Kind attention: Head of the Department / ECE,

Dear Sir/Madam,

Sub: Invitation for attending faculty development programme on "Internet of Things (IoT)" reg.

We are pleased to inform that the Department of Electronics & Communication Engineering, Navodaya Institute of Technology is organizing a Faculty Development Programme on "Internet of Things (IoT)" during August 23rd & 24th 2017, (2 days) at our college premises. This programme will equip the faculty to teach the subject in better way and also enable them to offer final year project in this specialization. We invite your faculty members to participate in this FDP from your esteemed institution.

Please see the brochure enclosed with this for your perusal and circulation among the faculty members of ECE/CSE department.

For further details please feel free to contact.

Thanks & Regards

Dr. K M Palaniswamy
Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY • RAICHUR

PRINCIPAL 16/8/2017
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned with CamScanner

Registration Form

(photocopy may be used if required)

Name (in BLOCK letters): _____

_____

Academic Qualification : _____

Experience: _____

Designation & Department: _____

College Address: _____

_____

_____

Name of Affiliated University: _____

E mail : _____

Contact Number: _____

DD Number/ Date _____

Declaration:

The information furnished above is true to the best of my knowledge.

Date:

Place: Signature of the Applicant

Mr./Ms./Mrs. _____

__ is an employee (faculty)/PG student of our institute. He / She will be permitted to attend the programme if selected.

Date:

Place:

Signature & Seal
HOD/Head of the Institution
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Chief Patron
Sri. S. R. Reddy, Hon'ble Chairman
Navodaya Education Trust (NET®), Raichur.

Patron
Sri. N. Bharath Reddy, Trustee
Navodaya Education Trust (NET®), Raichur.

Chief Convener
D.r Mallikarjun Sharma., Dean/Principal, NIT, Raichur.

Convener
Dr. K M Palaniswamy, HOD,ECE, NITR

Event Coordinator
Prof. D.Rammurthy
Assistant Professor, Dept of ECE, NITR.

Coordinator
Prof. N.Priyadarshini
Assistant Professor, Dept of ECE, NITR.
Email:priyadarshini.n@navodaya.edu.in
nspriya07@gmail.com
Contact no. : +91- 9986239962

Co-Coordinators
Prof. Archana Patil G
Assistant Professor Dept of ECE, NIT
Email: archana.patil@navodaya.edu.in
archanapatil.engg@gmail.com
Contact no. : +91- 9148945705

Prof. Amarappa pagi
Assistant Professor Dept of ECE, NIT
Email: amarappa.pagi@gmail.com
Contact no. : +91-9663338639

Organizing Committee

1. Prof. Channaveeramma.E
2. Prof. Madhuri devi Ch
3. Prof. Sachin kumar
4. Prof. LaxmikanthReddi
5. Prof. Gauravi Shetty

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103



Hands On Two Days Faculty Development Programme on Internet of Things(IoT) in association with Orange Research Labs, Hyderabad.

ORL Makers Garage

August 23rd & 24th 2017

ORGANIZED BY
Department of Electronics & Communication Engineering.

Navodaya Education Trust®
NAVODAYA INSTITUTE OF TECHNOLOGY
Navodaya Nagar, Bijanagera Road, Raichur-584103, Karnataka, India.

Scanned with CamScanner

About the Institute:

Navodaya Institute of Technology(NIT), Raichur, established in 2008, is affiliated to Visvesvaraya Technological University (Belagavi). The institute is approved by All India Council for Technical Education (AICTE), New Delhi.

The guiding spirit and the driving force for all the progress and achievements of the Institution is our Honourable chairman of Navodaya Education Trust (NET®), Sri S. R. Reddy. NIT focuses on promoting quality technical education in north Karnataka region and Hyderabad-Karnataka region in particular.

The education is of paramount importance for socio-economic development. The institute focuses on integrated scientific training with value based education, to enable overall development of the aspirants.

NIT is all-inclusive campus, with Administrative block, Library block, independent Academic blocks for each department, Separate Hostels for Boys and Girls, Canteen, Bank(SBI), in-campus ATM , Temple, Gym and Playground, all housed in a vast campus of more than 30 acres land surrounded by hillocks and lake.

About the Department:

The Electronics and Communication Engineering department aims in delivering excellence in theoretical and experimental value based education to students. The Department was started in the year 2008 with an intake of 60 students with the vision to provide the quality technical education. The department has good infrastructure with well-equipped laboratories and necessary softwares.

The Department has bagged many ranks in GATE. Many students were placed in various prestigious MNC's and pursuing their higher studies in NIT's and IIT's.

The Department also offers Postgradusate programme in Digital Communication and Networking (DCN), was started in the academic year 2014-15.The department has highly qualified, experienced and dedicated faculties with PhD and Post graduation (M.E. / M.Tech.) degrees. The workshops and Training programmes are planned regularly for the benefit of students and faculty members.

Vision: To excel in education and research in Electronics & Communication Engineering, thereby providing service to the society and human kind.

Mission: To create competent professionals by training the students in the design & implementation of Electronics and Communication Engineering products helping there by students to develop the skills to solve technological problems of modern society.

RESOURCE PERSONS:

1. Mr. Madhu P, Founder & CEO,ORL Makers Garage, Hyderabad

2. Mr Surya Kumar, Researcher, ORL makers garage, Hyderabad

About the workshop:

This FDP aims to give hands on training in Internet of Things and programming Raspberry Pi using Python.

Major Course Contents:

- Introduction to Internet of Things *(IoT)*
- Scope of IoT in different domains
- Practical Case Studies
- Introduction to Raspberry Pi
- Architecture and Pin Layout of Raspberry Pi
- Python Basic Syntaxes
- Interfacing BUTTON with Raspberry Pi
- Interfacing Air Quality Sensor with Raspberry Pi
- Interfacing Digital Temperature and Humidity sensor with Raspberry Pi
- Sending security alert through E-mail
- Android App controlled Home Automation
- Uploading Sensors Data with Raspberry Pi
- Data Analytics on Uploaded Data
- Reacts on Data Analytics

Important dates

FDP date : August 23rd & 24th 2017

Last date for registration : 20th Aug 2017.

Registration fees : Rs. 400 /-

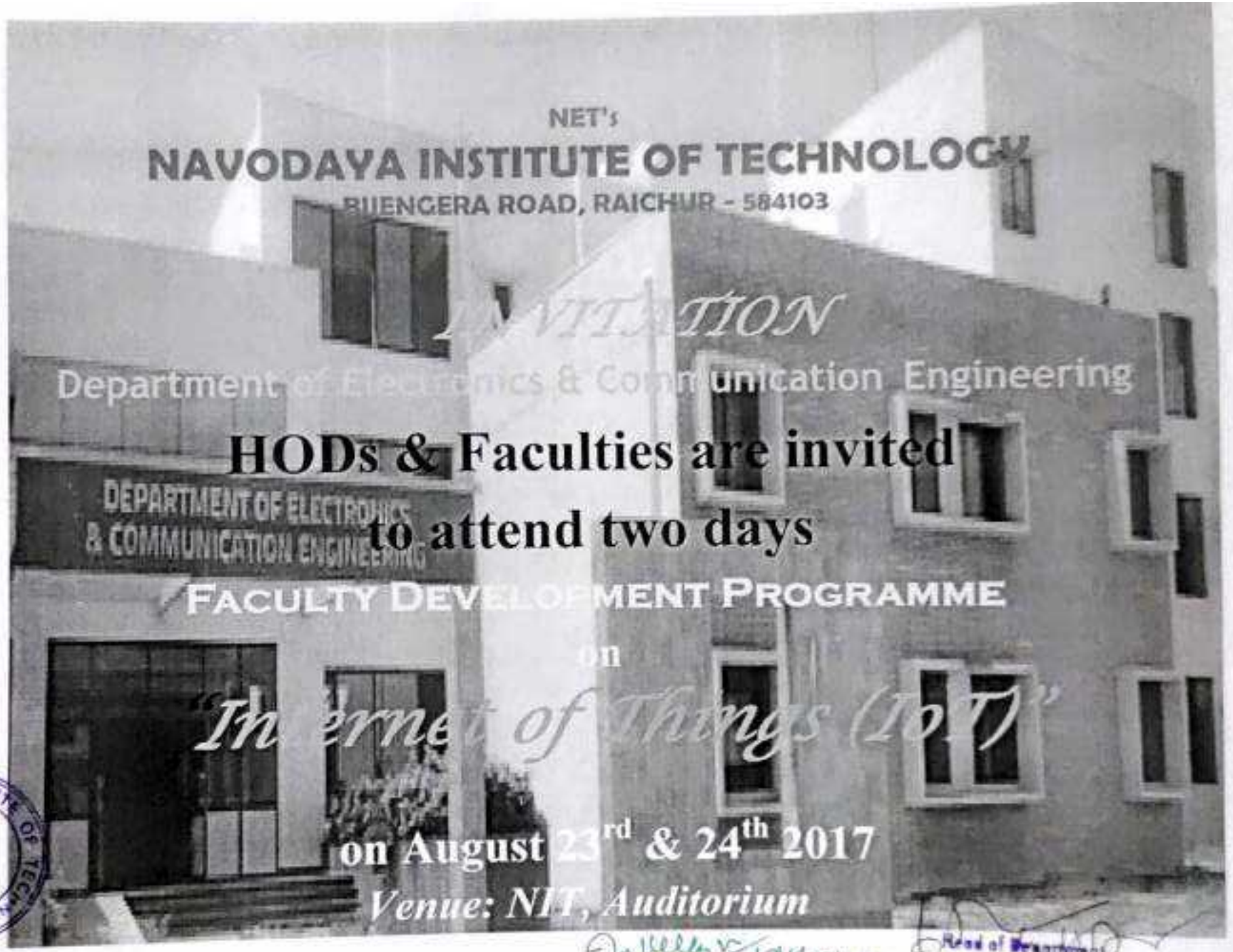
Registration fee may be paid in the form of DD in favour of **Principal, Navodaya Institute of Technology**, payable at **SBI, NET campus, Raichur.**

VENUE : NIT Auditorium, NIT, Raichur

Note:

1. Accommodation will be provided on prior request.
1. Lunch, Tea will be provided during the training in the campus.

Scanned with CamScanner



PRINCIPAL
Navodaya Institute of Technology
RAICHUR-584 103

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned with CamScanner

Schedule for Faculty Development Program (FDP) on **"Internet of Things (IoT)"**

Organised By

Navodaya Institute of Technology: Raichur (August 23rd & 24th 2017)

Timing of Each Day Session

Date/Day	Session I 9:30AM-11:00AM	TEA BREAK	Session II 11:30AM-1:00PM	LUNCH BREAK	Session III 2:00PM-5:00PM
23.08.2017 Wednesday	• Introduction to Internet of Things (IoT) • Scope of IoT in different domains • Practical Case Studies		• Introduction to Raspberry Pi • Architecture and Pin Layout of Raspberry Pi • Python Basic Syntaxes		• Interfacing BUTTON with Raspberry Pi • Interfacing Air Quality Sensor with Raspberry Pi
24.08.2017 Thursday	• Interfacing Digital Temperature and Humidity sensor with Raspberry Pi • Sending security alert through E-mail		• Android App controlled Home Automation • Uploading Sensors Data with Raspberry Pi		• Data Analytics on Uploaded Data • Reacts on Data Analytics

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY • RAICHUR

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned with CamScanner

ORL Makers Garage
IoT Workshop Proposal

Brief Introduction to Internet of Things:

The **Internet of Things** (IoT) is a system of interrelated computing devices, mechanical and digital machines, objects, animals or people that are provided with unique identifiers and the ability to transfer data over a network without requiring human-to-human or human-to-computer interaction.

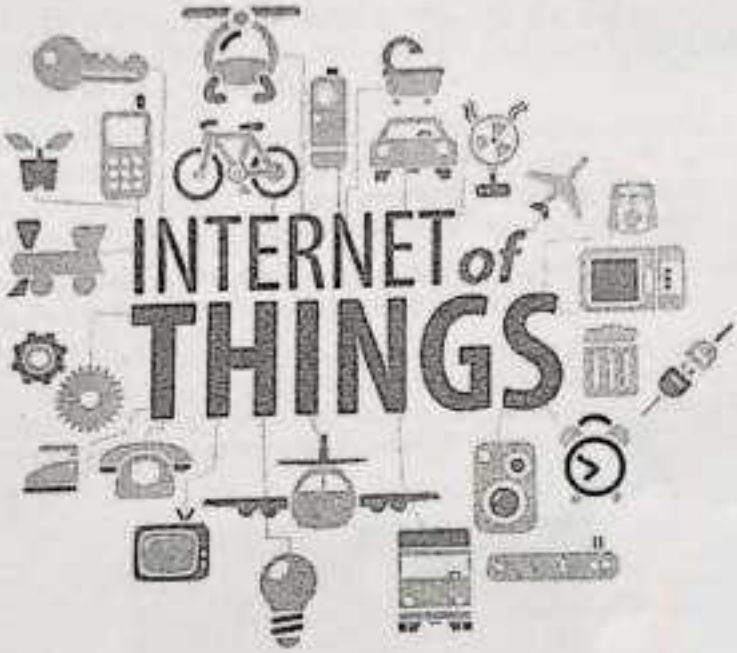


Figure – 1: Overview of IoT

The "Internet of things" (IoT) is becoming an increasingly growing topic of conversation both in the workplace and outside of it. It's a concept that not only has the potential to impact how we live but also how we work. But what exactly is the "Internet of things" and what impact is it going to have on you, if any? There are a lot of complexities around the "Internet of things" but we are able to discuss all these things in the session of Internet of Things. Get Ready.

NAVODAYA INSTITUTE OF TECHNOLOGY • RAICHUR

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned by CamScanner

Scanned with CamScanner

ORL Makers Garage
IoT Workshop Proposal

Brief Introduction to Raspberry Pi:

Did you really think the Raspberry Pi would stop getting better? At this point, we sound like a broken record, extolling on the new Pi's myriad improvements like we're surprised that the folks at the Raspberry Pi foundation are continuously making their flagship board better. Still, we'll say it again - the Raspberry Pi team has outdone themselves. The Raspberry Pi 3 Model B is out and it is AMAZING.

There are two giant upgrades in the Pi 3. The first is a next generation Quad Core Broadcom BCM2837 64-bit ARMv8 processor, making the processor speed increase from 900 MHz on the Pi 2 to up to 1.2GHz on the Pi 3.

The second giant upgrade (and this is the one we're personally most excited about) is the addition of a BCM43143 Wi-Fi chip BUILT-IN to your Raspberry Pi. No more pesky Wi-Fi adapters - this Pi is Wi-Fi ready. There's also Bluetooth Low Energy (BLE) on board making the Pi an excellent IoT solution (BLE support is still in the works, software-wise)

Lastly, there's an upgraded switched power source that goes up to 2.5 Amps instead of just 2 Amps - allowing your Pi to power even more powerful devices over USB ports.

The best part about all this is that the Pi 3 keeps the same shape, connectors, and mounting holes as the Pi 2. The only difference is a slight change in where the LEDs are. 99% of cases and accessories will still be fully compatible with the Pi 3, Pi 2, and Pi B + - though if you have a case with a metal top there might be some Wi-Fi chip difficulties.

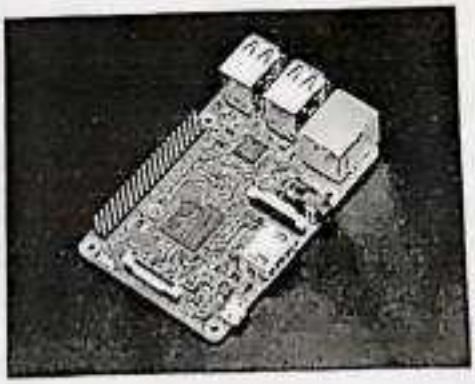


Figure – 2: Raspberry Pi 3

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

PRINCIPAL 13/8/2017
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned by CamScanner

Scanned with CamScanner

ORL Makers Garage
IoT Workshop Proposal

Day – 1:

Session – 1: [Duration: 1 Hour] – IoT

- Introduction to Internet of Things (IoT)
- Scope of IoT in different domains
- Practical Case Studies

Session – 2: [Duration: 2 Hours] – IoT Platform

- Introduction to Raspberry Pi
- Architecture and Pin Layout of Raspberry Pi
- Python Basic Syntaxes

Session – 3: [Duration: 5 Hours] – IoT Hardware

- Getting Started with Raspberry Pi
- Interfacing LED with Raspberry Pi
- Interfacing BUTTON with Raspberry Pi
- Interfacing Air Quality Sensor with Raspberry Pi
- Interfacing Light Intensity Sensor with Raspberry Pi

Day – 2:

Session – 4: [Duration: 3 Hours] – IoT Cloud

- Uploading Sensors Data with Raspberry Pi
- Data Analytics on Uploaded Data
- Reacts on Data Analytics

Session – 5: [Duration: 4 Hours] – IoT Gateway and Protocols

- Building local server on Raspberry Pi
- MQTT Protocol
- IBM Node Red Package
- Practical Case Studies
- Notifications on Twitter, and Facebook
- Socket Communication using TCP and UDP Protocols

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned by CamScanner

Scanned with CamScanner

The activities of the FDP are as follows.

Day – 1:

Session – 1: [Duration: 1 Hour]

Introduction to Internet of Things (IoT)

Scope of IoT in different domains

Practical Case Studies

Session – 2: [Duration: 2 Hours]

Introduction to Raspberry Pi

Architecture and Pin Layout of Raspberry Pi

Python Basic Syntaxes

Session – 3: [Duration: 3 Hours]

Booting Linux OS into the Raspberry Pi

Updating and Upgrading OS

Configuring Keyboard and Wi-Fi/LAN

Getting Started with Raspberry Pi

Interfacing LED with Raspberry Pi

Interfacing BUTTON with Raspberry Pi

Interfacing Air Quality Sensor with Raspberry Pi

Day – 2 :

Session – 4: [Duration: 3 Hours]

Interfacing Digital Temperature and Humidity Sensor with Raspberry Pi

Sending Security Alert through E-Mail

Android APP Controlled Home Automation

Session – 5: [Duration: 4 Hours]

Uploading Sensors Data with Raspberry Pi

Data Analytics on Uploaded Data

In this Regard, it is feasible to fix Rs400/-Per head for participants.

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned by CamScanner

Scanned with CamScanner

INDUSTRIES

Technology | Incubation Business

MENTORS VISITING YOUR COLLEGE:

1. Mr. Madhu Parvathaneni, M Tech - Embedded Systems
2. Mr. Hari Babu Vanga, BTech (ECE)
3. Mr. Harish M (B.Tech (ECE)
4. ~~Mr. Anil Kumar~~

ABOUT Mr. Madhu Parvathaneni:

Madhu Parvathaneni,
Founder & CSO,
Orange Research Labs and ORL Industries Pvt. Ltd,
madhu@orangeresearchlabs.com
0-7893015625

Mr. Madhu has worked as Industrial Mentor for the event Excite 2016, an initiative of Telangana Academy for Skill and Knowledge (TASK) and Hyderabad Software Entrepreneurs Association (HYSEA) on Internet of Things and plays a vital role in transforming 90 ideas into 27 start-ups.

Mr. Madhu has been certified by Robotics Certifications Standard Alliance, and International Robotics Community, Canada in the year 2012.

Mr. Madhu has started Orange Research Labs in the year 2010 and has started ORL Industries Pvt. Ltd in the year 2015.

Mr. Madhu has 6 + years of Industrial Exposure in Embedded Systems and Robotics.
Mr. Madhu has 2+ years of Industrial Exposure in Internet of Things.
Mr. Madhu has trained 22000+ students on IoT, Embedded Systems and Robotics.
Mr. Madhu has organized 150+ workshops on IoT, Embedded Systems and Robotics.
Mr. Madhu has mentored 6000+ ideas at undergraduate and postgraduate level.
Mr. Madhu has worked with 50+ engineering colleges in India.
Mr. Madhu has participated in 10+ Hackathons in India.

Mr. Madhu expertise is on Internet of Things, Embedded Systems, and Robotics.

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

PRINCIPAL 13/8/17
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned by CamScanner

Scanned with CamScanner

ORL
INDUSTRIES

Technology | Incubation Business

ABOUT Mr. Hari Babu Vanga:

Hari Babu Vanga,
Sr. IoT Engineer,
ORL Industries Pvt. Ltd,
hari@orangeresearchlabs.com

Mr. Hari has worked as Industrial Mentor for the event Excite 2016, an initiative of Telangana Academy for Skill and Knowledge (TASK) and Hyderabad Software Entrepreneurs Association (HYSEA) on Internet of Things and plays a vital role in transforming 90 ideas into 27 start-ups.

Mr. Hari has 2+ years of experience in Embedded Systems.
Mr. Hari has 1+ years of experience in Internet of Things.
Mr. Hari has trained 1500+ students on Embedded Systems and Internet of Things.
Mr. Hari has mentored 500+ ideas at undergraduate and postgraduate level.
Mr. Hari has worked on Raspberry Pi, Arduino, ESP, Node MCU and the other IoT Boards.
Mr. Hari has worked on 3 last products of ORL.
Mr. Hari is now working on ORL Arambha.

ABOUT Mr. Harish M:

Harish M,
Application Engineer,
ORL Industries Pvt. Ltd,
hareeshm@orangeresearchlabs.com

Mr. Harish has 2+ years of experience in Embedded Systems.
Mr. Harish has 1+ years of experience in Internet of Things.
Mr. Harish has mentored 1500+ ideas at undergraduate and postgraduate level.
Mr. Harish has worked on Raspberry Pi, Arduino, ESP, Node MCU and the other IoT Boards.
Mr. Harish has worked on R & D for the last 3 products of ORL.

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

PRINCIPAL 13/1/2020
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned by CamScanner

Scanned with CamScanner

ORL
INDUSTRIES

(2)

Technology | Incubation Business

ABOUT Mr. Anil Kumar:

Anil Kumar,
IoT Engineer,
ORL Industries Pvt. Ltd.
anil@orangeresearchlabs.com

Mr. Anil has 1+ years of experience in Embedded Systems.
Mr. Anil has 1+ years of experience in Internet of Things.
Mr. Anil has worked on Raspberry Pi, and Arduino.
Mr. Anil is now working on ORL Arambha.

Sir,

From the above observation, it is found that ORange Research Labs & ORL Industries PVT LTD, Hyderabad are experts in Internet of Things (IoT). By this from the we can get exposure in IoT & also live demonstration about the Interface & Interfacing of things with Internet.

Mr Madhu Patwardhan is present talking has cheerful that he would be invited in Jan placements & students placement in future.

So This Workshop would help both faculty & students to gain knowledge in IoT. the said Industrial exposure would provide an enhanced knowledge to us.

9/9/2017

HoD/ECE

Department of
Electronics & Communication Engineering
Navodaya Institute of Technology
RAICHUR-584 103.

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR ADOPTION

PRINCIPAL 13/9/17
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned by CamScanner

Scanned with CamScanner

The activities of the FDP are as follows.

Day – 1:

Session – 1: [Duration: 1 Hour]

Introduction to Internet of Things (IoT)

Scope of IoT in different domains

Practical Case Studies

Session – 2: [Duration: 2 Hours]

Introduction to Raspberry Pi

Architecture and Pin Layout of Raspberry Pi

Python Basic Syntaxes

Session – 3: [Duration: 3 Hours]

Booting Linux OS into the Raspberry Pi

Updating and Upgrading OS

Configuring Keyboard and Wi-Fi/LAN

Getting Started with Raspberry Pi

Interfacing LED with Raspberry Pi

Interfacing BUTTON with Raspberry Pi

Interfacing Air Quality Sensor with Raspberry Pi

Day – 2 :

Session – 4: [Duration: 3 Hours]

Interfacing Digital Temperature and Humidity Sensor with Raspberry Pi

Sending Security Alert through E-Mail

Android APP Controlled Home Automation

Session – 5: [Duration: 4 Hours]

Uploading Sensors Data with Raspberry Pi

Data Analytics on Uploaded Data

In this Regard, it is feasible to fix Rs400/-Per head for participants.

PRINCIPAL 13/5/2022
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned by CamScanner

Scanned with CamScanner

Date: 16.8.2017

From,

Dr. K.M. Palaniswamy,
Professor and Head,
Navodaya Institute of Technology,
Raichur-584103

To,

The Principal,
Navodaya Institute of Technology,
Raichur-584103

Sir,

Subject: Seminar Hall for Workshop on Internet of Things (IoT) in association with Orange Research Labs, Hyderabad on August 23rd & 24th 2017.

We have proposed to conduct Workshop on ***"Internet of Things (IoT)"*** in association with Orange Research Labs, Hyderabad on August 23rd & 24th 2017. For this Purpose, We need Seminar Hall on August 23rd & 24th 2017, Wednesday and Thursday, Time-9.30am-5.30pm.

Thanking You,

Yours faithfully

16/8/17

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

1

PRINCIPAL 16/8/2017
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned with CamScanner

16.08.2017

From,

Dr. K M Palaniswamy,

HOD/ ECE,

NIT, Raichur

To,

The Dean cum Principal,

NIT, Raichur.

Sir,

Sub: Requisition for Accommodation and Food -Reg

I bring to your kind notice that we are conducting the Two days FDP on "***Internet of Things (IoT)***" our department on August 23rd & 24th 2017. For that we want to arrange the accommodation and food for the Resource Persons in the above days. So kindly do the needful.

Resource Person Details,

1. Mr. Madhu P,
   Founder & CEO,
   ORL Makers Garage, Hyderabad

2. Mr Surya Kumar,
   Researcher,
   ORL makers garage, Hyderabad

To Manager Mr Palani for n.a
16/8/17

Thanking you,

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

PRINCIPAL 13/8/2017
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

HOD/ECE
18/8/17
Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned with CamScanner

NAVODAYA Institute of Technology RAICHUR

Attendance Report

Two day faculty development program on "Internet of Things(IoT)"

Sl.No	Name of the participant	23/8/2017	24/8/2017
1.	Rammurthy. D.		
2.	Madhuri Devi Chodey		
3	Md Shoaibuddin		
4	Sachin Kumar		
5.	Channaveeramma. E		
6	Reshma Arasikeri		
7.	Priyadharshini. N		
8.	Preeti Sagar Pattankude		
9.	Veenashree		
10.	Amaragappa Pagi		
11.	Sana		
12.	Geetha. M.		
13.	Kiran.		

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

24/8/17

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned with CamScanner

NAVODAYA Institute of Technology RAICHUR

Attendance Report

Two day faculty development program on "Internet of Things(IoT)"

Sl.No	Name of the participant	23/8/2017	24/8/2017
14.	Daneli		
15.	Madhu Babu		
16.	Satish		
17.	Velu		
18.	Sudhakar		
90	Praveen.		
21	Kailash		
22	Manjula.		
23	Jyothi		
24	Gangadhar		
25	Mallikarjun. G.		
26	Shravanagouda. Patil		
27			

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR ADDITIONAL

24/8/17

Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned with CamScanner

Faculty Development Program Evaluation Form

We would appreciate if you could take a few minutes to share your opinion with us so we can serve you better

FDP title: **Internet of Things (IoT)**

Date: **August 23rd & 24th 2017**

Resource person: **Mr. Madhu P, Mr Surya Kumar.**

		Strongly agree	Agree	Neutral	Disagree	Strongly Disagree
1.	The content was as described in publicity materials	1	2	✓	4	5
2.	The FDP was applicable to my research	1	2	✓	4	5
3.	I will recommend this FDP to other conservators	1	2	3	✓	5
4.	The program was well placed within the allotted time	✓	2	3	4	5
5.	The resourse person is good communicator	1	✓	3	4	5
6.	The material was presented in an organized manner	1	✓	✓	4	5
7.	The instructor was knowledgeable on the topic	1	✓	3	4	5
8.	I would be interested in attending a follow-up, more advanced FDP on this same subject	✓	2	3	4	5

9. Given the topic, was this FDP ☐ a. Too short ☑ b. Right length ☐ c. Too long

10. In your opinion, was this FDP ☐ a. introductory ☑ b. Intermediate ☐ c. Advanced

11. What did you most appreciate/enjoy/think was best about the course? Any suggestions for improvement?

Event was well organized.

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Thank You!

Please return this form to the coordinator at the end of workshop.

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
Raichur-584 103

Scanned with CamScanner

Navodaya Educational Trust

NAVODAYA INSTITUTE OF TECHNOLOGY

Bijangera Road, RAICHUR.

organized by

Department of Electronics & Communication Engineering

ORL Makers Garage

Certificate

This is to certify that Mr/ Ms/ Dr. Sachin Kumar

has pratcipated in *2-Day Faculty Development Programme on " Internet of Things(IoT) "*

on 23rd & 24th, August 2017, in association with *Orange Research Labs, Hyderabad,* held

at NAVODAYA INSTITUTE OF TECHNOLOGY, Raichur.

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Co Ordinator

HOD

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Principal

Scanned with CamScanner

Navodaya Educational Trust

NAVODAYA INSTITUTE OF TECHNOLOGY

Bijangera Road, RAICHUR.

organized by

Department of Electronics & Communication Engineering

ORL Makers Garage

Certificate

This is to certify that Mr/ Ms/ Dr. Mrs. ChanaVeerampa E.

has pratcipated in *2-Day Faculty Development Programme on " Internet of Things(IoT) "*

on 23rd & 24th, August 2017, in association with *Orange Research Labs, Hyderabad*, held

at NAVODAYA INSTITUTE OF TECHNOLOGY, Raichur.

NAVODAYA INSTITUTE OF TECHNOLOGY • RAICHUR

Co Ordinator

HOD

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Principal

Scanned with CamScanner

Scanned with CamScanner

Navodaya Educational Trust

NAVODAYA INSTITUTE OF TECHNOLOGY

Bijangera Road, RAICHUR.

organized by

of Electronics & Communication Engineering

ORL Makers Garage

Certificate

This is to certify that Mr/ Ms/ Dr. Mrs. Gauravi Shetty

has pratcipated in *2-Day Faculty Development Programme on " Internet of Things(IoT) "*

on 23rd & 24th, August 2017, in association with *Orange Research Labs, Hyderabad*, held

at NAVODAYA INSTITUTE OF TECHNOLOGY, Raichur.

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Co Ordinator

HOD

PRINCIPAL

Principal

Navodaya Institute of Technology (NIT),
RAICHUR-584 103

Scanned with CamScanner

NET's

NAVODAYA INSTITUTE OF TECHNOLOGY, RAICHUR

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

SUMMARY OF FACULTY DEVELOPMENT PROGRAMME ON

"Internet of Things (IoT)"

Department of ECE organized faculty development programme on ***"Internet of Things (IoT)"*** was held at Navodaya Institute of Technology, seminar hall from August 23rd & 24th 2017. The session was inaugurated with prayer song followed by the welcome speech by the Head of the department, Dr. K M Palaniswamy.

Principal presided over the function. Mr. Madhu P, Founder & CEO, ORL Makers Garage, Hyderabad, **Mr Surya Kumar**, Researcher, ORL makers garage, Hyderabad were the resource persons.

First day **Mr. Madhu P** explained about the programming Raspberry Pi using Python and gave Introduction to Internet of Things (IoT), Scope of IoT in different domains ,Practical Case Studies, Introduction to Raspberry Pi, Architecture and Pin Layout of Raspberry Pi and Python Basic Syntaxes

Second day **Mr Surya Kumar** explained about Interfacing BUTTON with Raspberry Pi, Interfacing Air Quality Sensor with Raspberry Pi, Interfacing Digital Temperature and Humidity sensor with Raspberry Pi, Sending security alert through E-mail, Android App controlled Home Automation, Uploading Sensors Data with Raspberry Pi, Data Analytics on Uploaded Data, Reacts on Data Analytics. Totally twenty five faculty members were participated and benefited.



NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

PRINCIPAL 13/9/2017
Navodaya Institute of Technology (NIT),
RAICHUR-584 103

Scanned with CamScanner

Date: 02.01.2018

From,

Dr.K.M. Palaniswamy,
Professor and Head,
Navodaya Institute of Technology,
Raichur-584103

To,

The Principal,
Navodaya Institute of Technology,
Raichur-584103

Sir,

Subject: Permission for conducting one week FDP from 16th to 22nd January 2018

from ECE dept.,

We are going to conduct FDP on **"*Computation Techniques in Optics and Photonics*"**.

The FDP is to be carried out for seven days from 16th to 22nd January 2018. So please provide permission to conduct the above FDP. The proposed expense is attached in next page. The Expected expense is Rs 40, 000/- whose details are provided in next page.

Thanking you,

The faculty Coordinators are

1. Prof. Sachin Kumar N
2. Prof. Md. Shoaibuddin Madni

Approved
2/1/2018

Yours Faithfully

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

PRINCIPAL 13/8/2020
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned with CamScanner

NET's
NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

Faculty Development Programme - PROPOSAL

Date: 02.01.2018

1	ACADEMIC YEAR	2017-18
2	TYPE OF THE PROGRAMME PROPOSED	Faculty Development Programme
3	TITLE OF THE PROGRAMME:	*"Computation Techniques in Optics and Photonics"*
4	BROAD OBJECTIVE OF THE PROGRAMME:	This FDP aims to give hands-on lab sessions, tool demonstrations, and discussion/presentation sessions with few lectures. Major Course Contents: • Optical Communication Technology: Possibilities and challenges. • Slow Light in Photonic Crystal for Faster Photonic Devices. • Recent trends in Photonics. • Experimental study for Simulation of Solar cell. • Nitride devices their fabrication and applications. • Characterization (XPS, AFM, SEM etc) of MOCVD grown and fabricated III-Nitride devices. • Graphene/GaN Nanowire based Photo-detectors and Nano LEDs. • Modeling & Simulations of microwave components & devices, Optimization Techniques. • Si v/s III - IV group Integrated Photonics, MEMS in Photonics, Modeling and Simulations of Metamaterials.
5	NAME OF THE INSTITUTION	NETs NAVODAYA INSTITUTE OF TECHNOLOGY
6	NAME OF THE DEPARTMENT(S)	ELECTRONICS AND COMMINICATION ENGINEERING
7	BENEFICIARY	Faculty Members
8	DATE/DURATION OF SUCH PROGRAMMES CONDUCTED IF ANY IN THIS ACADEMIC YEAR:	16th January to 22nd January 2018
9	ELIGIBILITY CRITERIA SET:	Faculties from Electronics and Communication Engineering.

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

PRINCIPAL 13/01/2018
Navodaya Institute of Technology (R.I.T.)
RAICHUR-584 101.

Scanned with CamScanner

10	NUMBER OF DAYS REQUIRED TO ORGANISE THE PROGRAMME	One Week FDP
11	PROPOSED DURATION OF THE PROGRAMME	42 hours
12	TOTAL NO. OF PERIODS REQUIRED FOR THE PROGRAMME	42 Periods
13	NO. OF PERIODS REQUIRED PER DAY	06 Periods
14	LOCATION DETAILS:	NETs NIT Auditorium
15	MINIMUM NO OF PARTICIPANTS:	30
16	DETAILS OF SPONSORERS:	-
17	DETAILS OF RESOURCE PERSONS:	Prof. Venkatachalam K
18	REGISTRATION FEE WITH CERTIFICATION:	Rs 1500 per participant
19	REGISTRATION FEE WITHOUT CERTIFICATION:	Rs - per participant.
20	FACULTY COORDINATOR:	1. Prof. Sachin Kumar N 2. Prof. Md Shoaibuddin Madni
21	PROGRAMME PROPOSED BY:	Department of ECE
22	ANY OTHER RELEVANT MATTER:	Nil
23	ESTIMATED BUDGET IN Rs:	Rs.40,000

This is to the Honorable Chairman of NIT for kind approval:

PRINCIPAL

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

HOD
Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

EVENT COORDINATOR

APPROVED/NOTAPPROVED

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned with CamScanner

NET's
NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

Tel : +91 8532-200115, 240334, Fax : +91-8532-240054
E-Mail :hodece.nit@navodaya.edu.in

CIRCULAR

Cir.No:2017-18/FDP/TF/02

02.01.2018

Chief Patron
Shri. S. R. Reddy, Hon'ble Chairman,
Navodaya Education Trust (NET®),
Raichur.

Patron
Shri. N. Bharath Reddy., Hon'ble Trustee.
Navodaya Education Trust (NET®),
Raichur.

Chief Convener
Dr. Mallikarjun Sharma,
Principal and Dean, NIT, Raichur.

Convener
Dr. K M Palaniswamy,
HOD/ ECE, NIT. Raichur.

Coordinator
Prof. Sachin Kumar N
Assistant Professor, Dept of ECE, NITR.
Email: sachin.kumar@navodaya.edu.in
nsachin834@navodaya.edu.in
Contact no. : +91- 8618477445

Co-Coordinators
Prof. Md Shoaibuddin Madni
Assistant Professor, Dept of ECE, NIT
Email: shoaibuddin.madni@navodaya.edu.in
shoaib.madni7@gmail.com
Contact no. : +91- 8792374743

Organizing Committee

1. Prof. Madhuri Devi Ch
2. Prof. Channaveeramma. E
3. Prof. Ramurthy Dasari
4. Prof. Laxmikanth Reddy
5. Prof.. Amarappa Pagi
6. Prof. Priyadarshini N
7. Prof. Archana Patil
8. Prof. Reshma Arasikere
9. Prof. Gauravi Shetty

Respected Sir / Madam,

Greetings from Navodaya Institute of Technology, Raichur...

The department of Electronics and Communication Engineering is organizing one week FDP on ***"Computation Techniques in Optics and Photonics"*** The attached brochure may kindly be circulated among the students and faculty members / colleagues.

One-week training programme is being organized for faculty of engineering and technological institutions. It is also open to faculties, persons from industry and doctoral students of Indian organizations. The objective is to provide an exposure to the participants about various computational methods (like FEM, FDTD, TMM etc.) in the field of engineering. The technical program will include mostly hands-on lab sessions, tool demonstrations, and discussion/presentation sessions with few lectures.

We are pleased to inform that the department of Electronics and Communication Engineering, Navodaya Institute of Technology is organizing a Faculty Development Programme from 16th January to 22nd January 2018.

In this context, we look forward your support and request you to encourage participation of professionals by circulating the attached brochure.

Thanking you,

Yours sincerely,

Dr. K M Palaniswamy,
Convener

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned with CamScanner

NET's
NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Ph. No. 08532-220330, Extn-703,702,713, 07353295425
e-mail hodece.nit@navodaya.edu.in

Chief Patron
Shri. S. R. Reddy, Hon'ble Chairman,
Navodaya Education Trust (NET®), Raichur.

Patron
Shri. N. Bhanth Reddy., Hon'ble Trustee,
Navodaya Education Trust (NET®), Raichur.

Chief Convener
Dr. Mallikarjun Sharma,
Principal and Dean, NIT, Raichur

Convener
Dr. K M Palaniswamy,
HOD/ ECE, NIT, Raichur

Coordinator
Prof. Sachin Kumar N
Assistant Professor, Dept of ECE, NITR.
Email: sachin.kumar@navodaya.edu.in
Contact no. : +91- 8618477445

Co-Coordinators
Prof. Md Shoaibuddin Madni
Assistant Professor, Dept of ECE, NIT
Email: shoaibuddin.madni@navodaya.edu.in
Contact no. : +91- 8792374743

Organizing Committee

1. Prof. Madhuri Devi Ch
2. Prof. Channaveeramma. E
3. Prof. Ramurthy Dasari
4. Prof. Laxmikanth Reddy
5. Prof. Amarappa Pagi
6. Prof. Priyadarshini N
7. Prof. Archana Patil
8. Prof. Reshma Arasikere
9. Prof. Gauravi Shetty

04.01.2018

Faculty Development Programme on
"Computation Techniques in Optics and Photonics"

To,

The Principal,

Warm Greeting!!!

Kind attention: Head of the Department / ECE,

Dear Sir/Madam,

Sub: Invitation for attending faculty development programme on *"Computation Techniques in Optics and Photonics"* reg.

We are pleased to inform that the Department of Electronics & Communication Engineering, Navodaya Institute of Technology is organizing a Faculty Development Programme on *"Computation Techniques in Optics and Photonics"* from 16th January to 22nd January 2018 (One Week) at our college premises. This programme will equip the faculty to teach the subject in better way and also enable them to offer final year project in this specialization. We invite your faculty members to participate in this FDP from your esteemed institution.

Please see the brochure enclosed with this for your perusal and circulation among the faculty members of ECE department.

For further details please feel free to contact.

Thanks & Regards

Head of Dept. Dr. K M Palaniswamy
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned with CamScanner

Registration Form

(photocopy may be used if required)

Name (in BLOCK letters): _____

_____

Academic Qualification : _____

Experience: _____

Designation & Department: _____

College Address: _____

_____

_____

Name of Affiliated University: _____

E mail : _____

Contact Number: _____

DD Number/ Date _____

Declaration:

The information furnished above is true to the best of my knowledge.

Date:

Place: Signature of the Applicant

Mr./Ms./Mrs. _____ is an employee (faculty)/PG student of our institute. He / She will be permitted to attend the programme if selected.

Date:

Place:

Signature & Seal

HOD/Head of Department

Electronics & Communication Engineering

Navodaya Institute of Technology (NIT)

Raichur-584 103

Chief Patron
Sri. S. R. Reddy, Hon'ble Chairman
Navodaya Education Trust (NET®), Raichur.

Patron
Sri. N. Bharath Reddy, Trustee
Navodaya Education Trust (NET®), Raichur.

Chief Convener
Dr. Mallikarjun Sharma., Dean/Principal, NIT, Raichur.

Convener
Dr. K M Palaniswamy, HOD,ECE, NITR

Coordinator
Prof. Sachin Kumar N
Assistant Professor, Dept of ECE, NITR.
Email: sachin.kumar@navodaya.edu.in
msachin834@navodaya.edu.in
Contact no. : +91- 8618477445

Co-Coordinators
Prof. Md Shoaibuddin Madni
Assistant Professor Dept of ECE, NIT
Email: shoaibuddin.madni@navodaya.edu.in
shoaib.madni7@gmail.com
Contact no. : +91- 8792374743

Organizing Committee

1. Prof. Madhuri Devi Ch
2. Prof. Channaveeramma. E
3. Prof. Ramurthy Dasari
4. Prof. Laxmikanth Reddy
5. Prof.. Amarappa Pagi
6. Prof. Priyadarshini N
7. Prof. Archana Patil
8. Prof. Reshma Arasikere
9. Prof. Gauravi Shetty

PRINCIPAL
Navodaya Institute of Technology
RAICHUR-584 103

NAVODAYA
Institute of Technology
RAICHUR



One Week Faculty Development Programme on Computation Techniques in Optics and Photonics

16th January to 22nd January 2018

ORGANIZED BY

Department of Electronics & Communication Engineering.

Navodaya Education Trust®
NAVODAYA INSTITUTE OF TECHNOLOGY
Navodaya Nagar, Bijanagera Road,
Raichur-584103, Karnataka, India.

Scanned with CamScanner

About the Institute:

Navodaya Institute of Technology(NIT), Raichur, established in 2008, is affiliated to Visvesvaraya Technological University (Belagavi). The institute is approved by All India Council for Technical Education (AICTE), New Delhi.

The guiding spirit and the driving force for all the progress and achievements of the Institution is our Honourable chairman of Navodaya Education Trust (NETR), Sri S. R. Reddy. NIT focuses on promoting quality technical education in north Karnataka region and Hyderabad-Karnataka region in particular.

The education is of paramount importance for socio-economic development. The institute focuses on integrated scientific training with value based education, to enable overall development of the aspirants.

NIT is all-inclusive campus, with Administrative block, Library block, independent Academic blocks for each department, Separate Hostels for Boys and Girls, Canteen, Bank(SBI), in-campus ATM , Temple, Gym and Playground, all housed in a vast campus of more than 30 acres land surrounded by hillocks and lake.

About the Department:

The Electronics and Communication Engineering department aims in delivering excellence in theoretical and experimental value based education to students. The Department was started in the year 2008 with an intake of 60 students with the vision to provide the quality technical education. The department has good infrastructure with well-equipped laboratories and necessary softwares.

The Department has bagged many ranks in GATE. Many students were placed in various prestigious MNC's and pursuing their higher studies in NIT's and IIT's.

The Department also offers Postgraduate programme in Digital Communication and Networking (DCN), was started in the academic year 2014-15.The department has highly qualified, experienced and dedicated faculties with PhD and Post graduation (M.E. / M.Tech.) degrees. The workshops and Training programmes are planned regularly for the benefit of students and faculty members.

Vision: To excel in education and research in Electronics & Communication Engineering, thereby providing service to the society and human kind.

Mission: To create competent professionals by training the students in the design & implementation of Electronics and Communication Engineering products helping there by students to develop the skills to solve technological problems of modern society.

RESOURCE PERSON:

Prof. Venkatachalam K.

Department of Electronics and Communication Engineering,
Mount Zion College of Engineering and Technology, Lenavilakku,
Padukkottai, Tamil Nadu

About the FDP:

This FDP aims to give hands-on lab sessions, tool demonstrations, and discussion/presentation sessions with few lectures.

Major Course Contents:

- Optical Communication Technology: Possibilities and challenges.
- Slow Light in Photonic Crystal for Faster Photonic Devices.
- Recent trends in Photonics.
- Experimental study for Simulation of Solar cell.
- Nitride devices their fabrication and applications.
- Characterization (XPS, AFM, SEM etc) of MOCVD grown and fabricated III-Nitride devices.
- Graphene/GaN Nanowire based Photo-detectors and Nano LEDs.
- Modeling & Simulations of microwave components & devices, Optimization Techniques.
- Si v/s III - IV group Integrated Photonics, MEMS in Photonics, Modeling and Simulations of Metamaterials.

Important dates

FDP date : 16th January to 22nd January 2018
Last date for registration : 13th January 2018.
Registration fees : Rs. 1500 /-

Registration fee may be paid in the form of DD in favour of Principal, Navodaya Institute of Technology, payable at SBI, NIT campus, Raichur.

VENUE : NIT Auditorium, NIT, Raichur

Note:

1. Accommodation will be provided on prior request.
2. Lunch, Tea will be provided during the training in the campus.

Scanned with CamScanner



Dr. K. VENKATACHALAM,
406-5/1B, Pudumanailst Street,
Rayavaram, Pudukkottai-622506,
Tamilnadu, India.
Mobile : 9843348526
E-Mail: venkatachalamece@gmail.com

CURRICULUM VITAE

Date of Birth: March 12, 1972
Place of Birth: Rayavaram
Citizenship: Indian

EDUCATION

1/2014- 4/2018	**Doctor of Philosophy** Department of Electronics and Communication Engineering National Institute of Technology, Tiruchirappalli
8/2010-5/2013	**Master of Engineering: ME** (Optical Communication) Department of Electronics and Communication Engineering AlagappaChettiar College of Engineering and Technology Karaikudi
7/2001-11/2004	**Bachelor of Technology: B.Tech** (Electronics and Communication) Department of Electronics and Communication Engineering National Institute of Technology Tiruchirappalli

PUBLICATIONS IN SCI JOURNALS

1. **Venkatachalam, K., D. Sriram Kumar and S. Robinson** (2017) Investigation on 2D-Photonic Crystal based Eight Channel Wavelength Division Demultiplexer, *Photon. Netw. Commun.*, Vol.34 (1), pp.100-110.
2. **VenkatachalamKannaiyan, Sriram Kumar Dhamodharan and Robinson Savarimuthu**(2016)Investigation on modified quasi-square PCRR based demultiplexer for WDM applications, *Opt. Quant. Electron.*, Vol.48 (12), pp.1-12.
3. **Venkatachalam, K., D. Sriram Kumar and S. Robinson** (2016) Performance analysis of 2D-photonic crystal based eight channel wavelength division demultiplexer. *Optik*, **127 (20)**, 8819-8826.

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned with CamScanner

PUBLICATIONS IN SCOPUS INDEXED JOURNALS

4. **VenkatachalamKannaiyan, Robinson Savarimuthu and Sriram Kumar Dhamodharan** (2018) Investigation of 2D-Photonic crystal resonant cavity based WDM demultiplexer, *Opto-Electronics Review*, Vol.26 (2), pp. 108-115.

5. **VenkatachalamKannaiyan, Sriram Kumar Dhamodharan and Robinson Savarimuthu** (2017) Performance analysis of 2DPC Octagonal Ring Resonator based Eight Channel Demultiplexer, *Optica Applicata*, Vol.XLVII (1), pp.7-18.

6. **VenkatachalamKannaiyan, Robinson Savarimuthu and Sriram Kumar Dhamodharan** (2017) Performance analysis of an eight channel demultiplexer using a 2D-photonic crystal quasi square ring resonator, *Opto-Electronics Review*, Vol.25 (2), pp.74 -79.

7. **Venkatachalam Kannaiyan, Sriram Kumar Dhamodharan, Robinson Savarimuthu (2018)** Investigation of 2D-PC Ring Resonator based Demultiplexer for ITU-T G.694.1 WDM Systems, *Journal of Optical Communications*, **DOI:** https://doi.org/10.1515/joc-2018-0037.

8. **Venkatachalam Kannaiyan, Sriram Kumar Dhamodharan, Robinson Savarimuthu (2019)** 2D-Photonic Crystal based Demultiplexer for WDM Systems – A Review, *Journal of Optical Communications*, **DOI:** https://doi.org/10.1515/joc-2018-0216.

PUBLICATIONS IN INTERNATIONAL (Annexure II) JOURNALS

9. **Venkatachalam, K., S. Robinson, D. Sriram Kumar and K. Kavithra** (2017) Investigation into Performance Analysis of 2DPC based Band Pass Filter, *International Journal of Photonics and Optical Technology*, Vol.3(1), pp.10-15.

10. **Venkatachalam, K., S. Robinson and S. Umamaheswari** (2016) Two Dimensional Photonic Crystal based Four Channel Demultiplexer for ITU.T.G 694.2 CWDM Systems. *International Journal of Photonics and Optical Technology*, **2(3)**, 37-41.

PRESENTATIONS IN CONFERENCES

1. **Venkatachalam, K., D. SriramKumar and S. Robinson,** Design and Analysis the Performance of Dual Ring Resonator based 2D-Photonic Crystal WDDM" Presented at NIT, Calicut (OPTICS'17),AIP Conference Proceedings, June 2017, (020016) pp.1-6.
2. **Venkatachalam, K., D. SriramKumar and S. Robinson,** Investigation on quasi square ring resonator based 2DPC eight channel dense wavelength division demultiplexer, Presented at IIT, Kanpur. (13th International Conference on Fiber Optics and Photonics, OSA Technical Digest (online) Optical Society of America Conference Proceedings, Dec 2016,W3A.92.
3. **Venkatachalam, K., D. Sriram Kumar and S. Robinson,** Design and analysis of quasi square ring resonator based 2DPC Wavelength division demultiplexer –IEEE CRALT 2016, IISC-Bangalore.
4. **Venkatachalam, K., D. Sriram Kumar and S. Robinson,** Analysis and Design of Photonic Crystal based Demultiplexer, Presented at Madras Institute of Technology, Chennai, IEEE Conference Proceedings (ICSCN 2015), Aug 2015, pp.1-4.
5. **Venkatachalam, K** Offline Handwritten English Character Recognition organized by the ISTE, IEEE & IETE Professional Societies, SSM College of Engineering, Kumarapalayam.
6. **Venkatachalam, K** Offline Handwritten English Character Recognition. MKCE-Confluence '13.
7. **Venkatachalam, K** Offline Handwritten English Character Recognition. ACCET-NCMOC '13

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned with CamScanner

Schedule for Faculty Development Program (FDP) on **"Computation Techniques in Optics and Photonics"**

Organised By

Navodaya Institute of Technology: Raichur **(16/01/2018 to 22/01/2018)**

Timing of Each Day Session

Date/Day	Session I 9:30AM-11:00AM		Session II 11:30AM-1:00PM		Session III 2:00PM-5:00PM
16.01.2018 Tuesday	Introduction Optical Communication Technology		Optical Communication Technology: Possibilities		Optical Communication Technology: Challenges.
17.01.2018 Wednesday	Slow Light in Photonic Crystal for Faster Photonic Devices		Recent trends in Photonics.		Recent trends in Photonics.
18.01.2018 Thursday	Hands-on training on Simulation of Solar cell		Nitride devices their fabrication		Applications of Nitride devices
19.01.2018 Friday	XPS Characterization of MOCVD		AFM & SEM Characterization of MOCVD		Fabricated III-Nitride devices.
20.01.2016 Saturday	Graphene/GaN Nanowire based Photo-detectors and Nano LEDs		Graphene/GaN Nanowire based Photo-detectors and Nano LEDs		Modeling & Simulations of microwave components & devices
21.01.2018 Sunday	Modeling & Simulations of microwave components & devices		Optimization Techniques.		Optimization Techniques.
22.01.2018 Monday	Si v/s III - IV group Integrated Photonics		MEMS in Photonics		Modeling and Simulations of Metamaterials.

TEA BREAK

LUNCH BREAK

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

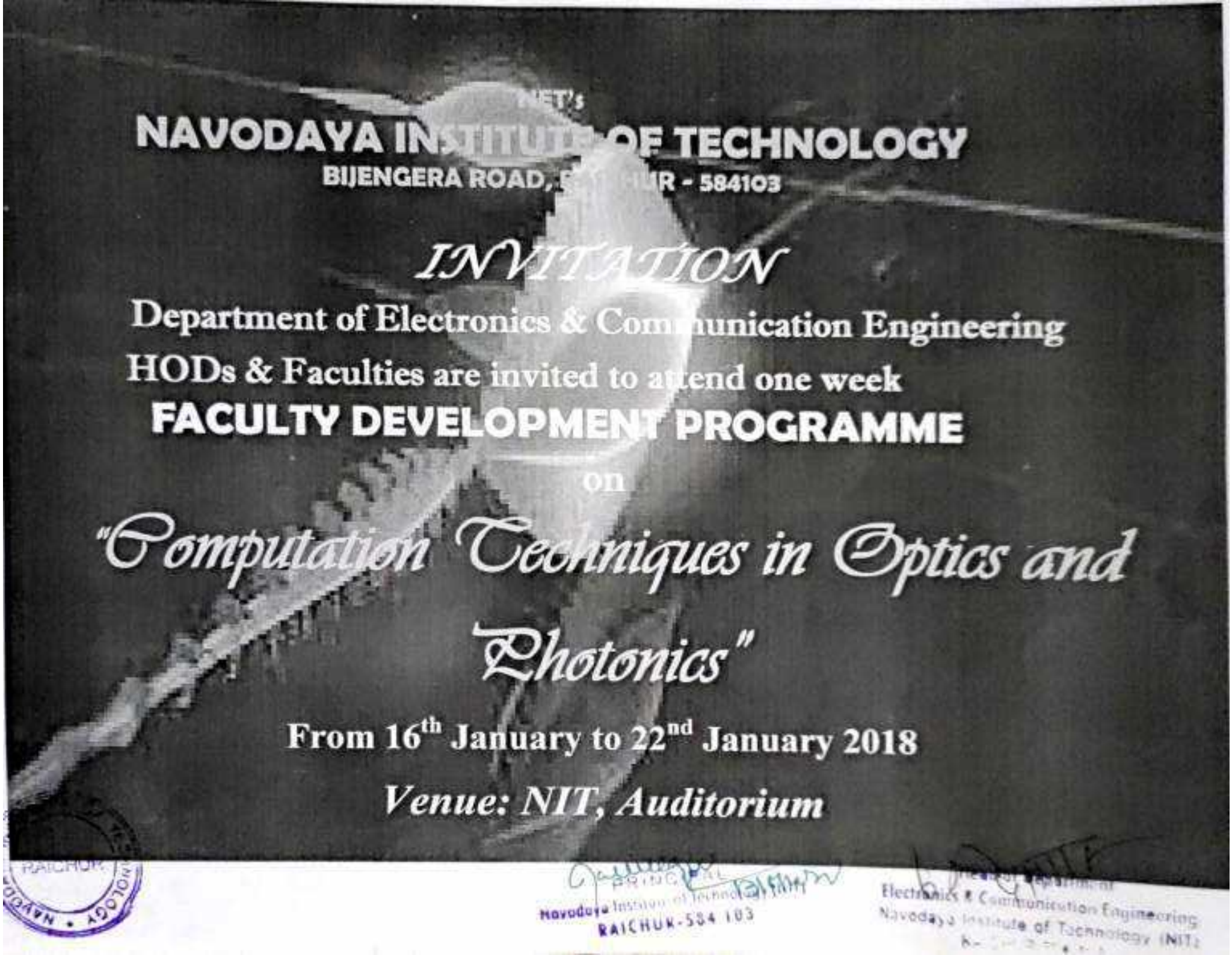
15/1/18

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

13/1/2018

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned with CamScanner



Navodaya Institute of Technology
RAICHUR-584 103

Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)

Scanned with CamScanner

Date: 02.01.2018

From,

Dr. K.M.Palaniswamy,

Professor and Head,

Navodaya Institute of Technology,

Raichur-584103

To,

The Principal,

Navodaya Institute of Technology,

Raichur-584103

Sir,

Subject: Seminar Hall for FDP on "*Computation Techniques in Optics and Photonics*" from 16th January to 22nd January 2018

We have proposed to conduct Workshop on "***Computation Techniques in Optics and Photonics***". For this Purpose, We need Seminar Hall from 16th to 22nd January 2018, Time-9.30am-5.30pm.

Thanking You,

Yours faithfully

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

PRINCIPAL
Navodaya Institute of Technology (NIT)

Scanned with CamScanner

From,

13.01.2018

Dr. K M Palaniswamy,

HOD/ ECE,

NIT, Raichur

To,

The Dean cum Principal,

NIT, Raichur.

Sir,

Sub: Requisition for Accommodation and Food -Reg

I bring to your kind notice that we are conducting the one week FDP on ***"Computation Techniques in Optics and Photonics"*** in the department of ECE from 16th to 22nd January 2018. For that we want to arrange the accommodation and food for the Resource Person in the above days. So kindly do the needful.

Resource Person Details,

Prof. Venkatachalam K
Assistant Professor,
Department of Electronics and Communication Engineering,
Mount Zion College of Engineering and Technology, Lenavilakku,
Pudukkottai, Tamil Nadu

Thanking you,

Approved 13/1/18

13/1/18

HOD/ECE
Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY • RAICHUR

PRINCIPAL 13/1/2018
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned with CamScanner

NAVODAYA Institute of Technology Raichur

Attendance Report

Seven days faculty development program on "Computation Techniques in Optics and Photonics"

Sl.No	Name of the participant	16/1/2018	17/1/2018	18/1/2018	19/1/2018	20/1/2018	21/1/2018	22/1/2018
1	Channaveeswara E							
2	Madhuri Devichandan							
3	Rammurthy . D							
4	Amarappa Pagi							
5	Sathish Kumar							
6	Reshma . A							
7	Md Shoaib uddin							
8	Punyadharshini							
9	Srinivas . D							
10	Gauravi Setty							
11	Laxmikanth							
12	Sudhakar							
13	Kiran							
14	Sara							
15	Geetha . M							
16	Madhu Babu							
17	Satish							

23/1/18

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

PRINCIPAL 23/1/2018
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Scanned with CamScanner

NAVODAYA

Attendance Report

Seven days faculty development program on "Computation Techniques in Optics and Photonics"

Sl No	Name of the participant	16/1/2018	17/1/2018	18/1/2018	19/1/2018	20/1/2018	21/1/2018	22/1/2018
18	Daniel							
19	Velu.							
20	gangadhar.							
21	Jyothi							
22	Manjunath							
23	Praveen							
24	Kailash.							
25	Shravan gowda							

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT),
Raichur-584 103

PRINCIPAL 13/4/2021
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Scanned with CamScanner

Faculty Development Program Evaluation Form

We would appreciate if you could take a few minutes to share your opinion with us so we can serve you better

FDP title: **Computation Techniques in Optics and Photonics**

Date: **16th January to 22nd January 2018**

Resource person: **Prof. Venkatachalam K**

		Strongly agree	Agree	Neutral	Disagree	Strongly Disagree
1.	The content was as described in publicity materials	✓	2	3	4	5
2.	The FDP was applicable to my research	1	2	✓	4	5
3.	I will recommend this FDP to other conservators	1	✓	3	4	5
4.	The program was well placed within the allotted time	1	✓	3	4	5
5.	The resourse person is good communicator	1	✓	3	4	5
6.	The material was presented in an organized manner	1	✓	3	4	5
7.	The instructor was knowledgeable on the topic	✓	2	3	4	5
8.	I would be interested in attending a follow-up, more advanced FDP on this same subject	✓	2	3	4	5
9.	Given the topic, was this FDP	☐ a. Too short	☑ b. Right length		☐ c. Too long	
10.	In your opinion, was this FDP	☐ a. introductory	☑ b. Intermediate		☐ c. Advanced	
11.	What did you most appreciate/enjoy/think was best about the course? Any suggestions for improvement? Organised in good Manner Executed in good way					

Thank You!

Please return this form to the coordinator at the end of workshop.

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

PRINCIPAL
Navodaya Institute of Technology
13/1/2018

Scanned with CamScanner

NAVODAYA INSTITUTE OF TECHNOLOGY, RAICHUR

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

SUMMARY OF FACULTY DEVELOPMENT PROGRAMME ON

"Computation Techniques in Optics and Photonics"

Department of ECE organized faculty development programme on ***"Computation Techniques in Optics and Photonics"*** was held at Navodaya Institute of Technology, seminar hall from 16th January to 22nd January 2018. The session was inaugurated with prayer song followed by the welcome speech by the Head of the department, Dr. K M Palaniswamy.

Principal presided over the function. **Prof. Venkatachalam K** was the resource persons.

First day Prof. Venkatachalam K gave a talk on Optical Communication Technology: Possibilities and challenges.

Second day Prof. Venkatachalam K explained about Slow Light in Photonic Crystal for Faster Photonic Devices and Recent trends in Photonics.

Third and Fourth day Prof. Venkatachalam K gave hands-on training on Simulation of Solar cell, Nitride devices their fabrication and applications, Characterization (XPS, AFM, SEM etc) of MOCVD grown and fabricated III-Nitride devices.

Fifth and Sixth day Prof. Venkatachalam K explained Graphene/GaN Nanowire based Photo-detectors and Nano LEDs, Modeling & Simulations of microwave components & devices, Optimization Techniques.

Seventh day Prof. Venkatachalam K explained Si v/s III - IV group Integrated Photonics, MEMS in Photonics, Modeling and Simulations of Metamaterials.



NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

PRINCIPAL 13/1
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned with CamScanner

NAVODAYA
Institute of Technology
RAICHUR

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Certificate of Participation

Certified that Mr./Ms. Preethi Sagar Pattankude

Of Navodaya Institute of Technology, Raichur has successfully participated in Five Days Faculty Development Program on ***"Computation Techniques in Optics and Photonics"*** organized by the department of Electronics & Communication Engineering, Navodaya Institute of Technology, Raichur, Karnataka from 16-01-2018 to 22-01-2018.

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

CO-ORDINATOR

HOD

PRINCIPAL 13/1/2018

Navodaya Institute of Technology (NIT)
RAICHUR-584 10:

PRINCIPAL

Scanned with CamScanner

NAVODAYA
Institute of Technology
RAICHUR

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Certificate of Participation

Certified that Mr./Ms. Madhu Babu. B.

Of NIT, Raichur has successfully participated in Five Days Faculty Development Program on ***"Computation Techniques in Optics and Photonics"*** organized by the department of Electronics & Communication Engineering, Navodaya Institute of Technology, Raichur, Karnataka from 16-01-2018 to 22-01-2018.

CO-ORDINATOR

HOD

PRINCIPAL

Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned with CamScanner

NAVODAYA
Institute of Technology
RAICHUR

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Certificate of Participation

Certified that Mr./Ms. Madhuri Devi Chodey

Of NIT, Raichur has successfully participated in Five Days Faculty Development Program on ***"Computation Techniques in Optics and Photonics"*** organized by the department of Electronics & Communication Engineering, Navodaya Institute of Technology, Raichur, Karnataka from 16-01-2018 to 22-01-2018.

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

CO-ORDINATOR

HOD

PRINCIPAL
Navodaya Institute of Technology (NIT)

PRINCIPAL

Scanned with CamScanner

NAVODAYA
Institute of Technology
RAICHUR

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Certificate of Participation

Certified that Mr./Ms. Sana Anjum

Of NIT, Raichur has successfully participated in Five Days Faculty Development Program on ***"Computation Techniques in Optics and Photonics"*** organized by the department of Electronics & Communication Engineering, Navodaya Institute of Technology, Raichur, Karnataka from 16-01-2018 to 22-01-2018.

CO-ORDINATOR **HOD** **PRINCIPAL**

Scanned with CamScanner

Date: 08.08.2016

From,

Dr. K.M. Palaniswamy,
Professor and Head,
Navodaya Institute of Technology,
Raichur-584103

To,

The Principal,
Navodaya Institute of Technology,
Raichur-584103

Sir,

Subject: Permission for conducting two Days FDP on **Aug 12th & 13th 2016** Reg

From ECE dept., we are going to conduct FDP on ***Applications of SCILAB in DSP and Image Processing*** in association with Orange Research Labs, Hyderabad.

The FDP is to be carried out for two days 12.8.2016 (Friday) and 13.8.2016 (Saturday). So please provide permission to conduct the above FDP. The proposed expense is attached in next page. The Expected expense is Rs. 20, 000/- whose details are provided in next page.

Thanking you,

The faculty Coordinators are
1. Prof. Channaveeramma E
2. Prof. Gauravi Shetty

Approved
9/8/16

Yours Faithfully,

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

PRINCIPAL 13/8/2016
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned with CamScanner

NET's

NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

Faculty Development Programme - PROPOSAL

Date: 05.08.2016

1	**ACADEMIC YEAR**	2016-17
2	**TYPE OF THE PROGRAMME PROPOSED**	Faculty Development Programme
3	**TITLE OF THE PROGRAMME:**	***"Applications of SCILAB in DSP and Image Processing"***
4	**BROAD OBJECTIVE OF THE PROGRAMME:**	This FDP aims to give hands on training in Internet of Things and programming Raspberry Pi using Python. **Major Course Contents:** • Why Scilab? Pros & Cons, Software Architecture; Variables and datatypes • Basic Matrix operations Indexing Using Built in functions • Loops- for, while, do Conditions- If else, select case • Basic input-output functions • How to create user defined functions Passing and returning multiple arguments • Simple plots – 1D,2D, 3D • Solving Equations Simple Examples • Architecture Matrix operation, Flow control, Introduction to MatlabFunctions,Structures, cells, Plots Interfacing Matlab and ScilabM to S conversion
5	**NAME OF THE INSTITUTION**	NETs NAVODAYA INSTITUTE OF TECHNOLOGY
6	**NAME OF THE DEPARTMENT(S)**	ELECTRONICS AND COMMINICATION ENGINEERING
7	**BENEFICIARY**	Faculty Members
8	**DATE/DURATION OF SUCH PROGRAMMES CONDUCTED IF ANY IN THIS ACADEMIC YEAR:**	**Aug 12th&13th 2016**
9	**FLIGIBILITY CRITERIA SET:**	Faculties from Electronics and Communication Engineering.

INSTITUTE OF TECHNOLOGY RAICHUR

PRINCIPAL 13/8/2016

Navodaya Institute of Technology

RAICHUR

Scanned with CamScanner

10	**NUMBER OF DAYS REQUIRED TO ORGANISE THE PROGRAMME**	Two Day FDP
11	**PROPOSED DURATION OF THE PROGRAMME**	12 hours
12	**TOTAL NO. OF PERIODS REQUIRED FOR THE PROGRAMME**	12 Periods
13	**NO. OF PERIODS REQUIRED PER DAY**	06 Periods
14	**LOCATION DETAILS:**	NETs NIT Auditorium
15	**MINIMUM NO OF PARTICIPANTS:**	30
16	**DETAILS OF SPONSORERS:**	-
17	**DETAILS OF RESOURCE PERSONS:**	**1. Prof.K.Ganesan** Associate Professor, SRS Institute of Technology, Salem. **2. Prof.R.Senthil Kumar** Associate Professor, IRTT, Erode, TamilNadu
18	**REGISTRATION FEE WITH CERTIFICATION:**	Rs 300 per participant
19	**REGISTRATION FEE WITHOUT CERTIFICATION:**	Rs - per participant.
20	**FACULTY COORDINATOR:**	1. Prof. Channaveeramma E 2. Prof. Gauravi Shetty
22	**PROGRAMME PROPOSED BY:**	Department of ECE
23	**ANY OTHER RELEVANT MATTER:**	Nil
24	**ESTIMATED BUDGET IN Rs:**	Rs.20,000

This is to the Honorable Chairman of NIT for kind approval:

PRINCIPAL
Navodaya Institute of Technology (NIT),
RAICHUR-584 103

HOD
Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
Raichur-584 103

EVENT COORDINATOR

APPROVED/NOTAPPROVED

13/11/16
PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned with CamScanner

NET's
NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

Tel : +91 8532-200115, 240334, Fax : +91-8532-240054
E-Mail :hodece.nit@navodaya.edu.in

CIRCULAR

Cir.No:2016-17/FDP/TF/01

Chief Patron
Sri. S. R. Reddy, Hon'ble Chairman
Navodaya Education Trust (NET®), Raichur.

Patron
Sri. N. Bharath Reddy, Trustee
Navodaya Education Trust (NET®), Raichur.

Chief Convener
Dr. Mallikarjun Sharma., Dean/Principal, NIT, Raichur.

Convener
Dr. K M Palaniswamy, HOD,ECE, NITR

Coordinator
Prof. Channaveeramma E
Assistant Professor, Dept of ECE, NITR.
Email: channaveeramma.e@navodaya.edu.in
Contact no. : +91- 8884977757

Co-Coordinators
Prof. Gauravi Shetty
Assistant Professor Dept of ECE, NIT
Email: gauravi.shetty@navodaya.edu.in
Contact no. : +91-7411747751

Organizing Committee

1. Prof. Channaveeramma.E
2. Prof. Madhuri devi Ch
3. Prof. Rammurthy D
4. Prof. Sachin kumar
5. Prof. LaxmikanthReddi
6. Prof. N.Priyadarshini
7. Prof. Reshma Arasikeri
8. Prof. Md Shoaibuddin Madni
9. Prof. Archana Patil G

08.08.2016

Respected Sir / Madam,

Greetings from Navodaya Institute of Technology, Raichur…

The department of Electronics and Communication Engineering is *organizing* a Two day FDP on **"Applications of SCILAB in DSP and Image Processing"** *The* attached brochure may kindly be circulated among the students and faculty members / colleagues.

This FDP aims to give training on Applications of SCILAB in DSP and Image Processing

We are pleased to inform that the department of Electronics and Communication Engineering, Navodaya Institute of Technology is organizing a Faculty Development Programme during **August 12th&13th 2016**

In this context, we look forward your support and request you to encourage participation of professionals by circulating the attached brochure.

Thanking you,
Yours sincerely,

Dr. K M Palaniswamy,
Convener

Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY • RAICHUR

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned with CamScanner

NET's
NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Ph. No.:08532-220330, Extn-703,702,713, 07353295425
e-mail:hodece.nit@navodaya.edu.in

Chief Patron
Shri. S. R. Reddy, Hon'ble Chairman,
Navodaya Education Trust (NET®),
Raichur.

Patron
Shri. N. Bharath Reddy., Hon'ble Trustee.
Navodaya Education Trust (NET®), Raichur.

Chief Convener
Dr. Mallikarjun Sharma,
Principal and Dean, NIT, Raichur

Convener
Dr. K M Palaniswamy,
HOD/ ECE, NIT, Raichur

Coordinator
Prof. Channaveeramma E
Assistant Professor, Dept of ECE, NITR.
Email: channaveeramma.e@navodaya.edu.in
Contact no. : +91- 8884977757

Co-Coordinators
Prof. Gauravi Shetty
Assistant Professor, Dept of ECE, NIT
Email: gauravi.shetty@navodaya.edu.in
Contact no. : +91-7411747751

Organizing Committee

1. Prof. Channaveeramma.E
2. Prof. Madhuri devi Ch
3. Prof. Rammurthy D
4. Prof. Sachin kumar
5. Prof. LaxmikanthReddi
6. Prof. N.Priyadarshini
7. Prof. Reshma Arasikeri
8. Prof. Md Shoaibuddin Madni
9. Prof. Archana Patil G

08.08.2016

Faculty Development Programme on
"Applications of SCILAB in DSP and Image Processing"

To

The Principal,

Warm Greeting!!!

Kind attention: Head of the Department / ECE,

Dear Sir/Madam,

Sub: Invitation for attending faculty development programme on ***"Applications of SCILAB in DSP and Image Processing"*** reg.

We are pleased to inform that the Department of Electronics & Communication Engineering, Navodaya Institute of Technology is organizing a Faculty Development Programme on "Applications of SCILAB in DSP and Image Processing" during August 12th & 13th 2016 (2 days) at our college premises. This programme will equip the faculty to teach the subject in better way and also enable them to offer final year project in this specialization. We invite your faculty members to participate in this FDP from your esteemed institution.

Please see the brochure enclosed with this for your perusal and circulation among the faculty members of ECE department.

For further details please feel free to contact.

Thanks & Regards

Dr. K M Palaniswamy
Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Scanned with CamScanner

Registration Form
(photocopy may be used if required)

Name (in BLOCK letters): _____

_____

Academic Qualification : _____

Experience: _____

Designation & Department: _____

College Address: _____

_____

_____

Name of Affiliated University: _____

E mail : _____

Contact Number: _____

DD Number/ Date _____

Declaration:
The information furnished above is true to the best of my knowledge.

Date:
Place: Signature of the Applicant

Mr./Ms./Mrs. _____
__ is an employee (faculty)/PG student of our institute. He / She will be permitted to attend the programme if selected.

Date:
Place:

Signature & Seal
HOD/Head of the Institution
Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Chief Patron
Sri. S. R. Reddy, Hon'ble Chairman
Navodaya Education Trust (NET®), Raichur.

Patron
Sri. N. Bharath Reddy, Trustee
Navodaya Education Trust (NET®), Raichur.

Chief Convener
D.r Mallikarjun Sharma., Dean/Principal, NIT, Raichur.

Convener
Dr. K M Palaniswamy, HOD,ECE, NITR

Coordinator
Prof. Channaveeramma E
Assistant Professor, Dept of ECE, NITR.
Email: channaveeramma.e@navodaya.edu.in
Contact no. : +91- 8884977757

Co-Coordinators
Prof. Gauravi Shetty
Assistant Professor Dept of ECE, NIT
Email: gauravi.shetty@navodaya.edu.in
Contact no. : +91-7411747751

Organizing Committee

1. Prof. Channaveeramma.E
2. Prof. Madhuri devi Ch
3. Prof. Rammurthy D
4. Prof. Sachin kumar
5. Prof. LaxmikanthReddi
6. Prof. N.Priyadarshini
7. Prof. Reshma Arasikeri
8. Prof. Md Shoaibuddin Madni
9. Prof. Archana Patil G

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA
Institute of Technology
RAICHUR



Hands On Two Days Faculty Development Programme

on

Applications of SCILAB in DSP and Image Processing

Aug 12th &13th 2016

ORGANIZED BY
Department of Electronics & Communication Engineering.

Navodaya Education Trust®
NAVODAYA INSTITUTE OF TECHNOLOGY
Navodaya Nagar, Bijanagera Road,
Raichur-584103, Karnataka, India.

Scanned with CamScanner

About the Institute:

Navodaya Institute of Technology(NIT), Raichur, established in 2008, is affiliated to Visvesvaraya Technological University (Belagavi). The Institute is approved by All India Council for Technical Education (AICTE), New Delhi.

The guiding spirit and the driving force for all the progress and achievements of the Institution is our Honourable chairman of Navodaya Education Trust (NET®), Sri S. R. Reddy. NIT focuses on promoting quality technical education in north Karnataka region and Hyderabad-Karnataka region in particular.

The education is of paramount importance for socio-economic development. The institute focuses on integrated scientific training with value based education, to enable overall development of the aspirants.

NIT is all-inclusive campus, with Administrative block, Library block, independent Academic blocks for each department, Separate Hostels for Boys and Girls, Canteen, Bank(SBI), in-campus ATM , Temple, Gym and Playground, all housed in a vast campus of more than 30 acres land surrounded by hillocks and lake.

About the Department:

The Electronics and Communication Engineering department aims in delivering excellence in theoretical and experimental value based education to students. The Department was started in the year 2008 with an intake of 60 students with the vision to provide the quality technical education. The department has good infrastructure with well-equipped laboratories and necessary softwares.

The Department has bagged many ranks in GATE. Many students were placed in various prestigious MNC's and pursuing their higher studies in NIT's and IIT's.

The Department also offers Postgraduate programme in Digital Communication and Networking (DCN), was started in the academic year 2014-15.The department has highly qualified, experienced and dedicated faculties with PhD and Post graduation (M.E. / M.Tech.) degrees. The workshops and Training programmes are planned regularly for the benefit of students and faculty members.

Vision: To excel in education and research in Electronics & Communication Engineering, thereby providing service to the society and human kind.

Mission: To create competent professionals by training the students in the design & implementation of Electronics and Communication Engineering products helping there by students to develop the skills to solve technological problems of modern society.

RESOURCE PERSONS:

1. Prof.K.Ganesan

Associate Professor,

SRS Institute of Technology, Salem.

2. Prof.R.Senthil Kumar

Associate Professor,

IRTT, Erode, TamilNadu.

About the workshop:

The FDP mainly aims to equip teachers with additional in depth knowledge in the field of image processing and its applications. Broad category of topics that are intended to cover in this FDP is given below:

Major Course Contents:

- Why Scilab? Pros & Cons, Software Architecture, Variables and datatypes
- Basic Matrix operations Indexing Using Built in functions
- Loops- for, while, do Conditions- If else, select case
- Basic input-output functions
- How to create user defined functions Passing and returning multiple arguments
- Simple plots – 1D,2D, 3D
- Solving Equations, Simple Examples
- Architecture Matrix operation, Flow control, Introduction to Matlab: Functions, Structures, cells, Plots
- Interfacing Matlab and Scilab: M to S conversion

Important dates

FDP date	:	**Aug 12th &13th 2016**
Last date for registration	:	**20th Aug 2017.**
Registration fees	:	**Rs. 300 /-**

Registration fee may be paid in the form of DD in favour of Principal, Navodaya Institute of Technology, payable at SBI, NET campus, Raichur.

VENUE : NIT Auditorium, NIT, Raichur

Note:

1. Accommodation will be provided on prior request.
1. Lunch, Tea will be provided during the training in the campus.

Scanned with CamScanner

NAVODAYA
Institute of Technology
RAICHUR

Inaugural Ceremony Invitation

Two Day Faculty Development Programme

On

APPLICATIONS OF SCILAB IN DSP AND IMAGE PROCESSING

AUG 12"&13" 2016

ORGANIZED BY

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING.

Resource Persons

1) **PROF.K.GANESAN**
ASSOCIATE PROFESSOR,
SRS INSTITUTE OF TECHNOLOGY, SALEM.

2) **PROF.R.SENTHIL KUMAR**
ASSISTANT PROFESSOR,
IRTT, ERODE, TAMILNADU.

PROGRAM FLOW

1. Introduction
2. Invocation Song.
3. Lighting the Lamp.
4. Welcome Address by Dr.K.M.PalaniSwamy.
5. Introduction of Resource Persons by Prof.Channaveeramma E.
6. Chief Guest Address.
7. Presidential Address.
8. Vote of Thanks by Prof.Gauravi Shetty & Continuation of FDP.

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned with CamScanner

Schedule for Faculty Development Program (FDP) on "***Applications of SCILAB in DSP and Image Processing***"

Organised By

Navodaya Institute of Technology: Raichur (**Aug 12th &13th 2016**)

Timing of Each Day Session

Date/Day	Session I 9:30AM-11:00AM		Session II 11:30AM-1:00PM		Session III 2:00PM-5:00PM
12.08.2016 Friday	Why SCILAB? Pros & Cons, Software Architecture, Variables and data types.	TEA BREAK	Basic Matrix operations indexing using built in functions, Loops- for, while, do;	LUNCH BREAK	Conditions- If else, select case, Basic input-output functions, How to create user defined functions Passing and returning multiple arguments.
13.08.2016 Saturday	Simple plots – 1D, 2D, 3D; Architecture Matrix operation, Flow control,		Introduction to MATLAB: Functions, Structures, cells, Plots;		Interfacing MATLAB and SCILAB: M to S conversion.

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned with CamScanner

Date: 12.8.2016

From,

Dr. K.M. Palaniswamy,

Professor and Head,

Navodaya Institute of Technology,

Raichur-584103

To,

The Principal,

Navodaya Institute of Technology,

Raichur-584103

Sir,

Subject: Seminar Hall for FDP on Applications of SCILAB in DSP and Image Processing on August 12th & 13th 2016.

We have proposed to conduct Workshop on "Applications of SCILAB in DSP and Image Processing" on August 12th & 13th 2016. For this Purpose, We need Seminar Hall on August 12th & 13th 2016, Friday and Saturday, Time-9.30am-5.30pm.

Thanking You,

To Mr. Amit
Arrange
11.8.16

Yours faithfully

12/8/16

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY * RAICHUR

12/8/2016

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned with CamScanner

From,

10.08.2016

Dr. K M Palaniswamy,

HOD/ ECE,

NIT, Raichur

To,

The Dean cum Principal,

NIT, Raichur.

Sir,

Sub: Requisition for Accommodation and Food -Reg

I bring to your kind notice that we are conducting the Two days FDP on "***Applications of SCILAB in DSP and Image Processing***" our department on **August 12th&13th 2016.**

For that we want to arrange the accommodation and food for the Resource Persons in the above days. So kindly do the needful.

Resource Person Details,

1. **Prof.K.Ganesan**
   Associate Professor,
   SRS Institute of Technology, Salem.
2. **Prof.R.Senthil Kumar**
   Associate Professor,
   IRTT, Erode, TamilNadu

To Mr. Manager for n.a.
10-8-16

Thanking you,

HOD/ECE 10/8/16
Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

PRINCIPAL 13/8/2016
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Scanned with CamScanner

NAVODAYA
Institute of Technology
RAICHUR

Attendance Report

Two day faculty development program on "Applications of SCILAB in DSP and Image Processing"

Sl.No	Name of the participant	12/8/2016	13/8/2016
1.	Madhuri Devi Chodey		
2	Sachin Kumar		
3	Md. Shoaibuddin		
4	Gaurav Shetty		
5.	Rammurthy. D	12/8/16	13/8/16
6.	Channaveeramma. E		
7.	Smitha. Shetty		
8.	Lanmikanth Reddy		
9.	Madan Kumar		
10.	Priyadharshini		
11.	Reshma Arasikeri		
12.	Akbar Patil		
13.	Daniel		

INSTITUTE OF TECHNOLOGY RAICHUR NAVODAYA

13/8/16

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
Raichur-584 103

13/8/16
PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned with CamScanner

NAVODAYA Institute of Technology RAICHUR

Attendance Report

Two day faculty development program on "Applications of SCILAB in DSP and Image Processing"

Sl.No	Name of the participant	12/8/2016	13/8/2016
14	Sara.		
15	geetha		
16	Sudhakar		
17	Jyothi		
18	Manjula DC		
19	Gangadhar Rao		
20	Sangameh .H		
21	Praveen .S		
22	Manjunath		
23	Mallikarjun		
24	Dr. Kumar Anjan		
25	Dr. S. Sathees		
26	Dr. Imanuel		
27	Kailash Patil		

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
Raichur-584 103

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned with CamScanner

Faculty Development Program Evaluation Form

We would appreciate if you could take a few minutes to share your opinion with us so we can serve you better

FDP title: **Applications of SCILAB in DSP and Image Processing**

Date: **August 12th & 13th 2016**

Resource person: **Prof.K.Ganesan, Prof.R.Senthil Kumar**

		Strongly agree	Agree	Neutral	Disagree	Strongly Disagree
1.	The content was as described in publicity materials	(1)	2	3	4	5
2.	The FDP was applicable to my research	1	(2)	3	4	5
3.	I will recommend this FDP to other conservators	1	(2)	3	4	5
4.	The program was well placed within the allotted time	1	2	(3)	4	5
5.	The resourse person is good communicator	1	(2)	3	4	5
6.	The material was presented in an organized manner	(1)	2	3	4	5
7.	The instructor was knowledgeable on the topic	1	(2)	3	4	5
8.	I would be interested in attending a follow-up, more advanced FDP on this same subject	1	(2)	3	4	5

9. Given the topic, was this FDP ☐ a. Too short ☑ b. Right length ☐ c. Too long

10. In your opinion, was this FDP ☐ a. introductory ☐ b. Intermediate ☑ c. Advanced

11. What did you most appreciate/enjoy/think was best about the course? Any suggestions for improvement?

Well Organised, Executed in Excellent Manner

Thank You!

Please return this form to the coordinator at the end of workshop.

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Principal 12/8

Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
Raichur-584 103

Scanned with CamScanner

NAVODAYA
Institute of Technology
RAICHUR

NET's

NAVODAYA INSTITUTE OF TECHNOLOGY

Navodaya Nagar, Bijiengera Road, RAICHUR - 584103.

Certificate

This is to certify Mr./Ms./Dr. Gauravi Shetty

of NIT, Raichur *has*

participated and successfully completed Two Days Faculty Development programme on

"Applications of SCILAB in DSP & Image processing" *organized by the*

Department of Electronics & Communication Engineering,

from ***12th to 13th Aug. 2016.***

Coordinator

HOD 12/8/2016

Dean /Principal

Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Scanned with CamScanner

NEET's

NAVODAYA INSTITUTE OF TECHNOLOGY

Navodaya Nagar, Bijiengera Road, RAICHUR - 584103.

Certificate

This is to certify Mr./Ms./Dr. Sachin Kumar *has*

of NIT, Raichur

participated and successfully completed Two Days Faculty Development programme on

"Applications of SCILAB in DSP & Image processing" *organized by the*

Department of Electronics & Communication Engineering,

from ***12th to 13th Aug. 2016.***

Coordinator

HOD 13/8/2016

Dean / Principal

Navodaya Institute of Technology
RAICHUR-584 103

Scanned with CamScanner

NET's

NAVODAYA INSTITUTE OF TECHNOLOGY

Navodaya Nagar, Bijiengera Road, RAICHUR - 584103.

Certificate

This is to certify Mr./Ms./Dr. E. Channaveeraiah

of NIT, Raichur *has*

participated and successfully completed Two Days Faculty Development programme on

"Applications of SCILAB in DSP & Image processing" *organized by the*

Department of Electronics & Communication Engineering,

from ***12th to 13th Aug. 2016.***

E. Channaveeraiah
Coordinator

13/8/16
HOD

Dean /Principal

PRINCIPAL 13/8/16
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned with CamScanner

Scanned with CamScanner

NET's

NAVODAYA INSTITUTE OF TECHNOLOGY, RAICHUR

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

SUMMARY OF FACULTY DEVELOPMENT PROGRAMME

ON

Applications *of SCILAB in DSP and Image Processing*

Department of ECE organized faculty development programme on "**Applications *of SCILAB in DSP and Image Processing***" was held at Navodaya Institute of Technology, seminar hall on August 12th & 13th 2016. The session was inaugurated with prayer song followed by the welcome speech by the Head of the department, Dr. K M. Palaniswamy.

Principal presided over the function. **Prof. K. Ganesan,** Associate Professor, SRS Institute of Technology, Salem and **Prof. R. Senthil Kumar,** Associate Professor, IRTT, Erode, Tamilnadu were the resource persons.

The first day activity were as follows

Why SCILAB? Pros & Cons, Software Architecture, Variables and data types, Basic Matrix operations indexing using built in functions, Loops- for, while, do; Conditions- If else, select case, Basic input-output functions, How to create user defined functions Passing and returning multiple arguments.

The Second day activity were follows

Simple plots – 1D, 2D, 3D; Architecture Matrix operation, Flow control, Introduction to MATLAB; Functions, Structures, cells, Plots; Interfacing MATLAB and SCILAB: M to S conversion.



Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned with CamScanner

Date: 30.12.2015

From

Dr.K.M. Palaniswamy,
Professor and Head,
Navodaya Institute of Technology,
Raichur-584103

To

The Principal,
Navodaya Institute of Technology,
Raichur-584103

Sir,

Subject: Permission for conducting seven days FDP from 07.01.2016 to 14.01.2016-Reg

From ECE dept., we are going to conduct FDP on "**Artificial Neural Network using MATLAB**".

The FDP is to be carried out for seven days from 07.01.2016 to 14.01.2016. So please provide permission to conduct the above said FDP. The proposed expense is attached in next page. The expected expense is Rs 50, 000/- whose details are provided in next page.

Thanking you

The faculty Coordinators are
1. Prof. Sachin Kumar.
2. Prof. Md Shaoibuddin Madni

Yours Faithfully

30/12/15

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

APPROVED 30/12/15

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

PRINCIPAL 13/1/2016
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned by CamScanner

NET's
NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

Faculty Development Programme - PROPOSAL

Date: 28.12.2015

1	**ACADEMIC YEAR**	**2015-16**
2	**TYPE OF THE PROGRAMME PROPOSED**	***Faculty Development Programme***
3	**TITLE OF THE PROGRAMME:**	**"Artificial Neural Network using MATLAB"**
4	**BROAD OBJECTIVE OF THE PROGRAMME:**	This Program aims to give hands on training on Artificial Neural Network using MATLAB. **Major Course Contents:** • Introduction of Neural Network and its modeling. Different models of Artificial Neural Network, topologies and basic learning laws. • Hebbian supervised and unsupervised learning for pattern association. Perception learning rule for pattern classification. • Exposure with MATLAB environment, its common functions. Programming for modeling of neural networks, Perception learning and Hebbian learning • Work with neural network toolbox for Perception model. • Feed forward Neural Network architecture and generalize pattern classification. • Pattern mapping, pattern mapping network, function approximation and Back propagation learning rule. Discussion of Back propagation learning rule and its characteristics with its limitations and enchantments • Implementation for feed forward neural network in MATLAB environment through command window and programming using in built functions for pattern classification, pattern mapping, generalization, approximation and predication. • Implementation for feed forward neural network in MATLAB environment through Neural network toolbox using in built functions for pattern classification, pattern mapping, generalization, approximation and predication.

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

PRINCIPAL 13/8/2015
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned by CamScanner

5	NAME OF THE INSTITUTION	NETs NAVODAYA INSTITUTE OF TECHNOLOGY
6	NAME OF THE DEPARTMENT(S)	ELECTRONICS AND COMMINICATION ENGINEERING
7	BENEFICIARY	Faculty Members
8	DATE/DURATION OF SUCH PROGRAMMES CONDUCTED IF ANY IN THIS ACADEMIC YEAR:	07.01.2016 to 14.01.2016
9	ELIGIBILITY CRITERIA SET:	Faculties from Electronics and Communication Engineering.
10	NUMBER OF DAYS REQUIRED TO ORGANISE THE PROGRAMME	Seven Day FDP
11	PROPOSED DURATION OF THE PROGRAMME	42 hours
12	TOTAL NO. OF PERIODS REQUIRED FOR THE PROGRAMME	42 Periods
13	NO. OF PERIODS REQUIRED PER DAY	06 Periods
14	LOCATION DETAILS:	NETs NIT Auditorium
15	MINIMUM NO OF PARTICIPANTS:	30
16	DETAILS OF SPONSORERS:	-
17	DETAILS OF RESOURCE PERSONS:	Dr. M. Srinivasan: He has done Ph.D. in Power systems Anna University, Chennai, Tamilnadu. M.E.in Electrical Machines at PSG College of Technology, Coimbatore Bharathiar University. B.E.in Electrical and Electronics Engineering Government College of Technology, Coimbatore Bharathiar University. Dr. M. Srinivasan is Professor at VCET Erode, Tamilnadu
18	REGISTRATION FEE WITH CERTIFICATION:	Rs 1200 per participant
19	REGISTRATION FEE WITHOUT CERTIFICATION:	Rs - per participant.
20	FACULTY COORDINATOR:	1. Sachin Kumar 2. Md. Shoaibuddin Madni

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned by CamScanner

22	**PROGRAMME PROPOSED BY:**	Department of ECE
23	**ANY OTHER RELEVANT MATTER:**	Nil
24	**ESTIMATED BUDGET IN Rs:**	Rs.33,000

This is to the Honorable Chairman of NIT for kind approval:

PRINCIPAL

HOD
Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

EVENT COORDINATOR

APPROVED/~~NOTAPPROVED~~

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned by CamScanner

Registration Form

(photocopy may be used if required)

Name (in BLOCK letters): _____

_____

Academic Qualification: _____

Experience: _____

Designation & Department: _____

College Address: _____

_____

_____

Name of Affiliated University: _____

E mail : _____

Contact Number: _____

DD Number/ Date: _____

Declaration:

The information furnished above is true to the best of my knowledge.

Date:

Place: Signature of the Applicant

Mr./Ms./Dr. _____

_____ is an employee (faculty)/PG student of our institute. He / She will be permitted to attend the programme if selected.

Date:

Place:

Signature & Seal
HOD/Head of the Institution
Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT),
RAICHUR-584 103

Chief Patron
Sri. S. B. Reddy, Hon'ble Chairman
Navodaya Education Trust (NETR), Raichur

Patron
Sri. N. Bharath Reddy, Trustee
Navodaya Education Trust (NETR), Raichur.

Chief Convener
Dr Shivaprakash C. K., Dean/Principal, NIT, Raichur

Convener
Dr. K M Pulavissarry, HOD,ECE, NITR

Coordinator
Prof. Sachin Kumar M
Assistant Professor, Dept of ECE, NITR
Email:sachin.kumar@navodaya.edu.in
Msachin834@gmail.com
Contact no. : +91- 8861235444

Co-Coordinator
Prof. Md Shoaibuddin Makki
Assistant Professor Dept of ECE, NIT
Email: shoaibuddin.makki@navodaya.edu.in
shoaib.makki7@gmail.com
Contact no. : +91- 8792374743

Organizing Committee

1. Prof. Ramamurthy D
2. Prof. Channaveeramma E
3. Prof. Priyadarshini N
4. Prof. Sachin kumar
5. Prof. LaxmikanthReddi
6. Prof. Archana Patil
7. Prof. Rashma A

NAVODAYA
Institute of Technology
RAICHUR



Seven Days Faculty Development Programme On Artificial Neural Network using MATLAB

January 07th – 14th 2016

ORGANIZED BY
Department of Electronics & Communication Engineering.

Navodaya Education Trust®
NAVODAYA INSTITUTE OF TECHNOLOGY
Navodaya Nagar, Bijnagera Road,
Raichur-584103, Karnataka, India.

PRINCIPAL 15/1/2016
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned by CamScanner

About the Institute:

Navodaya Institute of Technology(NIT), Raichur, established in 2008, is affiliated to Visvesvaraya Technological University (Belagavi). The institute is approved by All India Council for Technical Education (AICTE), New Delhi.

The guiding spirit and the driving force for all the progress and achievements of the Institution is our Honourable chairman of Navodaya Education Trust (NET®), Sri S. R. Reddy. NIT focuses on promoting quality technical education in north Karnataka region and Hyderabad-Karnataka region in particular.

The education is of paramount importance for socio-economic development. The institute focuses on integrated scientific training with value based education, to enable overall development of the aspirants.

NIT is all-inclusive campus, with Administrative block, Library block, independent Academic blocks for each department, Separate Hostels for Boys and Girls, Canteen, Bank(SBI), in-campus ATM , Temple, Gym and Playground, all housed in a vast campus of more than 30 acres land surrounded by hillocks and lake.

About the Department:

The Electronics and Communication Engineering department aims in delivering excellence in theoretical and experimental value based education to students. The Department was started in the year 2008 with an intake of 60 students with the vision to provide the quality technical education. The department has good infrastructure with well-equipped laboratories and necessary softwares.

The Department has bagged many ranks in GATE. Many students were placed in various prestigious MNC's and pursuing their higher studies in NIT's and IIT's.

The Department also offers Postgraduate programme in Digital Communication and Networking (DCN), was started in the academic year 2014-15.The department has highly qualified, experienced and dedicated faculties with PhD and Post graduation (M.E. / M.Tech.) degrees. The workshops and Training programmes are planned regularly for the benefit of students and faculty members.

Vision: To excel in education and research in Electronics & Communication Engineering, thereby providing service to the society and human kind.

Mission: To create competent professionals by training the students in the design & implementation of Electronics and Communication Engineering products helping there by students to develop the skills to solve technological problems of modern society.

About the Resource Person:

Dr. M. Srinivasan:
He has done Ph.D. in Power systems Anna University, Chennai, Tamilnadu.
M.E.in Electrical Machines at PSG College of Technology, Coimbatore Bharathiar University.
B.E.in Electrical and Electronics Engineering Government College of Technology, Coimbatore Bharathiar University.

Dr. M. Srinivasan is Professor at VCET Erode, Tamilnadu.

About the FDP:
This FDP aims to give hands on training on Artificial Neural Networks using MATLAB.

Major Course Contents:

- Introduction of Neural Network and its modeling.Different models of Artificial Neural Network, topologies and basic learning laws.
- Hebbian supervised and unsupervised learning for pattern association.Perceptron learning rule for pattern classification.
- Exposure with MATLAB environment, its common functions.Programming for modeling of neural networks, Perceptron learning and Hebbian learning.
- Work with neural network toolbox for Perceptron model.
- Feed forward Neural Network architecture and generalize pattern classification.
- Pattern mapping, pattern mapping network, function approximation and Backpropagation learning rule.Discussion of Backpropagation learning rule and its characteristics with its limitations and enchantments.

RESOURCE PERSONS:

1. Dr. M. Srinivasan, Professor at VCET Erode, Tamilnadu.

Important dates

Workshop date : January 7th – 14th 2016
Last date for registration : 5th Jan 2016.
Registration fees : Rs. 1200 /-

Registration fee may be paid in the form of DD in favour of **Principal, Navodaya Institute of Technology**, payable at **SBI, NET campus, Raichur.**
VENUE : NIT Auditorium, NIT, Raichur

Note:
1. Accommodation will be provided on prior request.
1. Lunch, Tea will be provided during the training in the campus.

Scanned by CamScanner

NET's
NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

Tel : +91 8532-200115, 240334, Fax : +91-8532-240054
E-Mail :hodece.nit@navodaya.edu.in

CIRCULAR

Cir.No:2015-16/FDP/TF/01

Chief Patron
Shri. S. R. Reddy, Hon'ble Chairman,
Navodaya Education Trust (NET®),
Raichur.

Patron
Shri. N. Bharath Reddy., Hon'ble Trustee,
Navodaya Education Trust (NET®),
Raichur.

Chief Convener
Dr.ShivaPrakash C K,
Principal and Dean, NIT, Raichur

Convener
Dr. K M Palaniswamy,
HOD/ ECE, NIT. Raichur

Coordinator
Coordinator
Prof. Sachin Kumar M
Assistant Professor, Dept of ECE, NITR.
Email:sachin.kumar @navodaya.edu.in
Msachin834@gmail.com
Contact no. : +91- 8861235444

Co-Coordinator
Prof. Md Shoaibuddin Madni
Assistant Professor Dept of ECE, NIT
Email: shoaibuddin.madni@navodaya.edu
shoaib.madni7@gmail.com
Contact no. : +91- 8792374743

Organizing Committee

1. Prof. Rammurthy D
2. Prof. Channaveeramma.E
3. Prof. [illegible]shini N
4. Prof. Sachin Kumar
5. Prof. Laxmikant Reddi
6. Prof. Archana Patil
7. Prof. Reshma A

01.01.2016

Respected Sir / Madam,
Greetings from Navodaya Institute of Technology, Raichur...

The department of Electrical & Electronics Engineering is organizing a one week FDP on **"Artificial Neural Network using MATLAB"** The attached brochure may kindly be circulated among the students and faculty members / colleagues.

At various stages in engineering study, one comes across mathematical problems of engineering applications irrespective of the engineering discipline. A simple computational tool such as MATLAB can be quite handy to solve such mathematical problems, provided we can formulate the problems appropriately. This FDP aims to give hands on training in using Artificial Neural Network MATLAB.

We are pleased to inform that the department of Electronics and Communication Engineering, Navodaya Institute of Technology is organizing a Faculty Development Programme during January 7th - 14th 2016

In this context, we look forward your support and request you to encourage participation of professionals by circulating the attached brochure.

Thanking you,
Yours sincerely,

Dr. K M Palaniswamy,
Convener

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned by CamScanner

NET's
NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Ph. No.:08532-220330, Extn-703,702,713, 07353295425
e-mail:hodece.nit@navodaya.edu.in

Chief Patron
Shri. S. R. Reddy, Hon'ble Chairman,
Navodaya Education Trust (NET®),
Raichur.

Patron
Shri. N. Bharath Reddy., Hon'ble Trustee.
Navodaya Education Trust (NET®),
Raichur.

Chief Convener
Dr.Shivaprakash C. K.,
Principal and Dean, NIT, Raichur

Convener
Dr. K M Palaniswamy,
HOD/ ECE, NIT. Raichur

Coordinator
Prof. Sachin Kumar M
Assistant Professor, Dept of ECE, NITR.
Email:sachin.kumar @navodaya.edu.in
Msachin834@gmail.com
Contact no. : +91- 8861235444

Co-Coordinator
Prof. Md Shoaibuddin Madni
Assistant Professor Dept of ECE, NIT
Email:
shoaibuddin.madni@navodaya.edu.in
shoaib.madni7@gmail.com
Contact no. : +91- 8792374743

Organizing Committee

1. Prof. Rammurthy D
2. Prof. Channaveeramma.E
3. Prof. Priyadarshini N
4. Prof. Sachin kumar
5. Prof. LaxmikanthReddi
6. Prof. Archana Patil
7. Prof. Reshma A

01.01.2016

Faculty Development Programme
on
"Artificial Neural Network using MATLAB"

To

The Principal,

Warm Greeting!!!

Kind attention: Head of the Department / ECE,

Dear Sir/Madam,

Sub: Invitation for attending faculty development programme on ***"Artificial Neural Network using MATLAB"*** reg.

We are pleased to inform that the Department of Electronics & Communication Engineering, Navodaya Institute of Technology is organizing a Faculty Development Programme on ***"Artificial Neural Network using MATLAB"*** during January 7th to 14th 2016, (7 days) at our college premises. This programme will equip the faculty to teach the subject in better way and also enable them to offer final year project in this specialization. We invite your faculty members to participate in this FDP from your esteemed institution.

Please see the brochure enclosed with this for your perusal and circulation among the faculty members of EEE/ECE/EIE department.

For further details please feel free to contact.

Thanks & Regards

Dr. K M Palaniswamy
Head
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned by CamScanner

NAVODAYA INSTITUTE OF TECHNOLOGY

BIJENGERA ROAD, RAICHUR - 584103

INVITATION

Department of Electronics & Communication Engineering

HODs & Faculties are invited

to attend Seven days

FACULTY DEVELOPMENT PROGRAMME

on

"Artificial Neural Network Using MATLAB"

From January 07th to 14th 2016

Venue: NIT, Auditorium

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Head of Department

Scanned by CamScanner

Schedule for Faculty Development Program (FDP) on **"Artificial Neural Network using MATLAB"**

Organised By

Navodaya Institute of Technology: Raichur (07.01.2016 to 14.01.2016)

Timing of Each Day Session

Date/Day	Session I 9:30AM-11:00AM		Session II 11:30AM-1:00PM		Session III 2:00PM-5:00PM
07.01.2016 Thursday	• Introduction of Neural Network and its modeling. • Different models of Artificial Neural Network, topologies and basic learning laws.	TEA BREAK	• Different models of Artificial Neural Network, topologies and basic learning laws.	LUNCH BREAK	• Hebbian supervised and unsupervised learning for pattern association • Perceptron learning rule for pattern classification.
08.01.2016 Friday	• Practical with MATLAB and hands on practice with MATLAB		• Exposure with MATLAB environment, its common functions		• Programming for modeling of neural networks, Perception learning and Hebbian learning.
09.01.2016 Saturday	• Work with neural network toolbox for Perception model		• Work with neural network toolbox for Perception model		• Work with neural network toolbox for Perception model
10.01.2016 Sunday	**(Theoretical Concepts, mathematical formulation and working with MATLAB)** • Feed forward Neural Network architecture and generalize pattern classification		• Pattern mapping, pattern mapping network, function approximation and Back propagation learning rule.		• Discussion of Back propagation learning rule and its characteristics with its limitations and enchantments.
11.01.2016 Monday	• Implementation for feed forward neural network in MATLAB environment through command window and programming using in built functions for pattern classification, pattern		• Classification, pattern mapping, generalization, approximation and predication.		• Implementation for feed forward neural network in MATLAB environment through Neural network toolbox using in built functions for pattern

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned by CamScanner

	mapping, generalization, approximation and predication.				classification, pattern mapping, generalization, approximation and predication.
12.01.2016 Tuesday	• Feedback neural network, associative memory, Hopfield neural network & its energy functions analysis.		• Hopfield neural network & its energy functions analysis.		• Pattern storage and recalling in Hopfield neural networks, concept of stability, global minimum and false minima problem.
13.01.2016 Wednesday	• MATLAB implementation for Hopfield model through programming and command window.		• Implementation of competitive learning in MATLAB for pattern clustering.		• Implementation of competitive learning in MATLAB for pattern clustering.

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY • RAICHUR

PRINCIPAL 13/1/2016
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned by CamScanner

Date: 04.01.2016

From

Dr. K.M. Palaniswamy,
Professor and Head,
Navodaya Institute of Technology,
Raichur-584103

To

The Principal,
Navodaya Institute of Technology,
Raichur-584103

Sir

Subject: Seminar Hall for FDP on "***Artificial Neural Network using MATLAB***" from Jan 7th to 14th 2016.

We have proposed to conduct FDP on "***Artificial Neural Network using MATLAB***" from Jan 7th to 14th 2016, Time-9.30Am-5.30Pm. For this Purpose, We need Seminar Hall on Jan 7th to 14th 2016, Time-9.30Am-5.30Pm.

Thanking You

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

To Mr. Anil

Yours faithfully

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned by CamScanner

04.01.2016

From

Dr. K M Palaniswamy,

HOD/ ECE,

NIT, Raichur

To

The Dean cum Principal,

NIT, Raichur.

Sir,

Sub: Requisition for Accommodation and Food -Reg

I bring to your kind notice that we are conducting the Seven days FDP on "**Artificial Neural Network using MATLAB**" in the department ECE from January 7th to 14th 2016. For that, we need to arrange the accommodation and food for the Resource Person in the above said days. So kindly do the needful.

Resource Person Details,

Dr. M. Srinivasan is Professor at VCET Erode, Tamilnadu

Thanking you,

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

To Mr. Ravi for n-a

HOD/ECE
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned by CamScanner

NAVODAYA INSTITUTE OF TECHNOLOGY, RAICHUR

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

SUMMARY OF FACULTY DEVELOPMENT PROGRAMME ON

"Artificial Neural Network using MATLAB"

Department of ECE organized faculty development programme on **" Artificial Neural Network using MATLAB "** was held at Navodaya Institute of Technology, seminar hall from 7th January to 14th January 2016. The session was inaugurated with prayer song followed by the welcome speech by the Head of the department, Dr. K M Palaniswamy.

Principal presided over the function. Dr. M. Srinivasan ,Professor at VCET Erode, Tamilnadu was the resource person.

First day: Dr. M. Srinivasan explained the Theoretical concepts and mathematical formulation

- Introduction of Neural Network and its modeling.
- Different models of Artificial Neural Network, topologies and basic learning laws.
- Hebbian supervised and unsupervised learning for pattern association.
- Perceptron learning rule for pattern classification.

Second day: (Practical with MATLAB and hands on practice with MATLAB)

- Exposure with MATLAB environment, its common functions
- Programming for modeling of neural networks, Perception learning and Hebbian learning.

Third day: Work with neural network toolbox for Perception model.

Fourth day: (Theoretical Concepts, mathematical formulation and working with MATLAB)

- Feed forward Neural Network architecture and generalize pattern classification.
- Pattern mapping, pattern mapping network, function approximation and Back propagation learning rule.
- Discussion of Back propagation learning rule and its characteristics with its limitations and enchantments.

Fifth Day:

- Implementation for feed forward neural network in MATLAB environment through command window and programming using in built functions for pattern classification, pattern mapping, generalization, approximation and predication.
- Implementation for feed forward neural network in MATLAB environment through neural network toolbox using in built functions for pattern classification, pattern mapping, generalization, approximation and predication.

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

PRINCIPAL 13/1/16
Navodaya Institute of Technology (NIT),
RAICHUR-584 103

Scanned by CamScanner

Sixth day: (Theoretical Concepts, mathematical formulation and working with MATLAB)

- Feedback neural network, associative memory, Hopfield neural network & its energy functions analysis.
- Pattern storage and recalling in Hopfield neural networks, concept of stability, global minimum and false minima problem.

Seventh day:

- MATLAB implementation for Hopfield model through programming and command window.
- Implementation of competitive learning in MATLAB for pattern clustering.

Totally twenty seven faculty members were participated and benefited.



Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

PRINCIPAL 13/1/2012
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned by CamScanner

NAVODAYA Institute of Technology RAICHUR

Attendance Report

Seven days faculty development program on "Artificial Neural Network using MATLAB"

Sl.No	Name of the participant	7/1/2016	8/1/2016	9/1/2016	11/1/2016	12/1/2016	13/1/2016	14/1/2016
1.	Channaveelamma							
2.	Madhuri Deshpande							
3.	Md Shiraz Siddiqui							
4	Sathishkumar							
5	Priyadarshini Sridhar							
6	Ramamurthy							
7	Smitha. C							
8.	Jamuna Kanth							
9	Madan							
10	Parijatha Reddy							
11	Archana.							
12	Reshma							
13	Daniel							
14	Satish							
15	Geetha.							
16	Sudhakar							
17	Kailash							

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned by CamScanner

NAVODAYA Institute of Technology RAICHUR

Attendance Report

Seven days faculty development program on "Artificial Neural Network using MATLAB"

Sl.No	Name of the participant	7/1/2016	8/1/2016	9/1/2016	11/1/2016	12/1/2016	13/1/2016	14/1/2016
18	Praveen S							
19	Mallikarjun							
20	Shravanyouda							
21	Sharan K							
22	Sivitar.							
23	Amitkumar							
24	Manjula							
25	Jyothi							
26	Shiganeed							
27	Jatihal							
28	Sagar							
29	Dr. Kadam							
30	Madhukar							

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

PRINCIPAL 13/8/2022
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned by CamScanner

Faculty Development Program Evaluation Form

We would appreciate if you could take a few minutes to share your opinion with us so we can serve you better

FDP title: **Artificial Neural Network using MATLAB**

Date: **7th - 14th January 2016**

Resource person: **Dr. M. Srinivasan**

		Strongly agree	Agree	Neutral	Disagree	Strongly Disagree
1.	The content was as described in publicity materials	(1)	2	3	4	5
2.	The FDP was applicable to my research	1	(2)	3	4	5
3.	I will recommend this FDP to other conservators	(1)	2	3	4	5
4.	The program was well placed within the allotted time	1	(2)	3	4	5
5.	The resourse person is good communicator	1	(2)	3	4	5
6.	The material was presented in an organized manner	1	(2)	3	4	5
7.	The instructor was knowledgeable on the topic	(1)	2	3	4	5
8.	I would be interested in attending a follow-up, more advanced FDP on this same subject	1	(2)	3	4	5

9. Given the topic, was this FDP ☐ a. Too short ☑ b. Right length ☐ c. Too long

10. In your opinion, was this FDP ☐ a. introductory ☑ b. Intermediate ☐ c. Advanced

11. What did you most appreciate/enjoy/think was best about the course? Any suggestions for improvement?

Well Organised & Executed

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Thank You!

Please return this form to the coordinator at the end of workshop.

14/1/16

PRINCIPAL 16/1/2016
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Head of Department
Electronics & Communication Engg.
Navodaya Institute of Technology
RAICHUR-584 103

Scanned by CamScanner

NAVODAYA
Institute of Technology
RAICHUR

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Certificate of Participation

Certified that Mr./Ms. Geetha . K.M

Of NIT , Raichur has

successfully participated in One Week Faculty Development Program on *" Artificial Neural Network using MATLAB"* organized by the department of Electronics & Communication Engineering, Navodaya Institute of Technology, Raichur, Karnataka from 7-01-2016 to 14-01-2016.

NAVODAYA INSTITUTE OF TECHNOLOGY • RAICHUR

CO-ORDINATOR

HOD

PRINCIPAL 13/1/2016
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

PRINCIPAL

Scanned by CamScanner

NAVODAYA
Institute of Technology
RAICHUR

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Certificate of Participation

Certified that Mr./Ms. Madhuri Devi C.H

Of Navodaya Institute of Technology has successfully participated in One Week Faculty Development Program on " ***Artificial Neural Network using MATLAB***" organized by the department of Electronics & Communication Engineering, Navodaya Institute of Technology, Raichur, Karnataka from 7-01-2016 to 14-01-2016.

NAVODAYA INSTITUTE OF TECHNOLOGY * RAICHUR

CO-ORDINATOR

HOD

13/1/2016
PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

PRINCIPAL

Scanned by CamScanner

NAVODAYA
Institute of Technology
RAICHUR

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Certificate of Participation

Certified that Mr./Ms. Gauravi Shetty

Of NIT Raichur has

successfully participated in One Week Faculty Development Program on *" Artificial Neural Network using MATLAB"* organized by the department of Electronics & Communication Engineering, Navodaya Institute of Technology, Raichur, Karnataka from 7-01-2016 to 14-01-2016.

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

CO-ORDINATOR

HOD

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

PRINCIPAL

Scanned by CamScanner

NAVODAYA
Institute of Technology
RAICHUR

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Certificate of Participation

Certified that Mr./Ms. Md Shoaibuddin Madni

Of NIT, Raichur has

successfully participated in One Week Faculty Development Program on *" Artificial Neural Network using MATLAB"* organized by the department of Electronics & Communication Engineering, Navodaya Institute of Technology, Raichur, Karnataka from 7-01-2016 to 14-01-2016.

NAVODAYA INSTITUTE OF TECHNOLOGY • RAICHUR

CO-ORDINATOR

HOD

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

PRINCIPAL

Scanned by CamScanner

NAVODAYA
Institute of Technology
RAICHUR

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Certificate of Participation

Certified that Mr./Ms. Kiran Kumar T.H

Of NIT, Raichur has

successfully participated in One Week Faculty Development Program on *" Artificial Neural Network using MATLAB"* organized by the department of Electronics & Communication Engineering, Navodaya Institute of Technology, Raichur, Karnataka from 7-01-2016 to 14-01-2016.

NAVODAYA INSTITUTE OF TECHNOLOGY • RAICHUR

CO-ORDINATOR

HOD

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

PRINCIPAL

Scanned by CamScanner

15.04.2019

From
Dr.M.Srinivasan,
Professor and Head,
Navodaya Institute of Technology,
Raichur-584103.

To
The Principal,
Navodaya Institute of Technology,
Raichur-584103

Sir

Subject: Req. to conductTraining Programs for Non-Teaching Faculty- Reg

We have proposed to conduct the Training programson the topic **"Two-day workshop on "AutoCAD- Electrical"**on 22.04.2019 to 23.04.2019 at Navodaya Institute of Technology, Department of EEE, Raichur from 10.00 AM to 04.00PM.

Thanking You

Yours faithfully

[Dr.M.Srinivasan]

Head of the Department
Department of Electrical and
Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103. Karnataka

Permitted
PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NET's
NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

Tel : +91 8532-200115, 240334, Fax : +91-8532-240054
E-Mail :hodeee.nit@navodaya.edu.in

CIRCULAR

Greetings from Navodaya Institute of Technology, Raichur…

The department of Electrical & Electronics Engineering is organizing a **"Hands on Workshop in AutoCAD"** for Non-Teaching Faculties to upgrade the support staff skills.

The main objective of this workshop is to enhance their skills in AUTOCAD software.

AutoCAD Electrical design software is AutoCAD for electrical controls designers, purpose-built to create and modify electrical control systems. The comprehensive symbols libraries and tools help boost productivity by automating control engineering tasks.

We are pleased to inform that the department of Electrical and Electronics Engineering, Navodaya Institute of Technology is organizing a workshop for Non-Teaching Faculty during **22.04.2019 to 23.04.2019.**

In this context, we look forward your support and request you to encourage participation of support staff's by circulating the attached invitation.

Venue: Department of Electrical and Electronics Engineering
Navodaya Institute of Technology,Raichur.
Timing: 10.00AM to 4.00 PM

Thanking you,
Yours sincerely,

20/4/19
Dr.M.Srinivasan
HOD/EEE

NAVODAYA INSTITUTE OF TECHNOLOGY * RAICHUR

Head of the Department
Department of Electrical and
Electronics Engineering
Navodaya Institute of Technology
RAICHUR-584 103. Karnataka

NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

DEPARTMENT: EEE

ACADEMIC YEAR: 2018-19

DATE: 23/04/2019

S.No	Name of the Staff	Date	
01	SANDEEP KUMAR	22-04-19 to 23-04-19	Sandeep
02	Raghavan	22-04-19 to 23-04-19	Raghavan
03	Mahesh	22-04-19 to 23-04-19	
04	Bharani	22-04-19 to 23-04-19	
05	Channabasava	22-04-19 to 23-04-19	Channa
06	Suresh	22-04-19 to 23-4-19	
07	Shivakumar	22-04-19 to 23-4-19	
08	Vidyavathi	22-04-19 to 23-4-19	

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

23/4/19

HEAD OF THE DEPARTMENT

Navodaya Education Trust ®

Navodaya Institute of Technology

Raichur

A Report

On

Two-day workshop on "AutoCAD- Electrical"

Coordinators:

Prof.Geeta K.M, *Assistant Professor*

Prof. A Velu,*Assistant Professor*

Department of Electrical and Electronics Engineering

Organized by:

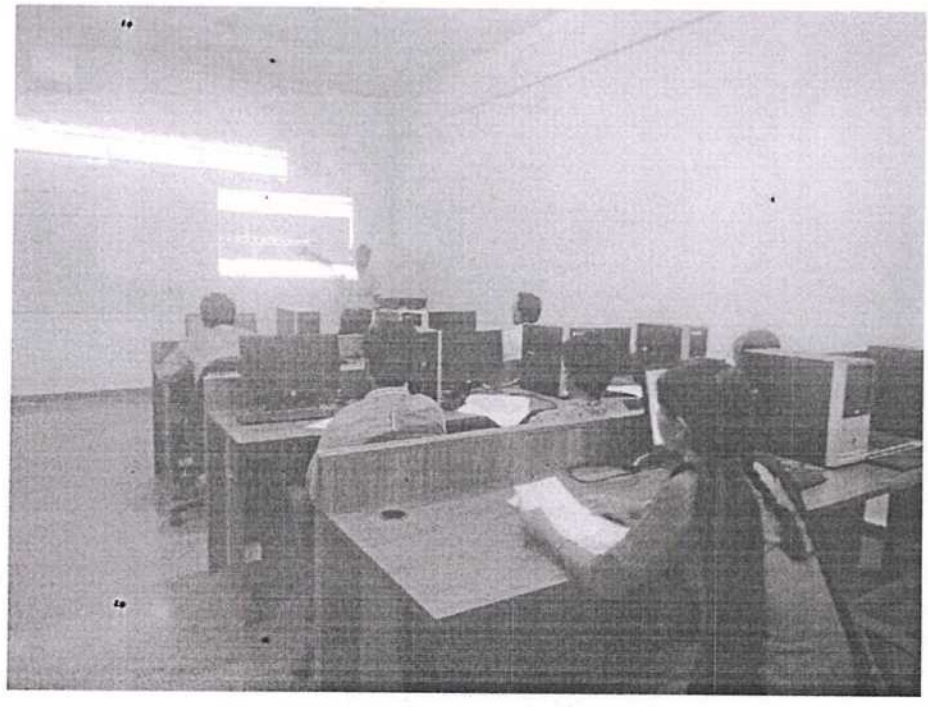
Department of Electrical and Electronics Engineering

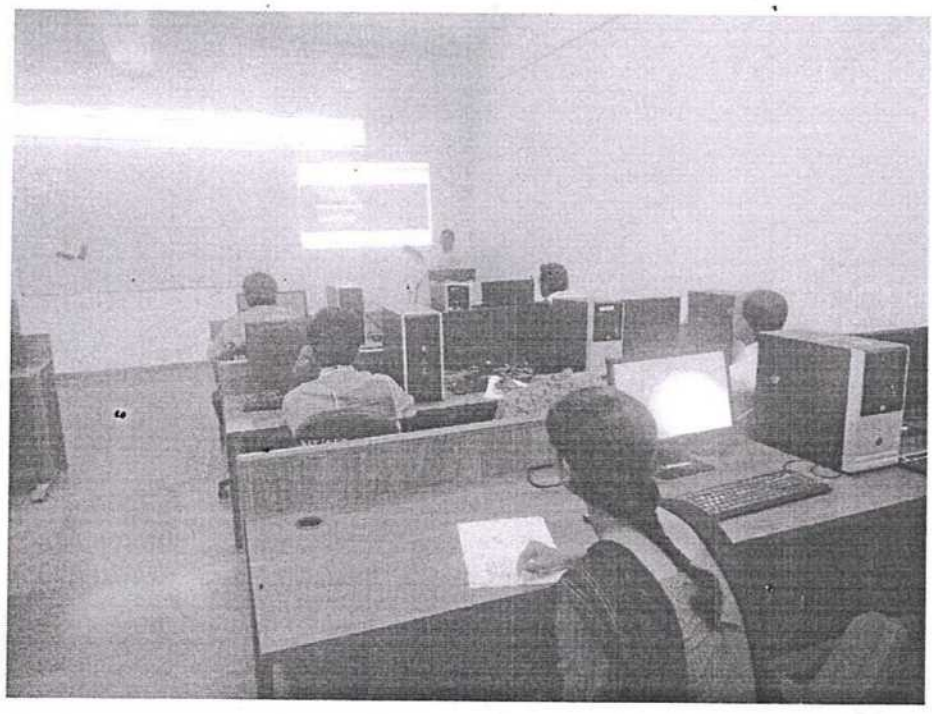
Navodaya Institute of Technology-Raichur

Date: 22 & 23 April, 2019 Time: 10:00am to 04:00pm

INTRODUCTION:

Department of Electrical and Electronics Engineering from arranged Two Days Workshop on AutoCAD –Electrical dated 22 & 23 April 2019. Coordinators-Prof. Geeta K.M, Assistant Professor and Prof. A Velu, Assistant Professorprovidetheir expertise to all Modules of AutoCAD on effective use of subjective knowledge to supporting staffs.





NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

2/2/19

Head of the Department
Department of Electrical and
Electronics Engineering
Navoda... ...titute of Technology,
RAIC... ...584 103. Karnataka

AUTOCAD COMMAND	DISCRIPTION	FURTHER INFORMATION
LINE	Create a line	Basic AutoCAD commands: Mastering the LINE command in AutoCAD
TRIM	Trim object to meet the edges of other objects	Basic AutoCAD commands: Trim and Extend in AutoCAD
CIRCLE	Create a circle	Basic AutoCAD commands: Circles in AutoCAD
EXTEND	Extend object to meet the edges of other objects	Basic AutoCAD commands: Trim and Extend in AutoCAD
ARRAYRECT	Create a rectangular pattern of object	Basic AutoCAD commands: Arrays in AutoCAD
ARRAYPOLAR	Create a circular pattern of object	Basic AutoCAD commands: Arrays in AutoCAD
COPY	Create a copy of an object	Basic AutoCAD commands: Move and Copy objects in AutoCAD MOVE
MOVE	Move an object	Basic AutoCAD commands: Move and Copy objects in AutoCAD ROTATE
ROTATE	Rotate an object	Basic AutoCAD commands: Mirror and Rotate in AutoCAD
MIRROR	Create a mirrored copy of an object	Basic AutoCAD commands: Mirror and Rotate in AutoCAD
ERASE	Delete an object	Basic AutoCAD commands: Explode and Erase in AutoCAD
CHAMFER	Bevels the edges of an object	Basic AutoCAD commands: Chamfer and Fillet
FILLET	Rounds the edges of objects	Basic AutoCAD commands: Chamfer and Fillet

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

Head of the Department
Department of Electrical and Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103. Karnataka

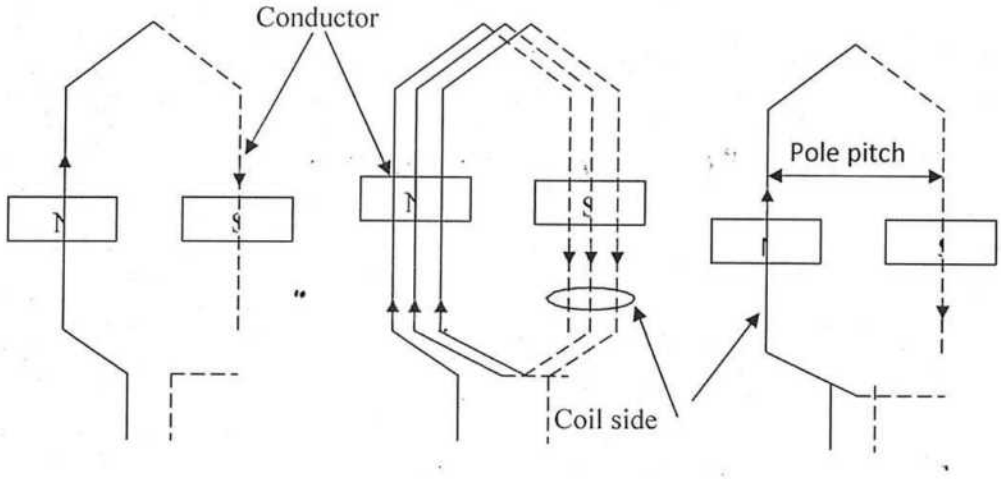
INTRODUCTION TO AUTOCAD:

Winding Diagrams: (i) DC Winding diagrams (ii) AC Winding Diagrams Terminologies used in winding diagrams:

Conductor: An individual piece of wire placed in the slots in the machine in the magnetic field.

Turn: Two conductors connected in series and separated from each other by a pole pitch so that the emf induced will be additive.

Coil: When one or more turns are connected in series and placed in almost similar magnetic positions. Coils may be single turn or multi turn coils.



(a) SingleturnCoil (b) 3 –turncoil (c) Multi turncoil

Fig. 1 Different types of winding coils representations

Coil group: One or more coil single coils formed in a group forms the coil group.

Winding: Number of coils arranged in coil group is said to be a winding.

Pole Pitch: Distance between the poles in terms of slots is called pole pitch.

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Head of the Department
Department of Electrical and
Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103. Karnataka

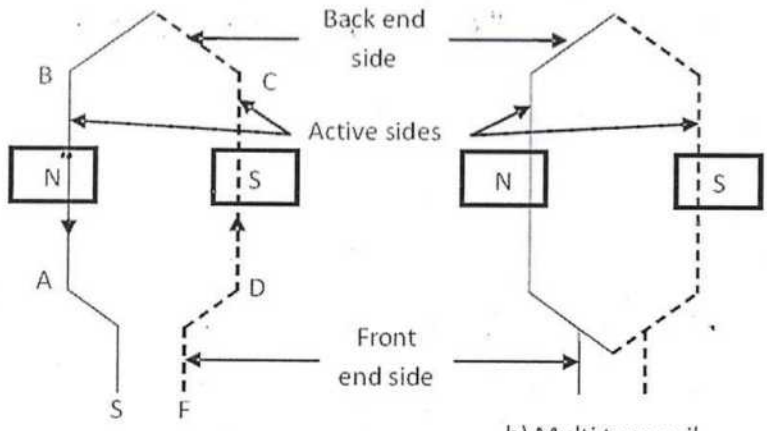


Fig. 2 Single and Multi turn coils

Full Pitch winding: If the coil pitch for a winding is equal to pole pitch the winding is called full pitch winding Fig .

Chorded winding: When the pitch of the winding is less than the full pitch or pole pitch then the winding is called short pitch winding or chorded winding.

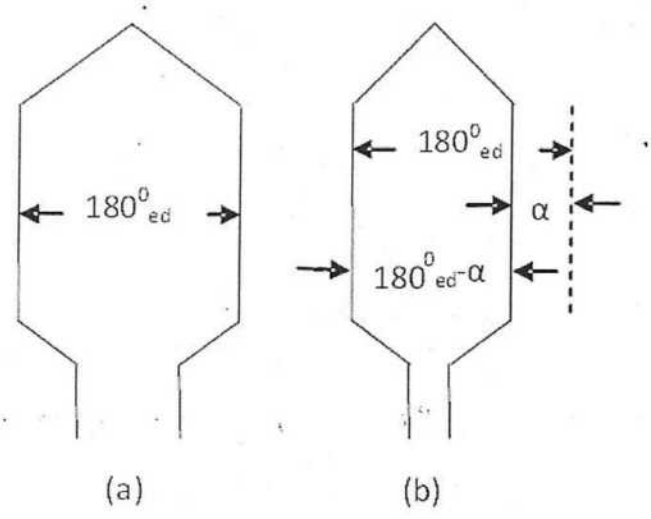


Fig. 3 Full pitched and short pitched coils

Single layer winding: Only one coil side placed in one slot.

Double layer winding: Two coil sides are placed in a single slot. Single and double layer windings are shown in Fig4

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

Head of the Department
Department of Electrical and
Electronics Engineering
Navod... Institute of Tech...ology,
Raichur-58 103. Karnataka

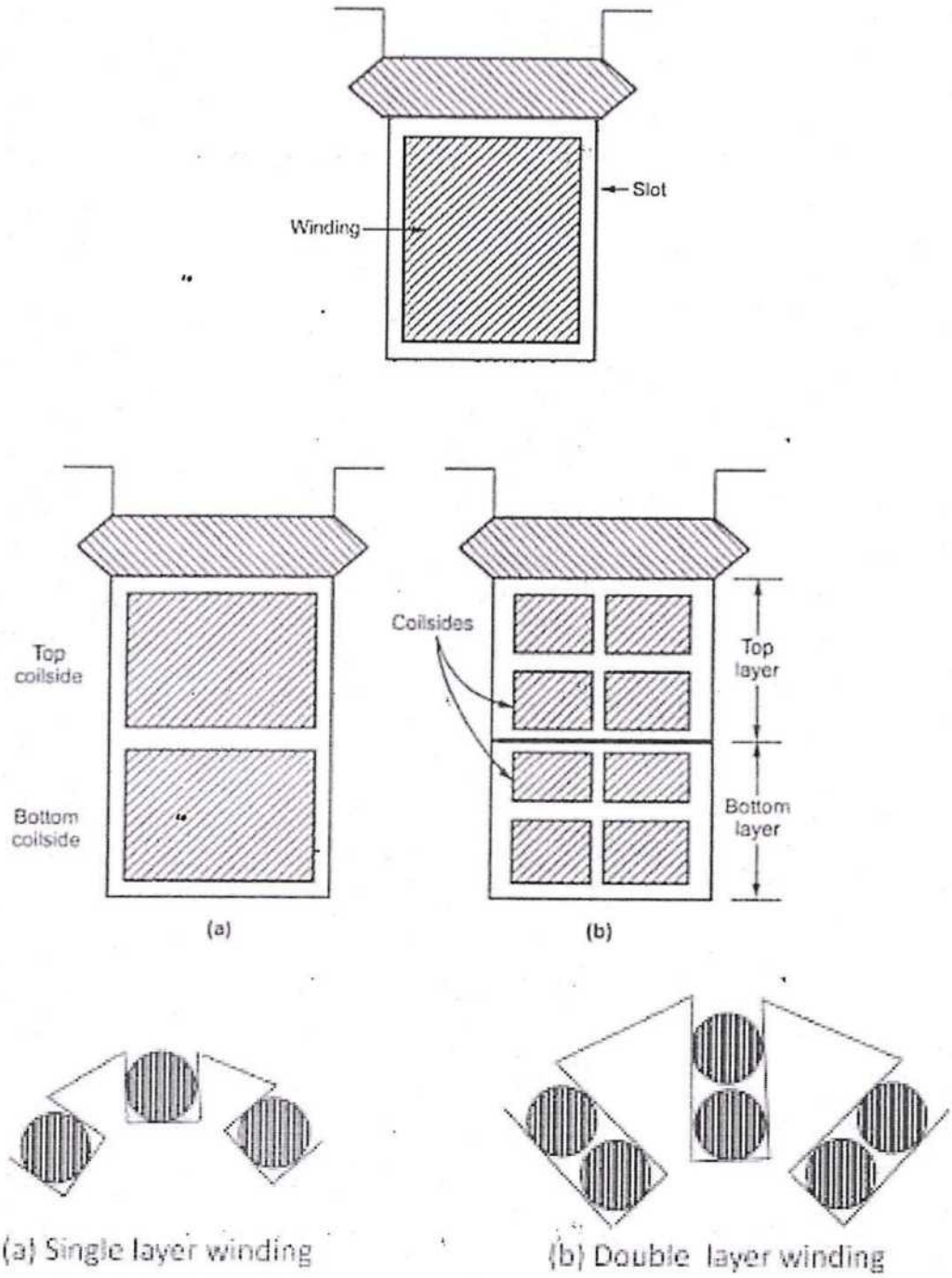


Fig.4 Single and double layer windings

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Head of the Department
Department of Electrical and Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103. Karnataka

Classification of windings: Closed type and open type winding

Closed type windings: In this type of winding there is a closed path around the armature or stator. Starting from any point, the winding path can be followed through all the turns and starting point can be reached. Such windings are used in DCmachines.

Open windings: There is no closed path in the windings. Such windings are used in AC machines.

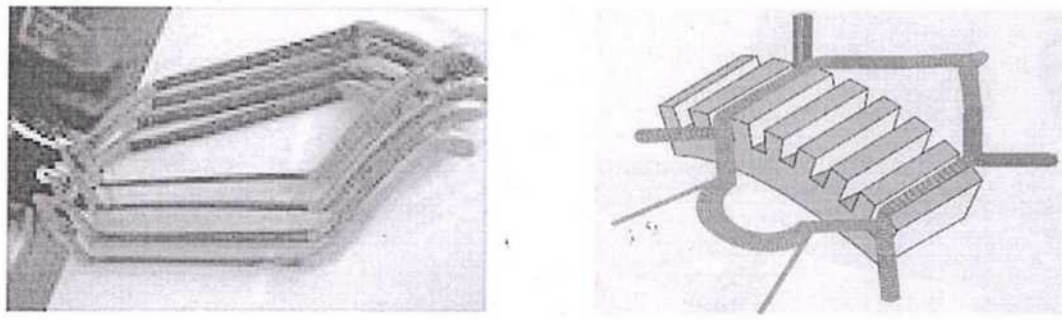


Fig. 5 Photographs of the windings and coils

DC Windings: Two types of windings (a) Lap winding (b) Wave winding
These two types of windings differ in two ways (i) number of circuits between positive and negative brushes, (ii) the manner in which the coil ends are connected. However the coils of both lap and wave windings are identicallyformed.

TYPES AND SHAPES OF WINDING WIRES: The winding wires used in electrical motors are classified as follows. 1) Round wires 2) Rectangular straps 3) Strandedwires

1. Round Wires: It has thin and thick conductors and are used in semi-closed slot type motors and mush winding rotors. It is wounded in reels and available inKilograms.
2. Rectangular straps: It is used in open type slot motors. These conductors are available as long straps in meters. They are used in the following places. 1) Low voltage motor windings. 2) Used as conductor in high current motor. 3) Series field motor windingcoils.

Winding Pitches:

Back Pitch: The distance between top and bottom coil sides of a coil measured around the back of the armature is called back pitch and is designated as y_b. Back pitch is approximately equal to number of coil sides per layer. Generally back pitch is an oddinteger.
Front Pitch: The distance between two coil sides connected to the same commutator segment is called the front pitch and is designated as y_f.
Winding Pitch: The distance between the starts of two consecutive coils measured in terms of coil sides is called winding pitch and is designated as Y.
$Y = y_b - y_f$ for lap winding $Y = y_b + y_f$ for wave winding

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Head of the Department
Department of Electrical and Electronics Engineering
Navodaya Institute of Technology
RAICHUR-584 103, Karnataka

Commutator pitch: The distance between the two commutator segments to which the two ends of a coil are connected is called commutator pitch and is designated as y_c and is measured in terms of commutator segments.

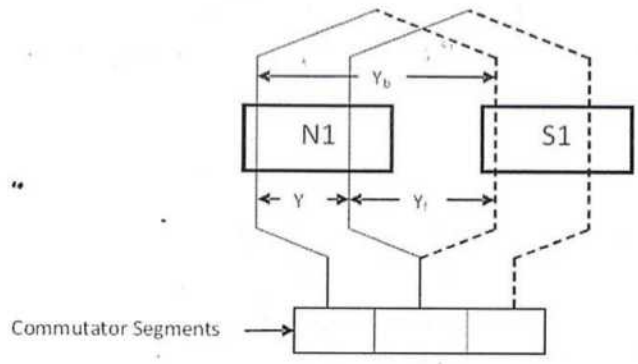


Fig. 6 Lap winding

Lap Winding: The winding in which successive coils overlap each other hence it is called lap winding. In this winding end of one coil is connected to the commutator segment and start of the adjacent coil situated underthesamepoleasshowninfig.6.Lapwindingisfurtherdividedassimplexand Duplexlapwinding.

Simplex lap winding: In this type of winding finish F1 of the coil 1 is connected to the start S2 of coil 2 starting under the same pole as start s1 of coil 1.
We have back pitch $y_b = 2c/p \pm k$ where c = number of coils in the armature, p = number of poles, k = an integer to make y_b an odd integer.

Important rules for Lap winding:
Let Z = Number of conductors
P = number ofpoles
Y_b = Back pitch
Y_f = Front pitch
Y_c = Commutator pitch
Y_a = Average pole pitch
Y_p = Pole pitch
Y_R = Resultant pitch

1. Y_b (Back pitch) and Y_f (Front pitch) must be approximately equal to Y_p (Polepitch)
2. Y_b (Back pitch) must be less or greater than Y_f (Front pitch) by 2m where m is the multiplicity of the winding. When Y_b is greater than Y_f the winding progresses from left to right and is known as progressive winding. . When Y_b is lesser than Y_f the winding progresses from right to left and is known as retrogressive winding. Hence $Y_b = Y_f \pm 2m$.
3. Y_b and Y_f must beodd.
4. Y_b and Y_f may be equal or differ by ±2. + for progressive winding, - for retrogressivewinding
5. $Y_a = (Y_b + Y_f) / 2 = Y_p$
6. Y_R(Resultant pitch) is alwayseven.
7. $Y_c = m$, m = 1 for simplex winding; m = 2 for duplexwinding

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Head of the Department
Department of Electrical and Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103. Karnataka

8. Number of parallel paths = mp = number ofbrushes.

Simplex wave winding: In this type of winding finish F1 of the coil 1 is connected to the start Sx of coil x starting under the same pole as start s1 of coil 1.

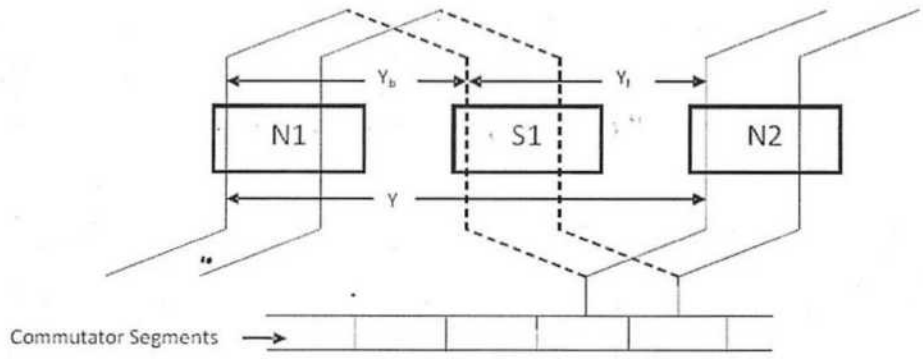


Fig. 7 wave windings

Wave winding: In wave winding the end of one coil is not connected to the beginning of the same coil but is connected to the beginning of another coil of the same polarityas that of the first coil as shown in fig. 7.
Important rules for Wave winding:

1. Y_b (Back pitch) and Y_f (Front pitch) must be approximately equal to Y_p (Polepitch)
2. Y_b and Y_f must beodd.
3. Y_b and Y_f may be equal or differ by ±2. + for progressive winding, - for retrogressivewinding
4. $Y_c = (Y_b + Y_f) / 2$ and should be a wholenumber.

Dummy coils: The wave winding is possible only with particular number of conductors and poles and slots combinations. Some times the standard stampings do not consist of the number of slots according to the design requirements and hence the slots and conductor combination will not produce a mechanically balanced winding. Under such conditions some coils are placed in the slots, not connected to the remaining part of the winding but only for mechanical balance. Such windings are called dummycoils.

Equalizer rings or Equalizer connections in Lap winding: This is the thick copper conductor connecting the equipotential points of lap winding for equalizing the potential of different parallel paths.

Sequence diagram or ring Diagram: The diagram obtained by connecting the conductors together with their respective numbers. This diagram is used for finding the direction of induced emf and the position of brushes.

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Head of the Department
Department of Electrical and Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103. Karnataka

03.01.2020

From
Dr.M.Srinivasan,
Professor and Head,
Navodaya Institute of Technology,
Raichur-584103.

To
The Principal,
Navodaya Institute of Technology,
Raichur-584103

Sir

Subject: Requisition to conduct Training Programs for Non-Teaching Faculty- Reg

We have proposed to conduct the Training programs on the topic **"Hands-on Workshop in MATLAB"** on 06.01.2020 and 08.01.2020 at Navodaya Institute of Technology, Department of EEE, Raichur from 10.00 AM to 05.00 PM.

Thanking You

Yours faithfully

31.12.2019

[Dr. M.Srinivasan]

Head of the Department
Department of Electrical and
Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103. Karnataka

Permitted

31/12/2019

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY

DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

CIRCULAR

04.01.2020

Greetings from Navodaya Institute of Technology, Raichur.

The department of Electrical & Electronics Engineering is organizing a **"Hands-on Workshop in MATLAB"** for Non-Teaching Faculties to upgrade the support staff's skills.

The main objective of this workshop is to enhance the skills in MATLAB software.

A simple computational tool such as MATLAB can be quite handy to solve such mathematical problems, provided we can formulate the problems appropriately.

Also, couple of sessions will be conducted for the use of MATLAB to demonstrate its applications to specific topics such as Power Electronics and Drives, Signal & Image Processing, control systems.

We are pleased to inform that the department of Electrical and Electronics Engineering, Navodaya Institute of Technology is organizing a workshop for Non-Teaching Faculty during **06.01.2020 to 08.01.2020.**

In this context, we look forward your support and request you to encourage participation of support staff's by circulating.

Venue: Department of Electrical and Electronics Engineering
Navodaya Institute of Technology, Raichur.
Timing: 10.00AM to 5.00PM

Thanking you,

Yours sincerely,

Dr.M.Srinivasan
HOD/EEE

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Head of the Department
Department of Electrical and Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103. Karnataka

NAVODAYA INSTITUTE OF TECHNOLOGY

DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

"Hands-on Workshop in MATLAB"

LIST OF PARTICIPANTS

Date: 06.01.2020 to 08.01.2020.

S.No	Name of the Staff	Department	Designation	Signature
1	Ms. BHAVANI	EEE	Instructor	8/1/20
2	Mr. CHANNABASAVA	EEE	Instructor	08/1/20
3	Ms. VIDHYA	ECE	Instructor	
4	Mr. SHIVA KUMAR	ECE	Instructor	

8/1/2020

HEAD OF THE DEPARTMENT

Head of the Department
Department of Electrical and
Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103. Karnataka

NAVODAYA INSTITUTE OF TECHNOLOGY * RAICHUR

Navodaya Education Trust ®
Navodaya Institute of Technology
Raichur

A Report

On

"Hands-on Workshop in MATLAB"

Coordinators:

Dr. M.Srinivasan, Head and Professor

Prof. B.K Mazumdar, Assistant Professor

Prof. Sathish Kumar K.S, Assistant Professor

Department of Electrical and Electronics Engineering

Organized by:

Department of Electrical and Electronics Engineering

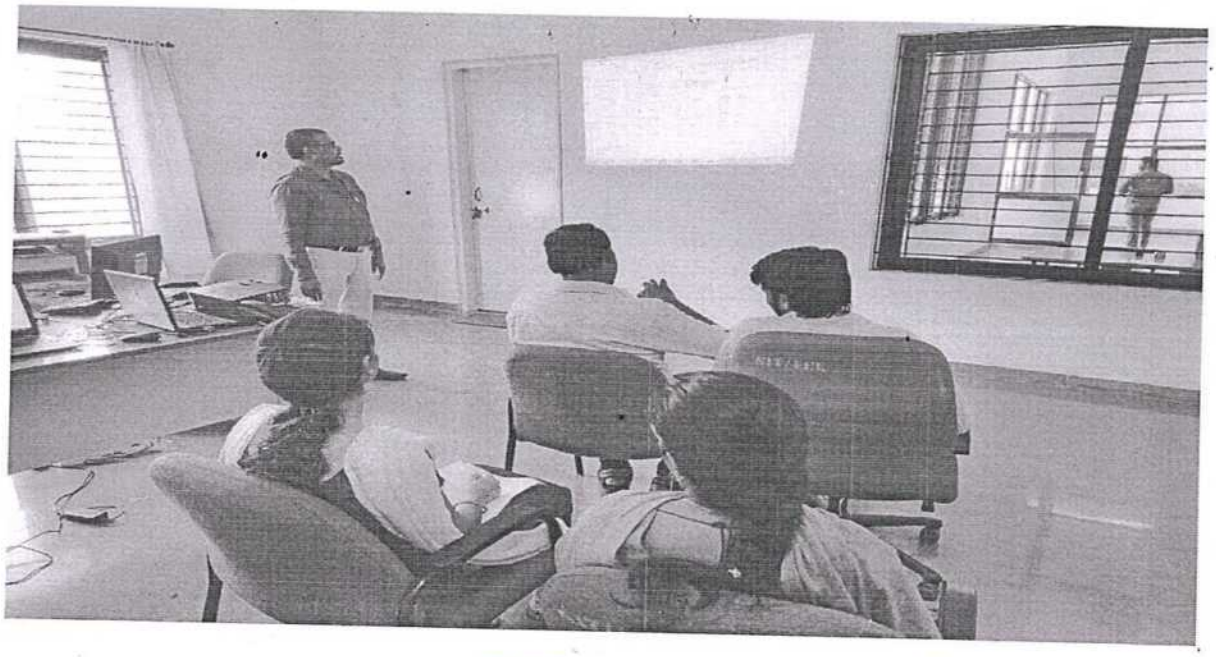
Navodaya Institute of technology-Raichur

Date: 06th January to 8 January, 2020

OBJECTIVE: - The workshop was based on the Basic of MATLAB.

Navodaya Institute of Technology, Raichur Electrical and Electronics Engineering department conducted a workshop on MATLAB. The workshop was based on the Basics of MATLAB to upgrade the knowledge of supporting staffs in the department. Coordinators- Dr. M.Srinivasan, Head and Professor, Mr. B.K Mazumdar, Assistant Professor, Mr. Sathish kumar K.S, Assistant Professor demonstrated with various programs related to Circuit Analysis, Control System , Digital Signal processing and Power system.

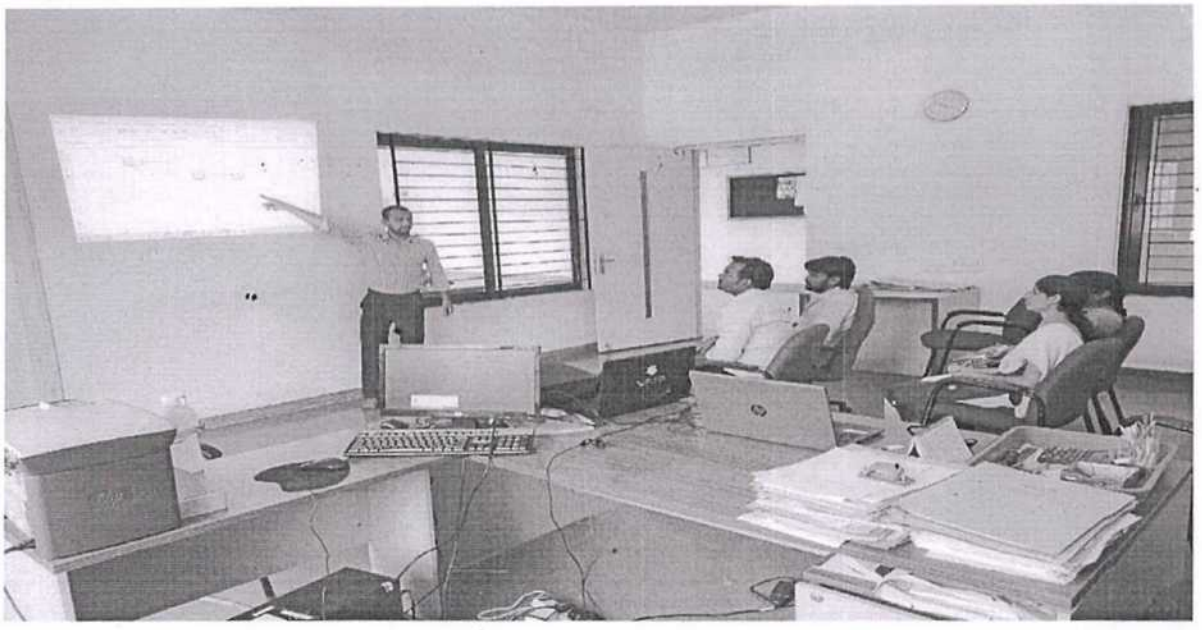




NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

8/1/2020

Head of the Department
Department of Electrical and
Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103. Karnataka



Module 1: Introduction to MATLAB --MATLAB SimPowerSystems --Simulation of Rectifier --Usage of MATLAB Demos	Dr. M.Srinivasan, *Professor and Head*
Module 2: Functions --Starting Simulink --Model Files -- Basic Elements --Running Simulations --Building Systems	Mr. B.K Mazumdar, *Assistant Professor*
Module 3: Signal Processing in MATLAB --Basic Commands and syntax --Identifiers --Arrays and Matrices --Referencing Elements --Matrix Operations --Array Operations	Mr. Sathish kumar K.S, *Assistant Professor*

31/8/2020

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

Head of the Department
Department of Electrical and
Electronics Engineering
Navodaya Institute of Technology,
RAICHUR-584 103. Karnata

20.07.2017

From,
Dr. K M Palaniswamy,
Professor and Head,
Department of Electronics and Communication Engineering,
Navodaya Institute of Technology,
Raichur-584103

To,
The Principal,
Navodaya Institute of Technology,
Raichur-584103

Sir

Subject: Requisition to conduct Training Programs for Non-Teaching Faculty- Reg

We are planning to organize the two day training program for Non-Teaching staff on "**A course on Repair and Maintenance of Equipments"** on 27/07/2017 & 28/07/2017 in the department of ECE. I assigned **Mr. Md Shoaibuddin Madni,** Assistant Professor/ECE to conduct this program to the non teaching faculties.

So, I request you to give permission to conduct training program.

Approved 20/7/17

Thanking You,

NAVODAYA INSTITUTE OF TECHNOLOGY * RAICHUR

Yours faithfully,

20/7/17

[Dr. K. M Palaniswamy]

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

PRINCIPAL 13/7/2017
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned by CamScanner

NET's
NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

Tel : +91 8532-240115, 240334, Fax : +91-8532-240054
E-Mail :hodece.nit@navodaya.edu.in

CIRCULAR

Cir.No:2017-18/FDP/NTF/01

Respected Sir / Madam,

Greetings from Navodaya Institute of Technology, Raichur…

The Department of Electronics & Communication Engineering is organizing a **"A Development course on repair and maintenance of equipments"** for Non-Teaching Faculties to upgrade the support staff's skills.

The main objective of this workshop is to enhance knowledge and skill in maintenance/troubleshooting/servicing/repair of electronic measuring instruments.

A couple of sessions will be conducted to demonstrate working, programming and simulation of all the softwares in electronics and communication laboratories.

We are pleased to inform that the Department of Electronics & Communication Engineering, Navodaya Institute of Technology is organizing a workshop for Non-Teaching Faculty during **27/07/2017 & 28/07/2017.**

In this context, we look forward your support and request you to encourage participation of support staff's by circulating the attached invitation.

Venue: Department of Electronics & Communication Engineering

Navodaya Institute of Technology, Raichur.

Timing: 9.00AM to 5.30PM

Thanking you,

Yours sincerely,

Dr.K. M. Palaniswamy

HOD/ECE

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned by CamScanner

Schedule for Faculty Development Program (FDP) on **"A Course on Repair and Maintenance of Equipments"**

Organised By

Navodaya Institute of Technology: Raichur (27/07/2017 & 28/07/2017)

Timing of Each Day Session

Day	Session I (10:30 am-1:00pm)		Session II (2:00pm-5:00pm)
27-07-2017	Basic working principles of the equipments used in electronics laboratories to enhance knowledge	LUNCH BREAK	Maintenance/troubleshooting/servicing/repair of electronic measuring instruments.
26-07-2017	Working and programming of all the software in electronics and communication laboratories.		Simulation of all the software in electronics and communication laboratories.

20/7/17

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

13/6/2017

Navodaya Institute of Technology
RAICHUR-584 103

Scanned by CamScanner

NAVODAYA
Institute of Technology
RAICHUR

Attendance Report

Two day faculty development program on "A development course on repair and maintenance of equipments"

Sl.No	Name of the participant	27/07/2017	28/07/2017
1.	Bhavani		
2.	Chanabasave		
3.	Vidya		
4.	Anusha		

INSTITUTE OF TECHNOLOGY RAICHUR

28/7/17
Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned by CamScanner

NET's

NAVODAYA INSTITUTE OF TECHNOLOGY, RAICHUR

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

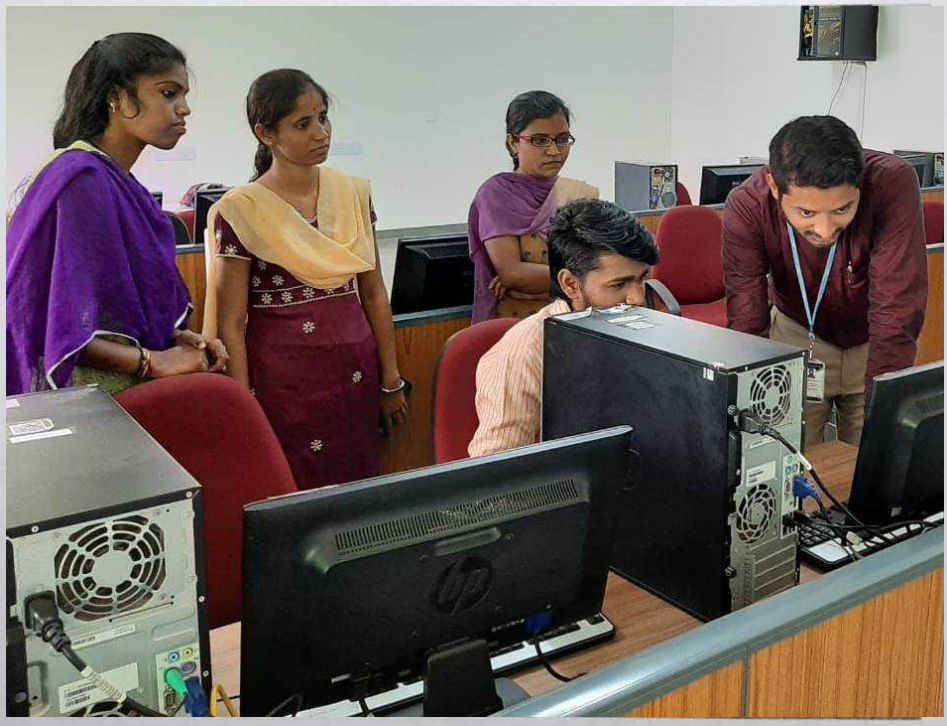
Date: 30/07/2017

Report on Two Days Training Program for Non-Teaching Staff

Two days training program "A development course on repair and maintenance of equipments" was conducted on 27/07/2017 & 28/07/2017 at Navodaya Institue of Technology, organized by Department of ECE.

On first day, the speaker for the session Mr Md Shoaibuddin Madni, Assistant Professor, Dept. of ECE showed the basic working principles of the equipments used in electronics laboratories to enhance knowledge and skill in maintenance/troubleshooting/servicing/repair of electronic measuring instruments.

On second day, the trainer showed the working, programming and simulation of all the software in electronics and communication laboratories.



28/7/17

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

PRINCIPAL 31/8/2017
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned by CamScanner

Training Equipment Users

The primary responsibility for the care and maintenance of equipment rests with the user. Users should understand how their equipment works, what its limitations are, and what it can and cannot do. All of these are usually well described in the user manual that is supplied by the equipment manufacturer or supplier. It is important that users read and understand the user manual and keep it in a safe place.

In situations where surgeons or clinicians work under extreme time pressure, they may not be able to pay sufficient attention to the care and maintenance of the equipment they use. In this case, nursing staff and patient attendants can be trained in basic preventative maintenance and care. However, surgeons and clinicians must still be trained to use equipment properly and safely; they are also responsible for reporting faults and should be included in discussions about maintenance and repair.

Users should be trained to do the following **preventative maintenance** tasks on a regular basis (check the manufacturer's guide or user manual for details):

- Clean outer as well as inner surfaces and lubricated parts.
- Check for damage, loose or missing screws, and corrosion.
- Change filters and renewable parts.
- Lubricate movable parts.

All users, including clinicians, are responsible for the **safety** of their equipment. Users should be trained and encouraged to do the following:

- Carefully wipe the surfaces of the instrument regularly. Check for sharp metal or broken lenses in the instrument that could injure the patient or user.

Top tips for training equipment users

- Demonstrate what to do.
- Allow the student to actually do the work and practice under supervision.

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned by CamScanner

- Maintain a friendly environment, rather than a highly competitive environment, in which to learn.
- Be patient with your students, but expect effort and excellence.
- Always have a back-up plan in case equipment breaks or a part is not available.

Training the Equipment maintenance and repair team

- Basic do's and don'ts when handling equipment
- How to operate equipment
- Basic anatomy cleaning of equipment
- Safety procedures.

Training is not an activity that only happens once. Training is required at various times throughout an employee's career:

- Induction training: when staff are newly placed in post, move to a new department or facility, or to a new location with different responsibilities
- Training when new equipment first arrives
- Refresher training: regular training to update and renew skills throughout the working life of staff.

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned by CamScanner

27.07.2018

From
Dr. K. Venkatachalam,
Professor and Head,
Department of Electronics and Communication Engineering,
Navodaya Institute of Technology,
Raichur-584103

To
The Principal,
Navodaya Institute of Technology,
Raichur-584103

Sir

Subject: Requisition to conduct Training Programs for Non-Teaching Faculty- Reg

We are planning to organize the two day training program for Non-Teaching staff on **"Hands-on Experience on Mvision KEIL IDE & ARM Microcontrollers"** on 30/07/2018 & 31/07/2018 in the department of ECE. I assigned **Mr. Sachin kumar**, Assistant Professor/ECE to conduct this program to the non teaching faculties.

So, I request you to give permission to conduct training program.

Thanking You,

Approved 27/7/18

Yours faithfully,

27/7/18

[Dr. K. Venkatachalam]

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY * RAICHUR

PRINCIPAL 31/8/(WW)
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned by CamScanner

NET's
NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

Tel : +91 8532-240115, 240334, Fax : +91-8532-240054
E-Mail :hodece.nit@navodaya.edu.in

CIRCULAR

Cir.No:2018-19/FDP/NTF/01

Respected Sir / Madam,

Greetings from Navodaya Institute of Technology, Raichur…

The Department of Electronics & Communication Engineering is organizing a **"Hands-on Experience on Mvision KEIL IDE & ARM Microcontrollers"** for Non-Teaching Faculties to upgrade the support staff's skills.

The main objective of this workshop is to enhance the skills in KEIL software.

A simple tool such as KEIL can be quite handy for Embedded-C programming. Also, couple of sessions will be conducted to demonstrate Software Development tools, Current Trends, Future Developments, Microcontroller architecture, Compilers, Debugger etc.

We are pleased to inform that the Department of Electronics & Communication Engineering, Navodaya Institute of Technology is organizing a workshop for Non-Teaching Faculty during **30/07/2018 & 31/07/2018.**

In this context, we look forward your support and request you to encourage participation of support staff's by circulating the attached invitation.

Venue: Department of Electronics & Communication Engineering

Navodaya Institute of Technology, Raichur.

Timing: 9.00AM to 5.30PM

Thanking you,

Yours sincerely,

Dr.K. Venkatachalam
HOD/ECE
Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned by CamScanner

Schedule for Faculty Development Program (FDP) on **"Hands-On Experience On Mvision KEIL IDE & ARM Micro-Controllers"**

Organized By

Navodaya Institute of Technology: Raichur (30/07/2018 & 31/07/2018)

Timing of Each Day Session

Day	Session I (10:30 am-1:00pm)		Session II (2:00pm-5:00pm)
30-07-2018	Introductory lecture over the theme mvision KEIL IDE & ARM micro-controllers, general characteristics of Embedded Systems, Software Development, Tools, Current Trends and Future Developments	LUNCH BREAK	Embedded-C programming on KEIL IDE software. A simple application program for blinking an LED , write and compile their own program code on KEIL IDE
31-07-2018	Basics of Microcontroller architecture, Compilers, Debugger.		Participants to do the procedures explained by the trainer on an 8051 Microcontroller trainer kit and watching the output real-time on the hardware board.

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

PRINCIPAL 13/8/2018
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned by CamScanner

NAVODAYA INSTITUTE OF TECHNOLOGY

Attendance Report

Two day faculty development program on "mvision KEIL IDE & ARM micro-controllers"

Sl.No	Name of the participant	30/7/2018	31/7/2018
1.	Vidya		
2.	Anusha		
3.	Bhavani		
4.	Channabasava		

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

31/7/18

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

PRINCIPAL 31/7/2018
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned by CamScanner

NET's

NAVODAYA INSTITUTE OF TECHNOLOGY, RAICHUR

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Date: 01/08/2018

Report on Two Days Training Program for Non-Teaching Staff

Two days training program "Hands-on experience on mvision KEIL IDE & ARM micro-controllers" was conducted on 30/07/2018 & 31/07/2018 at Navodaya Institue of Technology, organized by Department of ECE.

On first day, the speaker for the session Mr Sachin Kumar, Assistant Professor, Dept. of ECE delivered an introductory lecture over the theme mvision KEIL IDE & ARM micro-controllers; it covered topics like general characteristics of Embedded Systems, Software Development, Tools, Current Trends, and Future Developments and in the second session explained the basics of Microcontroller architecture, Compilers, Debugger etc.

On second day, the speaker started Embedded-C programming on KEIL IDE software. A simple application program for blinking an LED was explained to the participants and they were allowed to write and compile their own program code on KEIL IDE. Then, in second session all the participants were allowed to do the procedures explained by the trainer on an 8051 Microcontroller trainer kit, for watching the output real-time on the hardware board.



NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

PRINCIPAL 13/8/2018
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Head of Department 11/8/18
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned by CamScanner

Create Applications

This chapter guides you through the steps required to create and modify projects using CMSIS described in the previous chapter.

NOTE
The example code in this section works for the MCB1800 evaluation board (populated with LPC1857). Adapt the code for other starter kits or boards.

The tutorial creates the project *Blinky* in these two basic design concepts:

- RTOS design using Keil RTX5.
- Infinite loop design for bare-metal systems without RTOS Kernel.

Blinky with Keil RTX5

The section explains the creation of the project using the following steps:

- **Setup the Project:** create a project file and select the microcontroller device along with the relevant CMSIS components.
- Configure the Device Clock Frequency: configure the system clock.
- Create the Source Code Files: add and create the application files.
- **Build the Application Image:** compile and link the application for downloading it to an on-chip Flash memory of a microcontroller device.
- **Using the Debugger** on page 63 guides you through the steps to connect your evaluation board to the PC and to download the application to the target.

For the project *Blinky*, you will create the following application files:

main.c This file contains the *main()* function that initializes the RTOS kernel, the peripherals, and starts thread execution.

LED.c The file contains functions to initialize and control the GPIO port and the thread function *blink_LED()*. The *LED_Initialize()* function initializes the GPIO port pin. The functions *LED_On()* and *LED_Off()* control the port pin that interfaces to the LED.

LED.h The header file contains the function prototypes for the functions in *LED.c* and is included into the file *main.c*.

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR
3/7/18
Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103
PRINCIPAL 13/8/18
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned by CamScanner

Setup the Project

From the µVision menu bar, choose **Project – New µVision Project**.

☞ Select an empty folder and enter the project name, for example, *Blinky*. Click **Save**, which creates an empty project file with the specified name (*Blinky.uvprojx*).

Next, the dialog **Select Device for Target** opens.

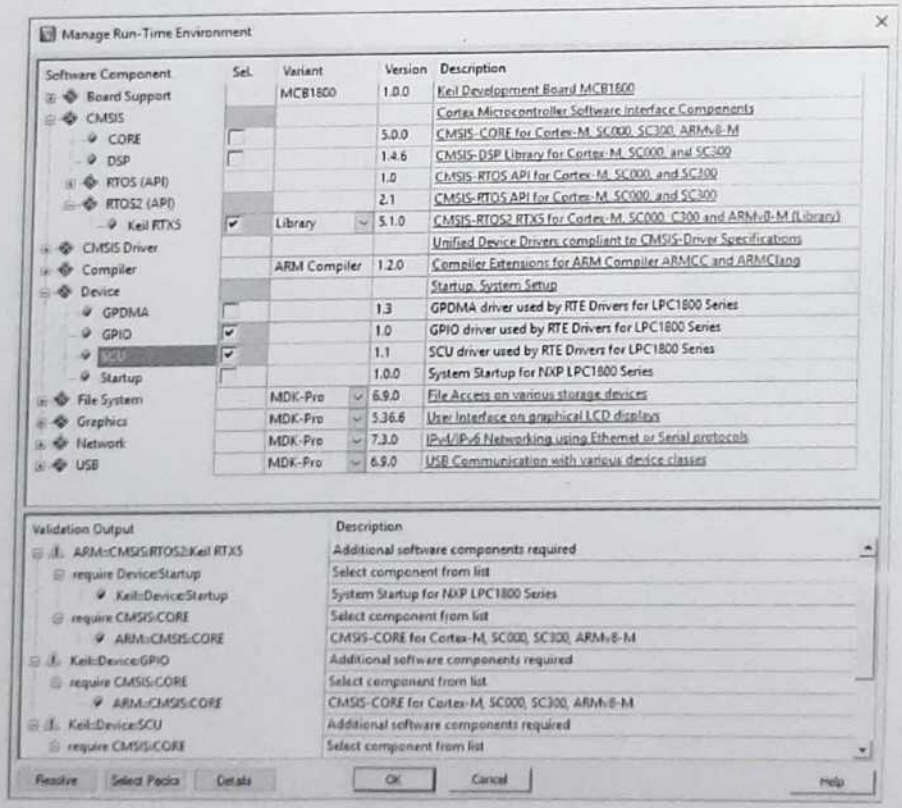
☞ Select the LPC1857 and click **OK**.

The device selection defines essential tool settings such as compiler controls, the memory layout for the linker, and the Flash programming algorithms.

The **Manage Run-Time Environment** dialog opens and shows the software components that are installed and available for the selected device.

☞ Expand **::CMSIS:RTOS2(API)** and enable **:Keil RTX5 (Library)**.

Expand **::Device** and enable **:GPIO** and **:SCU**.



INSTITUTE OF TECHNOLOGY • NAVODAYA • RAICHUR

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

PRINCIPAL 13/9/2023
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned by CamScanner

The **Validation Output** field shows dependencies to other software components. In this case, the components **ARM::CMSIS:CORE** and **::Device:Startup** are required.

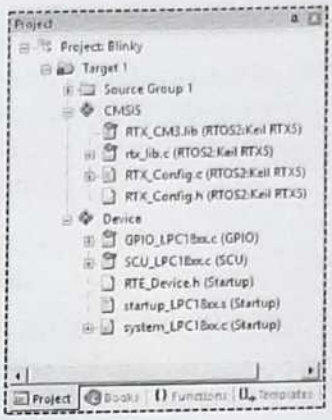
TIP: A click on a message highlights the related software component.

☞ Click **Resolve**.

This resolves all dependencies and enables other required software components (here **ARM::CMSIS:Core** and **::Device:Startup**).

☞ Click **OK**.

The selected software components are included into the project together with the startup file, the RTX sources and configuration files, as well as the CMSIS system files. The **Project** window displays the selected software components along with the related files. Double-click on a file to open it in the editor.



NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

31/8/20

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

31/8/2020

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned by CamScanner

Configure the Device Clock Frequency

The system or core clock is defined in the *system_<device>.c* file. The core clock is also the input clock for the RTOS Kernel Timer and, therefore, the RTX configuration file needs to match this setting.

NOTE
Some devices perform the system setup as part of the main function and/or use a software framework that is configured with external utilities.

*Refer to **Device Startup Variations** on page 56 for more information.*

The clock configuration for an application depends on various factors such as the clock source (XTAL or on-chip oscillator), and the requirements for memory and peripherals. Silicon vendors provide the device-specific file *system_<device>.c* and therefore it is required to read the related documentation.

TIP: Open the reference manual from the **Books** window for detailed information about the microcontroller clock system.

The MCB1800 development kit runs with an external 12 MHz XTAL. The PLL generates a core clock frequency of 180 MHz. As this is the default, no modifications are necessary. However, you can change the settings for your custom development board in the file *system_LPC18xx.c*.

☞ To edit the file *system_LPC18xx.c*, expand the group **Device** in the **Project** window, double-click on the file name, and modify the code as shown below.

Set PLL Parameters in *system_LPC18xx.c*

```
:
/* PLL1 output clock: 180MHz, Fcco: 180MHz, N = 1, M = 15, P = x          */
#define PLL1_NSEL    0              /* Range [0 -   3]: Pre-divider  ratio N */
#define PLL1_MSEL   14              /* Range [0 - 255]: Feedback-div ratio M */
#define PLL1_PSEL    0              /* Range [0 -   3]: Post-divider ratio P */

#define PLL1_BYPASS 0               /* 0: Use PLL, 1: PLL is bypassed        */
#define PLL1_DIRECT 1               /* 0: Use PSEL, 1: Don't use PSEL        */
#define PLL1_FBSEL  0               /* 0: FCCO is used as PLL feedback       */
                                    /* 1: FCLKOUT is used as PLL feedback    */
:
```

Keil RTX5 automatically detects the clock setting so that a manual adaption is not required.



31/7/18

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

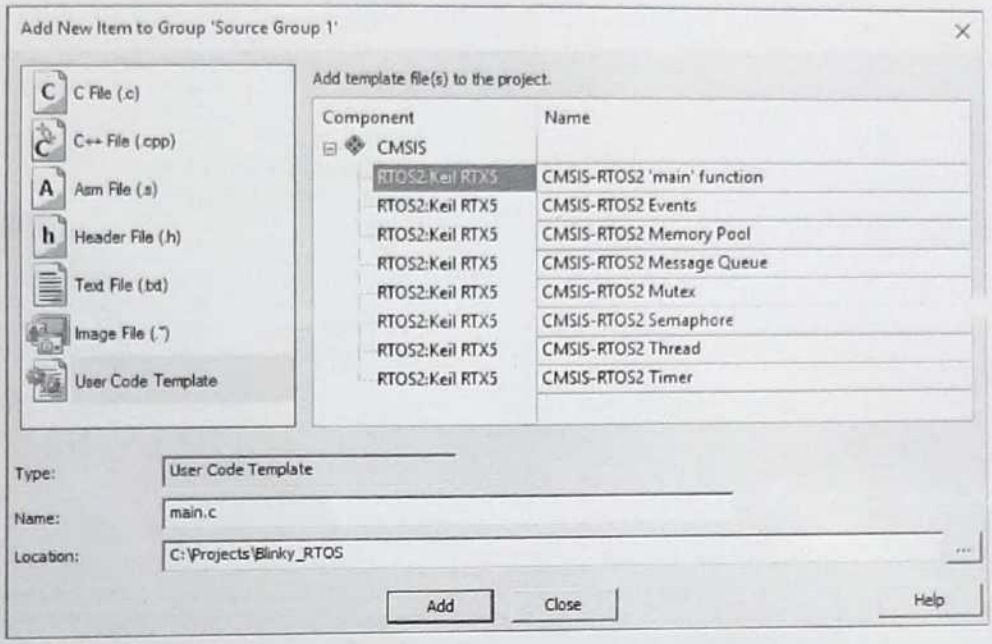
PRINCIPAL 13/9/18
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned by CamScanner

Create the Source Code Files

Add your application code using pre-configured **User Code Templates** containing routines that resemble the functionality of the software component.

☞ In the **Project** window, right-click **Source Group 1** and open the dialog **Add New Item to Group**.



☞ Click on **User Code Template** to list available code templates for the software components included in the project. Select **CMSIS-RTOS2 'main' function** and click **Add**.

This adds the file *main.c* to the project group **Source Group 1**. Now you can add application specific code to this file.

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned by CamScanner

Add the code below to create a function *blink_LED()* that blinks LEDs on the evaluation kit.

Code for *main.c*

```
/*----------------------------------------------------------------------------
 * CMSIS-RTOS 'main' function template
 *---------------------------------------------------------------------------*/

#include "RTE_Components.h"
#include  CMSIS_device_header
#include "cmsis_os2.h"
#include "LED.h"

#ifdef RTE_Compiler_EventRecorder
#include "EventRecorder.h"
#endif

/*----------------------------------------------------------------------------
* Application main thread
*---------------------------------------------------------------------------*/
void app_main (void *argument) {

  Init_BlinkyThread ();                    // Start Blinky thread
  for (;;) {}
}

int main (void) {

  // System Initialization
  SystemCoreClockUpdate();
#ifdef RTE_Compiler_EventRecorder
  // Initialize and start Event Recorder
  //EventRecorderInitialize(EventRecordError, 1U);
#endif
  // ...
  LED_Initialize ();                       // Initialize LEDs

  osKernelInitialize();                    // Initialize CMSIS-RTOS
  osThreadNew(app_main, NULL, NULL);       // Create application main thread
  osKernelStart();                         // Start thread execution
  for (;;) {}
}
```

NOTE
The file RTE_Components.h includes a define/macro specifying the name of the device header file such that you can specify the device include in a device agnostic way using #include CMSIS_device_header.

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
Raichur-584 103

PRINCIPAL 13/8/2021
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned by CamScanner

Create an empty C-file named *LED.c* using the dialog **Add New Item to Group** and add the code to initialize and access the GPIO port pins that control the LEDs.

Code for *LED.c*

```
/*----------------------------------------------------------------------------
 * File LED.c
 *----------------------------------------------------------------------------*/
#include "SCU_LPC18xx.h"
#include "GPIO_LPC18xx.h"
#include "cmsis_os2.h"                  // ARM::CMSIS:RTOS:Keil RTX5

osThreadId_t tid_blink_LED;             // Thread id of thread blink_LED

void blink_LED (void *argument);  // Prototype function

void LED_Initialize (void) {
  GPIO_PortClock      (1);              // Enable GPIO clock

  /* Configure pin: Output Mode with Pull-down resistors */
  SCU_PinConfigure (13, 10, (SCU_CFG_MODE_FUNC4|SCU_PIN_CFG_PULLDOWN_EN));
  GPIO_SetDir       (6, 24, GPIO_DIR_OUTPUT);
  GPIO_PinWrite     (6, 24, 0);
}

void LED_On (void) {
  GPIO_PinWrite     (6, 24, 1);           // LED on: set port
}

void LED_Off (void) {
  GPIO_PinWrite     (6, 24, 0);           // LED off: clear port
}

// Blink LED function
void blink_LED(void *argument) {
  for (;;) {
    LED_On ();                          // Switch LED on
    osDelay (500);                      // Delay 500 ms
    LED_Off ();                         // Switch off
    osDelay (500);                      // Delay 500 ms
  }
}

void Init_BlinkyThread (void) {
  tid_blink_LED = osThreadNew (blink_LED, NULL, NULL);  // Create thread
}
```

NOTE
You can also use the functions as provided by the ***Board Support*** *component described on page 45****Error! Bookmark not defined.****.*

RAICHUR INSTITUTE OF TECHNOLOGY
Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned by CamScanner

Create an empty header file named *LED.h* using the dialog **Add New Item to Group** and define the function prototypes of *LED.c*.

Code for *LED.h*

```
/*----------------------------------------------------------------------------
 * File LED.h
 *----------------------------------------------------------------------------*/
void LED_Initialize ( void );                          // Initialize GPIO
void LED_On ( void );                                  // Switch Pin on
void LED_Off ( void );                                 // Switch Pin off

void blink_LED ( void const *argument );               // Blink LEDs in a thread
void Init_BlinkyThread ( void );                       // Initialize thread
```

Build the Application Image

Build the application, which compiles and links all related source files.

Build Output shows information about the build process. An error-free build displays program size information, zero errors, and zero warnings.

```
Build Output
*** Using Compiler 'V5.06 update 4 (build 422)', folder: 'C:\Keil_v5\ARM\ARMCC\Bin'
Rebuild target 'Target 1'
compiling LED.c...
compiling rtx_lib.c...
compiling main.c...
compiling RTX_Config.c...
assembling startup_LPC18xx.s...
compiling GPIO_LPC18xx.c...
compiling SCU_LPC18xx.c...
compiling system_LPC18xx.c...
linking...
Program Size: Code=5664 RO-data=1036 RW-data=5504 ZI-data=1632
After Build - User command #1: C:\Keil_v5\/ARM/BIN/ElfDwT.exe .\Objects\Blinky.axf BASEADDRESS(0x1A000000)
ELFDWT - Signature Creator V1.2.0.0
COPYRIGHT Copyright (C) 2014-2016, ARM Ldt. and  ARM Germany GmbH
*** Updated Signature over Range[32] (0x1A000000 - 0x1A000018): @0x1A00001C = 0x53FFCD46
*** Processing completed, no Errors.
".\Objects\Blinky.axf" - 0 Error(s), 0 Warning(s).
Build Time Elapsed:  00:00:02
```

The section **Using the Debugger** on page 63 guides you through the steps to connect your evaluation board to the workstation and to download the application to the target hardware.

TIP: You can verify the correct clock and RTOS configuration settings of the target hardware by checking the one-second interval of the LED.

Head of Department
Electronics & Communication Engineering
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Scanned by CamScanner

16.04.2020

From
Dr. M. N. Faruk,
Professor and Head, CSE
Navodaya Institute of Technology,
Raichur-584103.

To
The Principal,
Navodaya Institute of Technology,
Raichur-584103

Sir

Subject: Requisition to conduct Training Programs for Non-Teaching Faculty- Reg

We have proposed to conduct the Training programs on the topic **"Hands-on Workshop on Hardware and Networking Basics"** on 21.04.2020 to 23.04.2020 at Navodaya Institute of Technology, Department of CSE, Raichur from 09.00 AM to 05.30 PM.

Thanking You

Yours faithfully

[Dr. M. N. Faruk]
Head of Department
Computer Science & Engineering
Navodaya Institute of Technology
RAICHUR-584 103

PRINCIPAL 16-4-20
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

NET's
NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

Tel : +91 8532-200115, 240334, Fax : +91-8532-240054
E-Mail :hodcse.nit@navodaya.edu.in

CIRCULAR

Cir: 2019-20/FDP/01 16-04-2020

Respected Sir / Madam,

Greetings from Navodaya Institute of Technology, Raichur…

The department of Computer Science & Engineering is organizing a **"Hands on Workshop on Hardware and Networking Basics"** for Non-Teaching Faculties to upgrade the support staff's skills. The main objective of this workshop is to enhance the skills in Hardware and networking within the college premises. The Hardware and Networking Course covers all the fundamental topics of Hardware and Networking, as well as special attention, is given to the practical implications. The syllabus includes the study of computer components, types of RAM, Internet connectivity procedures and other such hardware related concepts. The networking part of the syllabus includes dealing with important topics such as various internet protocols and network troubleshooting. The workshop makes the candidate capable enough to take up any challenges offered by the company.

We are pleased to inform that the department of Computer Science & Engineering, Navodaya Institute of Technology is organizing a workshop for Non-Teaching Faculty during **21.04.2020 to 23.04.2020.**

In this context, we look forward your support and request you to encourage participation of support staff's by circulating the attached invitation.

Venue: Department of Computer Science & Engineering
Navodaya Institute of Technology, Raichur.
Timing: 9.00AM to 5.30PM

Thanking you,
Yours sincerely,

PRINCIPAL 16.4.2020
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

RAICHUR

Dr. M. N. Faruk
HOD, CSE
Head of Department
Computer Science & Engineering
Navodaya Institute of Technology
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

NON TEACHING STAFF SKILLS UP GRADUATION

DEPARTMENT: **CSE**

ACADEMIC YEAR: **2019-20** DATE: **21-04-2020**

SL.No	Name of the Staff	Date	Details
1.	Mr. Vijay Vardhan (CSE)	21-04-2020 to 23-04-2020	Programmer, Dept. of CSE
2.	Ms. Vaishnavi (CSE)	21-04-2020 to 23-04-2020	Programmer, Dept. of CSE
3.	Ms. Vidya (ECE)	21-04-2020 to 23-04-2020	Lab Technician, Dept. of ECE

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Head of Department
Computer Science & Engineering
Navodaya Institute of Technology
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Navodaya Education Trust ®
Navodaya Institute of Technology
Raichur

A Report

On

"Hands on Workshop on Hardware and Networking Basics"

Coordinators:

Dr. M. N. Faruk, ***Head and Professor***

Prof. Vijaykumar Yadav, ***Assistant Professor***

Prof. Sivakumar Reddy, ***Assistant Professor***

Department of Computer Science & Engineering

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Organized by:

Department of Computer Science & Engineering

Navodaya Institute of Technology, Raichur

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Date: 21st April to 23rd April, 2020

Head of Department
Computer Science & Engineering
Navodaya Institute of Technology
RAICHUR-584 103

Objective:

The main objective of this workshop is to enhance the skills in Hardware and networking within the college premises. The Hardware and Networking Course covers some of the fundamental topics of Hardware and Networking, as well as special attention, is given to the practical implications. The syllabus includes the study of computer components, types of RAM, Internet connectivity procedures and other such hardware related concepts. The networking part of the syllabus includes dealing with important topics such as various internet protocols and network troubleshooting. The workshop makes the candidate capable enough to take up any challenges offered by the company.

23/4/2024

PRINCIPAL
...vodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

25/4/24

...ead of Department
...omputer Science & Engineering
...vodaya Institute of Technology
RAICHUR-584 103

Basic Networking Hardware

Agenda

- Basic LAN Definition
- Network Hardware
- Network Media
- Sample LAN Implementation

LANs

- Definition – LAN
  - "local area network"
  - is a group of computers and associated devices that share a common communications line or wireless link and typically share the resources of a single processor or server within a small geographic area (for example, within an office building).
  - Usually, the server has applications and data storage that are shared in common by multiple computer users.
  - A local area network may serve as few as two or three users (for example, in a home network) or many as thousands of users.

LANs

- Definition – Wireless LAN
  - A local area network that transmits over the air typically in an unlicensed frequency such as the 2.4GHz band.
  - A wireless LAN does not require lining up devices for line of sight transmission.
  - Wireless access points (base stations) are connected to an Ethernet hub or server and transmit a radio frequency over an area of several hundred to a thousand feet which can penetrate walls and other non-metal barriers.
  - Roaming users can be handed off from one access point to another like a cellular phone system.
  - Laptops use wireless network cards that plug into an existing PCMCIA slot or that are self contained on PC cards, while stand-alone desktops and servers use plug-in cards (ISA, PCI, etc.).

LANs

- Characteristics – LAN
  - Topology
    - The geometric arrangement of devices on the network or the shape of a local-area network (LAN) or other communications system.
  - Protocols
    - The rules and encoding specifications for sending data. The protocol defines the format and meaning of the data that is exchanged. The protocols also determine whether the network uses a peer-to-peer or client/server architecture.
  - Media
    - Devices can be connected by twisted-pair wire, coaxial cables, or fiber optic cables. Some networks do without connecting media altogether, communicating instead via radio waves.

LANs

- Topology types



- bus topology: All devices are connected to a central cable, called the bus or backbone. Bus networks are relatively inexpensive and easy to install for small networks. Ethernet systems use a bus topology.
- star topology: All devices are connected to a central hub. Star networks are relatively easy to install and manage, but bottlenecks can occur because all data must pass through the hub. This is not much of a problem anymore with the widespread deployment of switches.
- ring topology: All devices are connected to one another in the shape of a closed loop, so that each device is connected directly to two other devices, one on either side of it. Ring topologies are relatively expensive and difficult to install, but they offer high bandwidth and can span large distances.

INSTITUTE OF TECHNOLOGY
RAICHUR

Network Hardware

- Hub
  - An unintelligent network device that sends one signal to all of the stations connected to it.
  - All computers/devices are competing for attention because it takes the data that comes into a port and sends it out all the other ports in the hub.
  - Traditionally, hubs are used for star topology networks, but they are often used with other configurations to make it easy to add and remove computers without bringing down the network.
  - Resides on Layer 1 of the OSI model

OSI Model Layers
7 Application
6 Presentation
5 Session
4 Transport
3 Network
2 Data Link
1 Physical



Network Hardware

- Switch
  - Split large networks into small segments, decreasing the number of users sharing the same network resources and bandwidth.
  - Understands when two devices want to talk to each other, and gives them a switched connection.
  - Helps prevent data collisions and reduces network congestion, increasing network performance.
  - Most home users get very little, if any, advantage from switches, even when sharing a broadband connection.
  - Resides on Layer 2 of the OSI model.

OSI Model Layers
7 Application
6 Presentation
5 Session
4 Transport
3 Network
2 Data Link
1 Physical



Network Hardware

- Bridge
  - Connects two LANs and forwards or filters data packets between them.
  - Creates an extended network in which any two workstations on the linked LANs can share data.
  - Transparent to protocols and to higher level devices like routers.
  - Forward data depending on the Hardware (MAC) address, not the Network address (IP).
  - Resides on Layer 2 of the OSI model.

OSI Model Layers
7 Application
6 Presentation
5 Session
4 Transport
3 Network
2 Data Link
1 Physical



Head of Department
Computer Science & Engineering
Navodaya Institute of Technology
RAICHUR-584 103.

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103.

Network Hardware

- Repeater
  - Used to boost the signal between two cable segments or wireless access points.
  - Can not connect different network architecture.
  - Does not simply amplify the signal, it regenerates the packets and retimes them.
  - Resides on Layer 1 of the OSI model.

OSI Model Layers
7 Application
6 Presentation
5 Session
4 Transport
3 Network
2 Data Link
1 Physical



Network Hardware

- Router
  - A device that connects any number of LANs.
  - Uses standardized protocols to move packets efficiently to their destination.
  - More sophisticated than bridges, connecting networks of different types (for example, star and token ring)
  - Forwards data depending on the Network address (IP), not the Hardware (MAC) address.
  - Routers are the only one of these four devices that will allow you to share a single IP address among multiple network clients.
  - Resides on Layer 3 of the OSI model.

OSI Model Layers
7 Application
6 Presentation
5 Session
4 Transport
3 Network
2 Data Link
1 Physical



Network Hardware

- Additional Network Hardware Devices
  - Network Interface Cards (NICs)
    - Puts the data into packets and transmits packet onto the network
    - May be wired or wireless
  - Gateway
    - Connects networks with different protocols like TCP/IP network and IPX/SPX networks
    - Routers and Gateways often refer to the same device
  - Proxy server
    - Isolates internal network computers from the internet
    - The user first access the proxy server and the proxy server accesses the internet and retrieves the requested web page or document. The user then gets a copy of that page from the proxy server



Common Network Media

- Electrical (copper)
  - Coaxial Cable
    - Single copper conductor in the center surrounded by a plastic layer for insulation and a braided metal outer shield
  - Twisted pair
    - Four pairs of wires twisted to certain specifications
    - Available in shielded and unshielded versions
- Fiber-optic – A cable, consisting of a center glass core surrounded by layers of plastic, that transmits data using light rather than electricity
- Atmosphere/Wireless – Uses Electromagnetic waves, whose frequency range is above that of microwaves, but below that of the visible spectrum.
- Choose Media based on :
  - Wiring configurations
  - Distance and location limitations
  - Speed
  - Reliability
  - Security
  - Budget

Copper - Twisted Pair

- Dialup over telephone line.
  - DSL (Digital Subscriber Line)
    - High-speed (256 Kbps – 55 Mbps), Full-duplex
    - Asymmetric Digital Subscriber Line (ADSL) and High-bit-rate Digital Subscriber Line (HDSL)
- CAT5
  - Ethernet cable standard defined by the Electronic Industries Association and Telecommunications Industry Association (EIA/TIA)
  - Speeds up to 100 Mbps
- Connector
  - RJ-45 - Standard connectors used for unshielded twisted-pair cable.



Ethernet Specifications

- 10BaseT
  - Ethernet specification for unshielded twisted pair cable (category 3, 4 or 5), transmits signals at 10 Mbps (megabits per second) with a distance limit of 100 meters per segment.
- 10BaseF
  - Ethernet specification for fiber optic cable, transmits signals at 10 Mbps (megabits per second) with a distance limit of 2000 meters per segment.
- 100BaseT
  - Ethernet specification for unshielded twisted pair cabling that is used to transmit data at 100 Mbps (megabits per second) with a distance limit of 100 meters per segment.
- 1000BaseTX
  - Ethernet specification for unshielded twisted pair cabling that is used to transmit data at 1 Gbps (gigabits per second) with a distance limitation of 330 meters per segment.

Optical Fiber

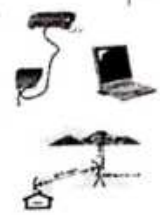
- Infrared light is transmitted through fiber and confined due to total internal reflection.
- Fibers can be made out of either plastic or glass.
- Used for high speed backbones and pipes over long distances.
- Comparatively expensive.



Cable Types Characteristics

Wireless Media

- Wireless LAN or WLAN
  - Wireless local area network that uses radio waves as its carrier
- Wi-Fi ("Wireless Fidelity")
  - A set of standards for WLANs based on IEEE 802.11
- Wi-Max
  - Emerging technology that can cover ranges up to 10 miles or more
- Satellite/Microwave
  - High speed media used for longer distances and remote locations

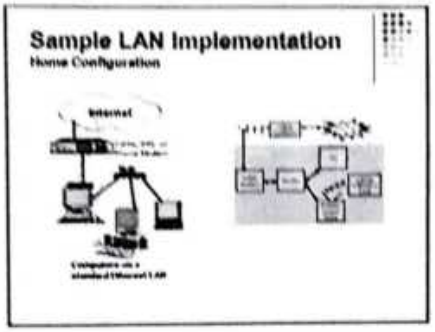


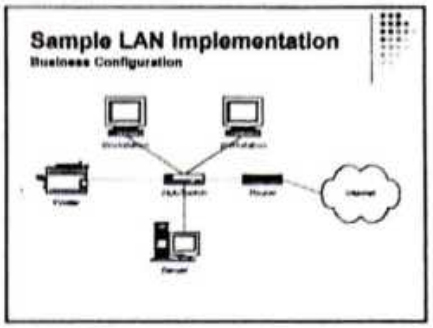
NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

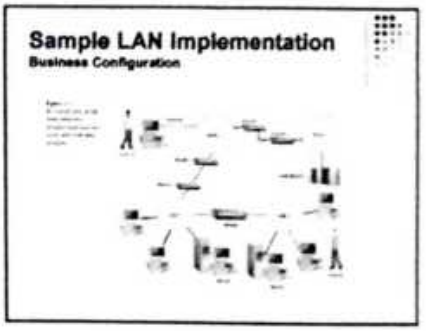
22/4/20

Head of Department
Computer Science & Engineering
Navodaya Institute of Technology
RAICHUR-584 103

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103







Head of Department
Computer Science & Engineering
Navodaya Institute of Technology
RAICHUR-584 103

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

NET's
NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

Tel : +91 8532-200115, 240334, Fax : +91-8532-240054
E-Mail :hodcse.nit@navodaya.edu.in

CIRCULAR

Cir : 2016-17/FDP/01

Date: 20-07-2017

Respected Sir / Madam,

Greetings from Navodaya Institute of Technology, Raichur…

The department of Computer Science & Engineering is organizing a **"Training Session on Microsoft Office Tools"** for Non-Teaching Faculties to upgrade the support staff's skills. The main objective of this workshop is to enhance the skills in microsoft tools like Microsoft office word, Powerpoint and Microsoft Excel. As we all are aware that the documentation works at the level of non teaching staff needs Word processing and data repository tools.

We are pleased to inform that the department of Computer Science & Engineering, Navodaya Institute of Technology is organizing a workshop for Non-Teaching Faculty during **23.07.2017 to 25.07.2017.**

In this context, we look forward your support and request you to encourage participation of support staff's by circulating the attached invitation.

Venue: Department of Computer Science & Engineering
Navodaya Institute of Technology, Raichur.
Timing: 9.00AM to 5.30PM

Thanking you,
Yours sincerely,

20/07/17.

Mr. Vijaykumar Yadav
(I/C) HOD, CSE
Computer Science & Engineering
Navodaya Institute of Technology
RAICHUR-584 103

PRINCIPAL 20.7.17
Navodaya Institute of Technology (NIT)
RAICHUR 584 103

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

20.07.2017

From
Mr. Vijaykumar Yadav,
(I/C) Head, CSE
Navodaya Institute of Technology,
Raichur-584103.

To
The Principal,
Navodaya Institute of Technology,
Raichur-584103

Sir

Subject: Requisition to conduct Training Programs for Non-Teaching Faculty- Reg

We have proposed to conduct the Training programs on the topic **"Training Session on Microsoft Office Tools"** on 23.07.2017 to 25.07.2017 at Navodaya Institute of Technology, Department of CSE, Raichur from 09.00 AM to 05.30 PM.

Thanking You

Yours faithfully

[Mr. Vijaykumar Yadav]

Head of Department
Computer Science & Engineering
Navodaya Institute of Technology
RAICHUR-584 103

20.7.17
PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

NON TEACHING STAFF SKILLS UP GRADUATION

DEPARTMENT: **CSE**

ACADEMIC YEAR: **2017-18**

DATE: **23-07-2017**

SL.No	Name of the Staff	Date	Details
1.	Mr. Anil Kandagal (CSE)	23-07-2017 to 25-07-2017	Programmer, Dept. of CSE
2.	Mr. Hariprasad (Civil)	23-07-2017 to 25-07-2017	Programmer, Dept. of CSE
3.	Ms. Vidya (ECE)	23-07-2017 ,to 25-07-2017	Lab Technician, Dept. of ECE
4.	Mr. Veeresh (Mech)	23-07-2017 to 25-07-2017	Lab Technician, Dept. of ME

PRINCIPAL
Navodaya Institute of Technology (III)
RAICHUR-584 103

Head of Department
Computer Science & Engineering
Navodaya Institute of Technology
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY • RAICHUR

NAVODAYA
Institute of Technology
RAICHUR

A Report On

"Training Session on Microsoft Office Tools"

Coordinators:

Prof. Vijaykumar Yadav, ***Assistant Professor (I/C HOD)***

Prof. Chetan Gudi, ***Assistant Professor***

Department of Computer Science & Engineering

Organized by:

Department of Computer Science & Engineering

Navodaya Institute of Technology, Raichur

Date: 23rd July to 25th July, 2017

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

Head of Department
Computer Science & Engineering
Navodaya Institute of Technology
RAICHUR-584 103

Course Description and Objective:

0ffice tools course would enable the faculty in crafting professional word documents, excel spread sheets, power point presentations using the Microsoft suite of office tools. To familiarize the faculties in preparation of documents and presentations with office automation tools.

Course Outcomes:

By learning the course, the students will be able

- to perform documentation
- to perform accounting operations
- to perform presentation skills

Word

Word Orientation :

The instructor needs to give an overview of Microsoft word & Importance of MS Word as word Processor, Details of the four tasks and features that would be covered Using word – Accessing, overview of toolbars, saving files, Using help and resources, rulers, format painter.

Task 1 : Using word to create project certificate. Features to be covered:-Formatting Fonts in word, Drop Cap in word, Applying Text effects, Using Character Spacing, Borders and Colors, Inserting Header and Footer, Using Date and Time option in Word.

Task 2 : Creating project abstract Features to be covered:-Formatting Styles, Inserting table, Bullets and Numbering, Changing Text Direction, Cell alignment, Footnote, Hyperlink, Symbols, Spell Check , Track Changes.

Task 3 : Creating a Newsletter : Features to be covered:- Table of Content, Newspaper columns, Images from files and clipart, Drawing toolbar and Word Art, Formatting Images, Textboxes and Paragraphs

Task 4 : Creating a Feedback form - Features to be covered- Forms, Text Fields, Inserting objects, Mail Merge in Word.

Excel

Excel Orientation : The instructor needs to tell the importance of MS Excel as a Spreadsheet tool, give the details of the four tasks and features that would be covered Excel – Accessing, overview of toolbars, saving excel files, Using help and resources {Comdex Information Technology course tool kit Vikas }

Task1: Creating a Scheduler - Features to be covered: Gridlines, Format Cells, Summation, auto fill, Formatting Text

Task 2 : Calculations - Features to be covered:- Cell Referencing, Formulae in excel – average, std.deviation, Charts, Renaming and Inserting worksheets, Hyper linking, Count function, LOOKUP/VLOOKUP

Task 3 : Performance Analysis - Features to be covered:- Split cells, freeze panes, group and outline, Sorting, Boolean and logical operators, Conditional formatting

PRINCIPAL
Navodaya Institute of Technology
RAICHUR-584 103

RAICHUR

Head of Department
Computer Science & Engineering
Navodaya Institute of Technology
RAICHUR-584 103

Task 4 : Cricket Score Card - Features to be covered:-Pivot Tables, Interactive Buttons, Importing Data, Data Protection, Data Validation

MS Power Point

Task1 : Students will be working on basic power point utilities and tools which help them create basic power point presentation. Topic covered includes :- PPT Orientation, Slide Layouts, Inserting Text, Word Art, Formatting Text, Bullets and Numbering, Auto Shapes, Lines and Arrows

Task 2 : This session helps students in making their presentations interactive. Topics covered includes : Hyperlinks, Inserting –Images, Clip Art, Audio, Video, Objects, Tables and Charts

Task 3 : Concentrating on the in and out of Microsoft power point. Helps them learn best practices in designing and preparing power point presentation. Topics covered includes :- Master Layouts (slide, template, and notes), Types of views (basic, presentation, slide slotter, notes etc), Inserting – Background, textures, Design Templates, Hidden slides. Auto content wizard, Slide Transition, Custom Animation, Auto Rehearsing

Task 4 : Power point test would be conducted. Students will be given model power point presentation which needs to be replicated (exactly how it's asked).

PRINCIPAL 25/7/12
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

Head of Department
Computer Science & Engineering
Navodaya Institute of Technology
RAICHUR-584 103

2019-20/W-S/01

01.02.2020

From
Dr.P. Rathnakumar,
Professor and Head,
Department of Mechanical Engineering
Navodaya Institute of Technology,
Raichur-584103.

To
The Principal,
Navodaya Institute of Technology,
Raichur-584103

Sir

Subject: Requisition to conduct Training Programs for Non-Teaching Faculty- Reg

We have proposed to conduct the Training programs on the topic **"Hands on Workshop in Solid Edge" from 05.02.2020 to 07.02.2020 for** non-teaching staff at Navodaya Institute of Technology, Department of Mechanical Engineering, and Raichur from 10.00 AM to 05.00 PM.

Thanking You

Yours faithfully

01/02

[Dr. Rathnakumar]

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology, NITS
RAICHUR.

01/2/2020

PRINCIPAL
Navodaya Institute of Technology (NITS)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECH
RAICHUR

NET's
NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

Tel : +91 8532-200115, 240334, Fax : +91-8532-240054
E-Mail :hodme.nit@navodaya.edu.in

CIRCULAR

2019-20/WS/02 03-02-20

TO
Non-teaching staff
NIT Raichur,

Greetings from Navodaya Institute of Technology, Raichur…

The department of Mechanical Engineering is organizing a **"Hands on Workshop in Solid Edge"** for Non-Teaching Faculties to upgrade the support staff's skills.

The main objective of this workshop is to enhance the skills in **Solid Edge** software. Simple Drawing software such as **Solid Edge** can be quite handy to draw highly complicated machine components; also couple of sessions will be conducted for the use of **Solid Edge** software to demonstrate its applications to specific topics such as Designing and modeling.

We are pleased to inform that the department of Mechanical Engineering, Navodaya Institute of Technology is organizing a workshop for Non-Teaching Faculty during **05.02.2020 to 07.02.2020.**

In this context, we look forward your support and request you to encourage participation of support staff's by circulating the attached invitation.

Venue: Department Mechanical Engineering
Navodaya Institute of Technology, Raichur.
Timing: 9.00AM to 5.00 PM

03/02/2020

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Thanking you,
Yours sincerely,

03/2

Dr.P.Rathnakumar
HOD/MECH

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology (NIT)
RAICHUR.

NAVODAYA INSTITUTE OF TECHNOLOGY

NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

NON TEACHING STAFF SKILLS UP GRADUATION

DEPARTMENT: Mechanical Engineering

ACADEMIC YEAR: 2019-2020 DATE: 07/02/2020

Sl.No	Name of the Staff	Date	Details	Signature
(1)	Veeresh	7/2/2020	Instructor (M/c shop)	
(2)	Raghavan	7/2/2020	Instructor (CAED Lab)	
(3)	Sandeep Kumar	7/2/2020	Instructor (CIM Lab)	
(4)	Y.B. Sandeep Raj	7/2/2020	Instructor (EC Lab)	
(5)	Chandra Shekar	7/2/2020	Foreman (m/c shop)	

07/02

HEAD OF THE DEPARTMENT

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology (NIT)
RAICHUR.

07/02/2020

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY ★ RAICHUR

Navodaya Education Trust ®
Navodaya Institute of Technology
Raichur

A Report

On

"Hands on Workshop in Solid Edge"

For Non-Teaching Staff

Coordinators:

Prof Shrikar G Kulkarni, *Assistant Professor*

Prof. Ravi Kulkarni, *Assistant Professor*

Prof. Priyankar D, *Assistant Professor*

Department of Mechanical Engineering

Organized by:

Department of Mechanical Engineering

Navodaya Institute of technology-Raichur

Date: 05th February to 7 February 2020

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

"Hands on Workshop in Solid Edge"

Navodaya Institute of Technology, Raichur Mechanical Engineering department conducted a worksh op on **"Solid Edge"** The workshop was based on the Basics of drawing to upgrade the knowledge of supporting staffs in the department. Coordinators- Prof Shrikar G Kulkarni, Assistant Professor Prof. Ravi Kulkarni, Assistant Professor Prof. Priyankar D, Assistant delivered the content with practical and hands on training to part icipants.





NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Module 1: PRIME

Learning Tools

Help
Tooltips
Command Finder
Tutorials
Take a tour of the User Interface

Design

Design Workflow
Application Button
Quick Access Toolbar
The Ribbon with Tabbed Groups
Command Bar with Docking Window and Tab Sets
Prompt and Status Bar
Mouse Button Options
Traditional Part Design Workflow
Creating Models
Edit and Change
Adding to the Model
Manage the Completed Model

Draft

Interface
Sheets
Views
Dimensions and Annotation Tools
2D to 3D to 2D Workflow
Custom Sheets and Title Blocks

Assembly

Interface
Building the Foundation: Relationships
Changing that Foundation, No Cracks!
Component Design Changes
Tools for Managing the Growing Assembly

Assembly Draft

Views
Bill of Materials / Parts List
Explode/Render/Animate – Exploded Views
--Basic Commands and syntax
--Identifiers
--Arrays and Matrices
--Referencing Elements
--Referencing Elements
--Matrix Operations
--Array Operations

Prof Shrikar G Kulkarni, Assistant Professor
Department of Mechanical Engineering
NIT Raichur



MODULE 2: SYNCHRONOUS TRANSITION

Itinerary

Interface - The New Look
Learning Tools
Help
Tooltips

Prof. Ravi Kulkarni, Assistant Professor
Department of Mechanical Engineering
NIT Raichur

Command Finder - Take a Tour of the User Interface **Design Workflow** Application Button Quick Access Toolbar The Ribbon with Tabbed Groups Command Bar with Docking Window and Tab Sets Prompt and Status Bars Mouse Button Options **Design** Optimized Part Design Workflow PRE-ST Design Tools Creating HYBRID Models: History-based and PRE-ST Working with HYBRID Models Synchronous Design Workflow Real-time Changes Live Rules **Assembly** Interface Building the Foundation: Relationships Changing that Foundation…No Cracks! Component Design Changes Tools for Managing the Growing Assembly Synchronous Assemblies Synchronous Assembly Design	**Prof. Ravi Kulkarni, Assistant Professor** **Department of Mechanical Engineering** **NIT Raichur**
Module 3: Advanced Part & Assembly Design **Part Breakdown** Where to start, Sketches or Regions? Traditional or Synchronous design? Which features to model first **Profile/Sketch Tools** 2D designs that will improve your use of Intellisketch Tips for faster more reliable sketching Predictable/reliable profiles that won't blow up **Optimize Design** Which features to draw? Which featured to model? Learn to model the part - not draw it, what are LIVE RULES? Combining features to reduce file overhead and control changes Model intermediate to difficult parts /SMART PARTS	**Prof. Priyankar D, Assistant Professor** **Department of Mechanical Engineering** **NIT Raichur**

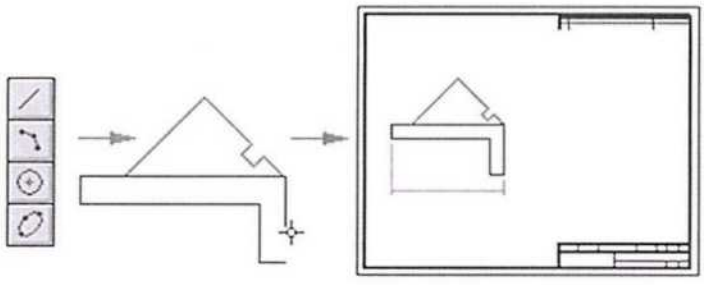


2D drawing views

A 2D drawing view consists of two-dimensional elements. It is not associative to a 3D model. A 2D drawing view allows you to quickly create or modify a drawing view without making changes to a part or assembly document.

To create a 2D drawing view of a part or assembly, you can convert a 3D part view or you can draw the 2D graphics yourself. You also can import a 2D design file and then create 2D views from it. You can layer 2D graphics on top of a 2D view.

Whenever you add or edit 2D graphical elements, a full range of drawing tools is provided. These include drawing and relationship commands that make it easy for you to draw an accurate 2D representation of a part or assembly.



Note:

For more information about 2D drawing in Solid Edge, see the Drawing 2D elements Help topic.

2D model views

2D model views are scaled 2D drawing views placed on working sheets of geometry that reside at full scale on the 2D Model sheet. You can create multiple 2D model views that reference the 2D model geometry, and you can customize the cropping boundary for each view created from the geometry on the 2D Model sheet.

Creating a 2D drawing view

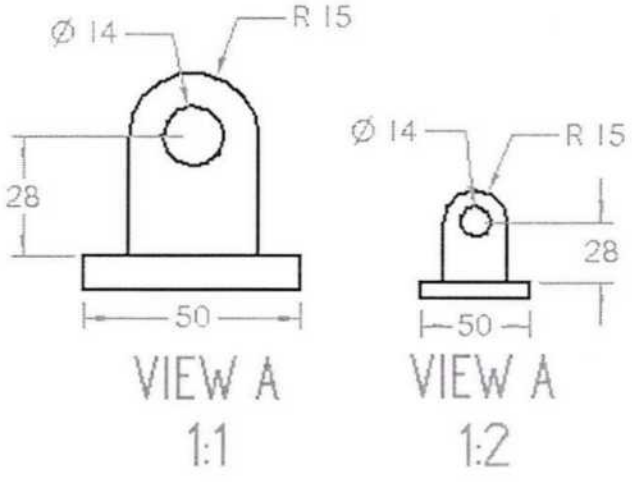
There are several commands related to creating a 2D view from existing graphics:

- 2D Model View command—Creates a 2D view that references geometry on the 2D Model sheet. Use the Drawing Area Setup command, which is available only for the 2D Model sheet, to set up a scaled work area in 2D model space.
- Convert to 2D View command—Converts a 3D part view to 2D geometry. Once you convert a part view to a 2D view, associativity to the part or assembly document cannot be retrieved.
- Draw In View command—Available for a 3D part, assembly, or sheet metal view placed on a working drawing sheet, this command opens a 2D View Edit window for you to draw in the view and to add annotations at a 1:1 scale.
- 2D View command—Superseded by the 2D Model View command, but still available through customization.

2D drawing scales

When drawing inside a 2D view placed on a working sheet, you typically work at 1:1 scale. You also can draw directly on the working sheet. If you decide later that you want to scale graphics you have drawn directly on the sheet, just move or copy them into a drawing view with the Cut, Copy, and Paste commands.

The dimension and annotation sizes on the working sheet are independent of the drawing view scale. For example, if you define the height and size of dimension text as 0.125 inches or 3.5 millimeters, these are the actual values of the dimension text on the printed drawing.



NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

Using the 2D Model sheet

You also can work on the 2D Model sheet in 2D Model space. The Drawing Area Setup command defines a scaled work area where you can create, edit, and annotate a 2D design at a scale appropriate to the size of the part or assembly, yet print at a scale appropriate to the dimensions of your drawing sheet.
The Auto-Hide layer is available at all times when working on the 2D Model sheet.

2D Model view workflow

This workflow is used to create a 2D model view in a draft document.

First, use the 2D Model Sheet command to display the full-scale 2D Model sheet. There is one 2D Model sheet per document.

Next, use the Drawing Area Setup command to define a work space on the 2D Model sheet.

Next, place or create the design geometry on the 2D Model sheet, using any combination of design file import, dragging an existing .dft file onto the sheet, and 2D line drawing tools.

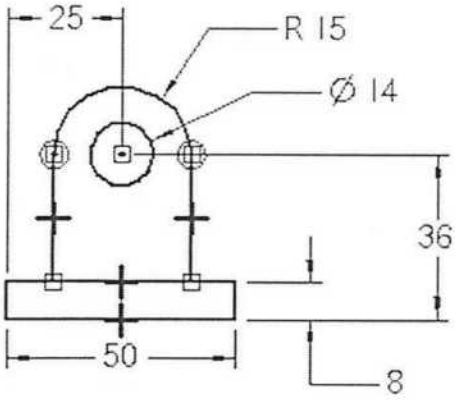
On the working drawing sheet, use the 2D Model View command to create one or more 2D model views that reference the 2D model geometry. You can customize the clipping boundary for each view created from the geometry on the 2D Model sheet, and assign a unique caption to each view.

Creating detail views from a 2D model view

You can use the Detail View command to create a dependent detail view from a 2D model view or a drawing view that has been converted to 2D geometry. You can create a detail view that displays a circular envelope or a detail view with a custom boundary.
Click here to learn more about Solid Edge detail views and the procedures for creating them.

2D views and associativity

If you set the Maintain Relationships option in the Relate group on the ribbon, the graphics you draw in a 2D view can be updated associatively, similar to the profiles you draw in the Part environment. You can place driving dimensions and apply relationships to control the size and location of the elements.



NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Hiding construction graphics, dimensions, and annotations

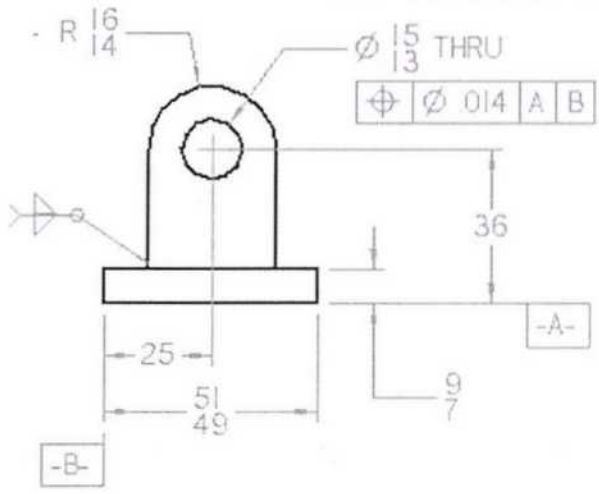
When you want to hide elements in a drawing view but you do not want to assign the hidden elements to individual layers, you can use the Auto-Hide layer. You can hide construction geometry, dimensions, and certain annotations. For example, you can place dimensions on the 2D Model sheet Auto-Hide layer to drive the size of the geometry but not display when a drawing view is placed on the working sheet.

- The Auto-Hide layer is available while you are drawing and dimensioning on the 2D Model sheet. You can use the 2D Model View command to create a drawing view of the 2D Model sheet geometry, and all elements on the Auto-Hide layer are hidden automatically.
- The Auto-Hide layer also is created automatically when you right-click a drawing view and choose the Draw In View command. When you close a Draw In View window, elements on the Auto-Hide layer are hidden automatically.

Completing the 2D view

When you finish drawing in a 2D view on the working sheet, click the Return button on the command bar to close the 2D View Edit window. After you close the 2D view window, you can add driven dimensions and annotations, such as weld symbols, feature control frames, and so forth to the drawing sheet.

If you are working in 2D model space on the 2D Model sheet, you can add and edit annotations and dimensions directly on the sheet. The graphics you add on the 2D Model sheet are visible in the 2D view on the working sheet when you click the sheet tab.

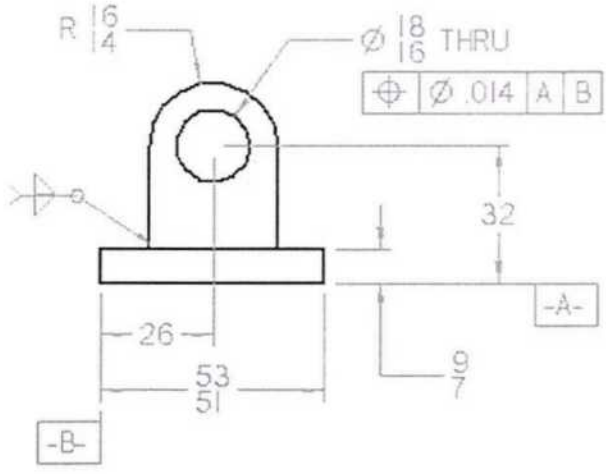


Editing 2D views

When you need to edit 3D model graphics in a 2D view, double-click the view. You can also use the Draw in View command on the shortcut menu.

If the 2D view graphics were created from the 2D Model sheet as a block, or dragged onto the sheet as a file, then you can use the Open command on the shortcut menu to open the graphics for editing. Or you can use the Unblock command to drop the block to its base elements for individual manipulation.

If you created the 2D view associatively, you can edit the driving dimensions to modify the graphics. When you close the 2D view, the driven dimensions you placed on the sheet will update.



What are you looking for?

How do I

- Convert a Part View to a 2D View
- Create a 2D model view
- Create automatic drawing views
- Define a 2D Model drawing area

Learn more about

- Drawing 2D elements
- Drawing area setup in 2D model space
- Drawing Production Overview

Look up more details

- Drawing Area Setup command
- View Wizard command
- 2D Model View command
- 2D Model Sheet command

08/02/2020

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584103

08/02/20

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology
RAICHUR.

NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

20.07.2018

2018-19/NIT/01

From
Dr.P. Rathnakumar,
Professor and Head,
Department of Mechanical Engineering
Navodaya Institute of Technology,
Raichur-584103.

To
The Principal,
Navodaya Institute of Technology,
Raichur-584103

Sir

Subject: Requisition to conduct Training Programs for Non-Teaching Faculty- Reg

We have proposed to conduct the Training programs on the topic **"Hands on Training Workshop in Microsoft Excel 2016" from 25.07.2018 to 27.07.2018 for** non-teaching staff at Navodaya Institute of Technology, Department of Mechanical Engineering, and Raichur from 10.00 AM to 05.00 PM.

Thanking You

20.07.2018
PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Yours faithfully

[Dr. Rathnakumar]

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology
RAICHUR.

NAVODAYA INSTITUTE OF TECHNOLOGY
RAICHUR

NET's
NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

Tel : +91 8532-200115, 240334, Fax : +91-8532-240054
E-Mail :hodme.nit@navodaya.edu.in

CIRCULAR

2018-19/M.S/02 23.07.2018

TO
Non-teaching staff
NIT Raichur.

Greetings from Navodaya Institute of Technology, Raichur…

The department of Mechanical Engineering is organizing a **"Hands on Training Workshop in Microsoft Excel 2016" from 25.07.2018 to 27.07.2018** for Non-Teaching Faculties to upgrade the support staff's skills.

The main objective of this workshop is to enhance the skills in **Microsoft Excel 2016** software. **Microsoft Excel 2016** is used very widely nowadays by everyone because it is very helpful and it helps in saving a lot of time. It is being used for so many years and it gets upgraded every year with new features. The most impressive thing about MS Excel is that it can be used anywhere for any kind of work. For example, it is used for billing, data management, analysis, inventory, finance, business tasks, complex calculations, etc. One can even do mathematical calculations using this and can also store important data in it in the form of charts or spreadsheets. We are pleased to inform that the department of Mechanical Engineering, Navodaya Institute of Technology is organizing a workshop for Non-Teaching Faculty during **25.07.2018 to 27.07.2018**

In this context, we look forward your support and request you to encourage participation of support staff's by circulating the attached invitation.

Venue: Department Mechanical Engineering
Navodaya Institute of Technology, Raichur.
Timing: 9.00AM to 5.00 PM

23/07/2018

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

Thanking you,
Yours sincerely,

Dr.P.Rathnakumar
HOD/MECH

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology
Raichur

NAVODAYA INSTITUTE OF TECHNOLOGY * RAICHUR *

NAVODAYA INSTITUTE OF TECHNOLOGY: RAICHUR

NON TEACHING STAFF SKILLS UP GRADUATION

DEPARTMENT: Mechanical Engineering

ACADEMIC YEAR: 2018 – 2019 DATE: 27.07.2018

Sl.No	Name of the Staff	Date	Details	Signature
(1)	Veeresh	27.07.18	Lab Instructor (Fm/CMP)	
(2)	Raghavendra	27.07.18	Lab Instructor (Lab Cas)	
(3)	Sandeep Kumar	—11—	Lab Instructor (CIM Lab)	Sandeep
(4)	Y. B. Sandeep Raj	—11—	Lab Instructor (E Lab)	
(5)	Chandra Shekhar	—11—	Foreman (M/C Shop)	

27/07/18

HEAD OF THE DEPARTMENT

Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology
RAICHUR.

27/07/18

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103

NAVODAYA INSTITUTE OF TECHNOLOGY ★ RAICHUR

Navodaya Education Trust ®
Navodaya Institute of Technology
Raichur

A Report

On

"Hands on Training Workshop in Microsoft Excel 2016"

From 25.07.2018 to 27.07.2018

For Non-Teaching Staff

Coordinators:

Prof Pradeep Ilay	*Assistant Professor*
Prof. Imran Basha,	*Assistant Professor*
Prof. Raja Shakarappa	*Assistant Professor*

Department of Mechanical Engineering

Organized by:

Department of Mechanical Engineering

Navodaya Institute of technology-Raichur

Date: 25.07.2018 to 27.07.2018

NAVODAYA INSTITUTE OF TECHNOLOGY • RAICHUR

"Hands on Training Workshop in Microsoft Excel 2016"

Navodaya Institute of Technology, Raichur Mechanical Engineering department conducted a workshop on "Microsoft Excel 2016". The workshop was based on the Basics of MS Excel to upgrade the knowledge of supporting staffs in the department. Coordinators- Prof Pradeep Ilay, Assistant Professor, Prof. Imran Basha, Assistant Professor Prof. Raja Shakarappa, Assistant professor delivered the content with practical and hands on training to participants.



NAVODAYA INSTITUTE OF TECHNOLOGY RAICHUR

Part 1: Create and format workbooks Chapter 1: Set up a workbook Chapter 2: Work with data and Excel tables Chapter 3: Perform calculations on data Chapter 4: Change workbook appearance	Prof Pradeep Ilay Assistant Professor Department of Mechanical Engineering
Part 2: Analyze and present data Chapter 5: Manage worksheet data Chapter 6: Reorder and summarize data Chapter 7: Combine data from multiple sources Chapter 8: Analyze alternative data sets Chapter 9: Create charts and graphics Chapter 10: Create dynamic worksheets by using Pivot Tab	Prof Imran Basha Assistant Professor Department of Mechanical Engineering
Part 3: Collaborate and share in Excel Chapter 11: Print worksheets and charts Chapter 12: Automate repetitive tasks by using macros Chapter 13: Work with other Microsoft Office apps Chapter 14: Collaborate with colleagues **Part 4: Perform advanced analysis** Chapter 15: Perform business intelligence analysis Chapter 16: Create forecasts and visualizations	Prof Imran Basha Assistant Professor Department of Mechanical Engineering & Prof Raja Shakarappa Assistant Professor Department of Mechanical Engineering



Shortcuts

1. Alt + down arrow

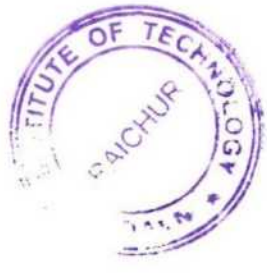
Currently, my favorite keyboard shortcut is **Alt+Down Arrow**. This allows you to create an instant drop-down list in a given cell. The list is based on data that is in the same column as the cell on which we used the keyboard shortcut. We just need to remember that the cell in which we want to use the instant drop-down list adjoins the data already entered (there was no empty data row between the data).

The Alt + down arrow combination also expands existing drop-down lists. This allows us to speed up our work if we focus on using the keyboard. It can also expand the filter menu in both standard data and pivot tables.

You can also move between elements of the chart with Alt + down arrow combination.

2. Alt-T-I and Alt-T-M-S

My Favorite shortcut for Win Excel. By pressing **Alt+T**, you can refer to the old menu structure that we used before Excel 2007. For example, to open the add-ins dialog to close or open add-ins you can use the shortcut Alt+T+I (the old Tools>Add-ins). If you want to open the Security dialog, you can use **Alt+T+M+S** (the old Tools>Macro>Security). You see that this is much easier than using File>Options>Trust Center>Trust Center Settings>Macro Settings.



3. Alt+F11 and Alt+F10

I specialize in automating Microsoft Office applications by developing VBA macros and add-ins. I write VBA code to interface Excel with PowerPoint and vice versa. For example, it's possible to programmatically create slides in PowerPoint from ranges and charts within Excel or, in the reverse direction, send content from a table in a PowerPoint slide to an Excel worksheet. You can learn more about VBA here.

My favorite shortcut is **Alt+F11** to open the VBE (Visual Basic Editor). From a non-programming perspective, Alt+F10 would be my favorite shortcut. This opens the Selection Pane which you use to reorder and rename the layers of shapes in your worksheet.

4. Ctrl+T My favorite Excel Shortcut is Ctrl+T. It is the shortcut to Convert the data into an Excel Table.

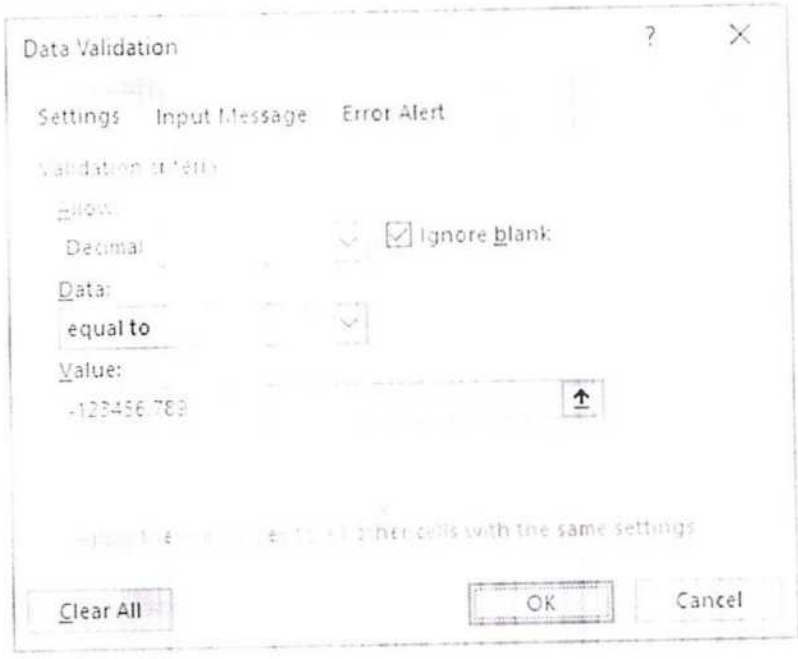
Converting data into an Excel Table is the best way to keep your data organized. As soon as a data range is converted into an Excel Table, it will acquire a set of awesome properties which makes the data easy to handle.

Some solid reasons to use Excel Tables include:

- Excel Tables are easy to Create, are Dynamic and come with Slicers
- Excel Tables can create human-readable, meaningful formulas which will be easy to understand
- Excel Tables are powered with Calculated Columns

Bonus Tip: CTRL+L is a lesser known shortcut to convert a data range into an Excel Table.





Head of Department
Department of Mechanical Engineering
Navodaya Institute of Technology
RAICHUR.

PRINCIPAL
Navodaya Institute of Technology (NIT)
RAICHUR-584 103